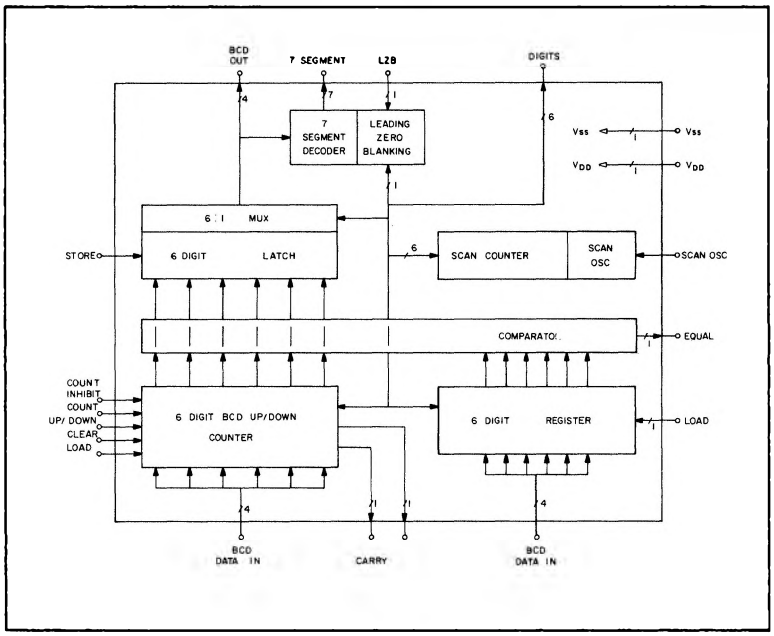
MK 50395

FEATURES:

- ☐ Single power supply 12 to 18 volts
- ☐ Synchronous up/down counting
- ☐ Presetable compare register with comparator output
- ☐ Multiplexed BCD and seven-segment outputs
- ☐ Internal or external scan (to D.C.) for free choice of output frequency

6-BIT UP/DOWN COUNTER



DESCRIPTION:

The MK 50395 is an ion-implanted P-channel MOS six-decade synchronous up/down counter/display driver with compare register, latches and output multiplexer. The counter, as well as the register, can be loaded digit-by-digit with BCD data. The counter has asychronous clear and count inhibit functions. All six counter stages can be separately mask-programmed to divide by 10 (BCD), by 6 or by 12.

Scanning is controlled by the scan oscillator input which is self-oscillating or can be overdriven by an external signal. The six-digit register is constantly compared to the state of the up/down counter; and when both the register and the counter have the same contents, an equal signal is generated. A particular state of the counter can be latched into the 6-digit latch which is then multiplexed from MSD to LSD in BCD and 7 segment format to the output. The seven-segment decoder incorporates a leading zero blanking circuit which can be turned off by an external signal.