National Semiconductor

MM54C905/MM74C905 12-Bit Successive Approximation Register

General Description

The MM54C905/MM74C905 CMOS 12-bit successive approximation register contains all the digit control and storage necessary for successive approximation analog-to digital conversion. Because of the unique capability of CMOS to switch to each supply rail without any offset voltage, it can also be used in digital systems as the control and storage element in repetitive routines.

Features

- Wide supply voltage range   3.0 V to 15 V
- Guaranteed noise margin   10. V
- High noise immunity   0.45 V_{CC} typ.
- Low power TTL compatibility   fan out of 2 driving 74L
- Provision for register extension or truncation
- Operates in START/STOP or continuous conversation mode
- Drive ladder switches directly. For 10 bits or less with 50 k/100 k R/2R ladder network

See page 2-7
for Detailed
Specifications