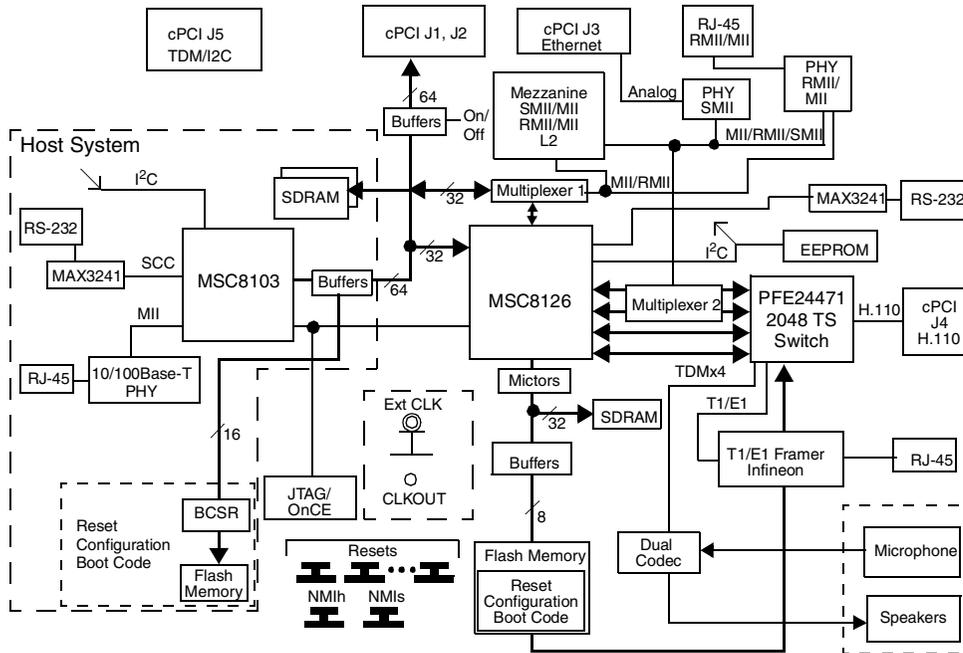


MSC8126ADS

MSC8126 Application Development System



Use this board as a reference design to start your MSC8126 projects immediately, instead of developing and assembling custom hardware. Accelerate product development and testing to shorten time to market.

Figure 1. MSC8126ADS Block Diagram

The MSC8126ADS board uses the Freescale MSC8126 processor, a highly integrated system-on-a-chip device containing four StarCore™ SC140 DSP cores along with an MSC8103 device as the host processor. Both devices use a similar system interface unit (SIU). The MSC8126ADS board serves as a platform for software and hardware development in the MSC8126 processor environment. Developers can use the on-board resources and the associated debugger to perform a variety of tasks, such as downloading and running code, setting breakpoints, displaying memory and registers, and connecting proprietary hardware via the expansion connectors. This board works seamlessly with the CodeWarrior® Development Studio for StarCore.

The MSC8126 targets high-bandwidth highly computational DSP applications and is optimized for wireless transcoding and packet telephony as well as high-bandwidth base station applications. The MSC8126 delivers enhanced performance while maintaining low power dissipation and greatly reducing system cost.

Features

Feature	Description
Board Specifications	<ul style="list-style-type: none"> • Operating temperature: 0 to 30° C (room temperature). • Storage temperature: -25 to 85° C. • Dimensions: 233.35 mm × 160.0 mm × 1.8 mm. • CS height: 17 mm. • PS height 1.9 mm. • 9-18 V external DC power supply. For a 12 V supply, maximum current 1.8 A.
MSC8126	<ul style="list-style-type: none"> • Four-core DSP with internal clock up to 500 MHz at 1.2 V. • System bus frequency up to 166 MHz using 64 or 32 data lines, addressing up to 4 GB external memory, connected to: <ul style="list-style-type: none"> — 16 MB of soldered, non-buffered on one 4-bank × 1 M × 32-bit device. — 4 MB of buffered Flash memory organized as 4 M × 8-bit for configuration/boot/program storage. • DSI frequency up to 100 MHz as a 32-bit or 64-bit slave on the MSC8103 system bus connects to: <ul style="list-style-type: none"> — 2 MB of non-buffered SDRAM organized as 32-bit (default) or 64-bit. — 16 MB of 100 MHz soldered, non-buffered SDRAM, organized on two 4-bank × 32-bit devices. — 4 MB of 16-bit buffered Flash memory. • Buffered board control and status register (BCSR) with eight byte-sized registers. • SDRAM machine controls the SDRAM on the system bus. • Four MSC8126 TDM ports connect to the Infineon TSI PEF24471 device. • Interconnection of T1/E1 timeslots between the Infineon FALC PEB2256 and the Dual CODEC MT92303. • TDM bus has optional connection to J4 cPCI connector. • RS-232 Transceiver MAX3241 supports the UART port operation of the MSC8126. • SMI1 support for MAC-to-PHY or MAC-to-MAC connections. • RMI1 and MI1 support for MAC-to-PHY connections. • Core power level adjustable via potentiometer. • Includes Viterbi coprocessor and Turbo coprocessor.
MSC8103	<ul style="list-style-type: none"> • Single-core DSP with internal clock up to 300 MHz. • System bus frequency up to 100 MHz. • 32 address lines and 64 data lines to address up to 4 GB of external memory. • Buffered connection to slave cPCI bus. • SDRAM machine controls system bus SDRAM. • 4 MB of 16-bit Flash memory for configuration/boot/program storage. • CPM ports to connect to 10/100Base-T controller and RS-232 transceiver. • 8-bit BCSR for ADS configuration.
MSC8126ADS	<ul style="list-style-type: none"> • Host debug through a single JTAG connector supports both the MSC8103 and MSC8126 processors. • MSC8103 is the MSC8126 host. The MSC8103 system bus connects to the MSC8126 DSI. • Flash memory for stand-alone applications. • Communications ports: <ul style="list-style-type: none"> — 10/100Base-T. — 155 Mbit ATM over Optical. — T1/E1 TDM interface. — H.110. — Voice codec. — RS-232. — High-density (MICTOR) logic analyzer connectors to monitor MSC8126 signals. — 6U CompactPCI form factor. • Emulates MSC8126 DSP farm by connecting to three other ADS boards. • Allows MAC-to-MAC connection between the MSC8126 and the MSC8103. • Programmable hard reset configuration for MSC8126 from Flash memory, the DSI, or forced from the BCSR. • MSC8126 boots from Flash memory on the system bus, UART, or TDM ports. • High density (MICTOR) logic analyzer connectors for MSC8126 signal measurement or external board connection. • As expansion connectors, CompactPCI® connectors carry MSC8126 signals to off-board tools to enable chip verification and evaluation. • MAX4372 current-sense amplifier for current measurement of MSC8126 PLL/IO/core power. • Debugging via an external command converter connected to the EOnCE 14-pin headers or parallel port connected to the cPCI J3 connector. • EOnCE debug chain to connect additional ADS boards via the backplane.

Feature	Description
MSC8126ADS (continued)	<ul style="list-style-type: none"> • After reset, selectable Debug Enable/Disable and Debug Request options. • Board identification and board status read via the BCSR. • Connection of external pulse generator to MSC8126 clock input via SMB-form RF connectors. • Variant board configurations available via the Dual-In-Line Package (DIP) Switch setting. • Push buttons for both the host and slave: power-on reset, soft reset, hard reset, and abort. • Voltage to the board via two DC-DC converters, one with 3.3 V at 16 A and the second with 0.9–2.0 V at 16 A. The first supplies 3.3 V I/O and the 1.6 V core voltage for the MSC8103. The second supplies the MSC8126 core voltage. • SLIC-SLAC interface enables use of 6-line communication board with Voice-over-Broadband SLIC/SLAC chip set. • Software option switch provides 8 software options via the BCSR. • LEDs indicate power supply, peripheral enables, EE1 pin status, and software signals.

Product Documentation

Table 1 lists the documentation that supports the MSC8126ADS. Documentation is available from a local Freescale distributor, a Freescale semiconductor sales office, or a Freescale Literature Distribution Center. For documentation updates, visit the Freescale DSP website.

Table 1. MSC8126ADS Documentation

Name	Description	Order Number
<i>MSC8126 Technical Data</i>	MSC8126 features list and physical, electrical, timing, and package specifications	MSC8126
<i>MSC8126 Reference Manual</i>	Detailed functional description of the MSC8126 memory and peripheral configuration, operation, and register programming	MSC8126RM
<i>StarCore™ SC140 DSP Core Reference Manual</i>	Detailed description of the SC140 family processor core and instruction set	MNSC140CORE/D
<i>MSC8103 Technical Data</i>	MSC8103 features list and physical, electrical, timing, and package specifications	MSC8103
<i>MSC8103 Reference Manual</i>	Detailed functional description of the MSC8103 memory and peripheral configuration, operation, and register programming	MSC8103RM
<i>MSC8122ADS/ MSC8126ADS Reference Manual</i>	Detailed functional description of the MSC8122ADS and MSC8126ADS boards including memory and peripheral configuration, switch settings, operation, connections, and programming	MSC8122ADSRM
<i>Application Notes</i>	Documents describing specific applications or optimized device operation including code examples	Refer to the MSC8126 and MSC8103 product pages.

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