This errata sheet describes the functional deviations known at the release date of this document.

Errata History

Lot Number / NVERS value	Trouble list
A00657 / 81h	T01, T02, T03, T04, T05, T06, T07, T08, T09, T10, T11, T12

Trouble descriptions

T01	MP3 - right to left channel crosstalk
Description	In stereo mode, right channel is in certain cases mixed with left channel. This leads to some left channel overflow.
Work around	None.

T02	MP3 - volume control inversion
Description	The MP3VOR register controls the left channel while the MP3VOL register controls the right channel.
Work around	Exchange MP3VOR & MP3VOL SFR addresses before compiling.

T03	MP3 - bad start when playing one song
Description	When skipping from one song while Playing to another, part of the previous song is played before the new song starts playing.
Work around	Use external DAC mute signal if available.

T04	MP3 - mono mode poor audio quality
Description	MP3 songs in mono mode are played with a poor quality.
Work around	None.

T05	MP3 - bass/medium/treble control
Description	bass/medium/treble control show only 5 control steps despite 31 steps available.
Work around	None.

T06	MMC - data FIFO empty flags not set after reset
Description	F1EI and F2EI flag in MMINT register are not set after reset despite FIFOs being empty.
Work around	Work around consists in writing a dummy data block to the MMC card during initialization. This empties the FIFO and sets the F1EI and F2EI flags.

T07	MP3 - Bad Write of Data in X1 Mode
Description	When writing data to MP3DAT register and CPU clock frequency << MP3 clock frequency (i.e. X1 mode), some data is lost. This leads to a playing error.
Work around	Always set X2 mode when writing MP3 data to decoder.

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T08	MP3 - Mono Mode File Plays Only on Left Channel
Description	When playing mono MP3 stream, audio is output on left channel only as it may be also output on right channel.
Work around	None.

T09	FLASH - Lock-Bit Erasure
Description	As soon as lock bits are programmed they sometimes become inerasable by full chip erase. This makes it impossible to reprogram parts later using an external parallel programmer. ISP is still available for In System Programming.
Work around	Secure code memory just before soldering part on application. ISP is still available for further programming.

T10	RST Pad - Pull-Down Resistor Value Out Of Specification
Description	R_{RST} is higher than the specification and leads to longer reset times.
Work around	Decrease external capacitor value to decrease reset time.

T11	SPI Controller - Bad Transfer in Slave Mode with CPHA= 0
Description	SPI controller used in slave mode with CPHA= 0, leads to erratic data reception and transmission.
Work around	Use slave mode only with CPHA= 1.

T12	P4.3 Pad - Product Hangs When P4.2 Drived at Reset
Description	If P4.3 pad is set to low level during reset, the product never exits the reset state. There is no code execution.
Work around	Never use P4.3 as input or be sure it is never drived to low level during reset.

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