

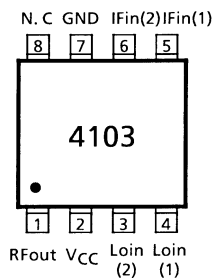
TA4103F

1.9 GHz Band Up Converter Application

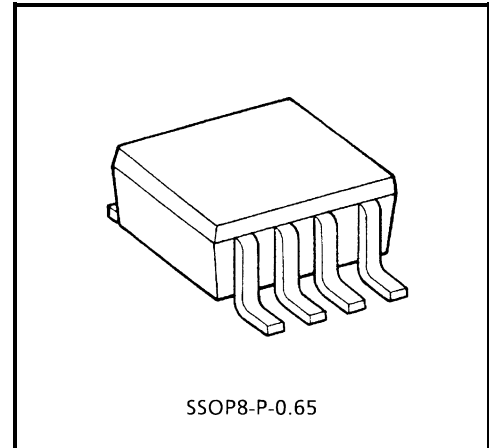
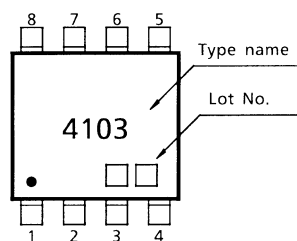
Features

- Built in Lo and IF buffer amplifiers.
- Double balanced MIX circuit
- High conversion gain: $G_C = 3\text{dB}$ (typ.)
- Recommended operating voltage: $V_{CC} = 2.7\sim 3.3\text{ V}$

Pin Assignment (top view)



Marking



Weight: 0.02 g (typ.)

Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{CC}	5	V
Total power dissipation	P_D (Note1)	300	mW
Operating temperature	T_{opr}	$-40\sim 85$	$^\circ\text{C}$
Storage temperature range	T_{stg}	$-55\sim 125$	$^\circ\text{C}$

Note 1: When mounted on the glass epoxy board of $2.5\text{ cm}^2 \times 1.6\text{ t}$

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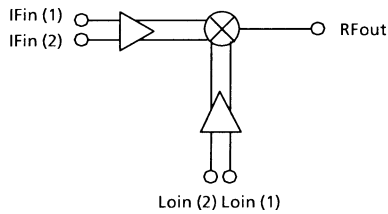
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Electrical Characteristics ($V_{CC} = 3\text{ V}$, $T_a = 25^\circ\text{C}$, $Z_g = Z_l = 50\ \Omega$)

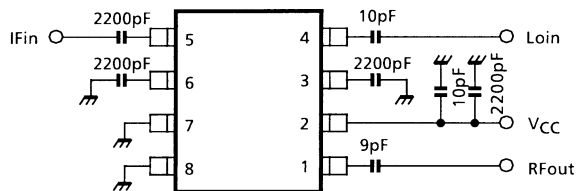
Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
RF frequency range	f_{RFout}	—	—	1895	—	1918	MHz
IF frequency range	f_{IFin}			220	—	250	MHz
Lo frequency range	f_{LoIn}			1645	—	1698	MHz
Circuit current	I_{CC}	—	non carrier	23	26.5	33	mA
Conversion gain	G_C	1	$P_{LoIn} = -20\text{dBmW}$ $P_{RFout} = -18\text{dBmW}$ $P_{IFin} = \text{Adjusted}$ $\Delta f = 600\text{ kHz (Note2)}$	1	3	—	dB
Output power at 1dB gain compression	P_{o1dB}			-19	-17	—	dB
Lo-RF leakage power	P_{RFLo}			—	—	-20	dBmW
Lo-IF leakage power	P_{IFLo}			—	—	-33	dBmW
Adjacent channel leakage power ratio	P_{adj}			—	-63	—	dB

Note 2: Input signal is modulated to $\pi/4$ QPSK ($\alpha = 0.5$). Bit rate is 384 kbps.

Block Diagram



Test Circuit 1



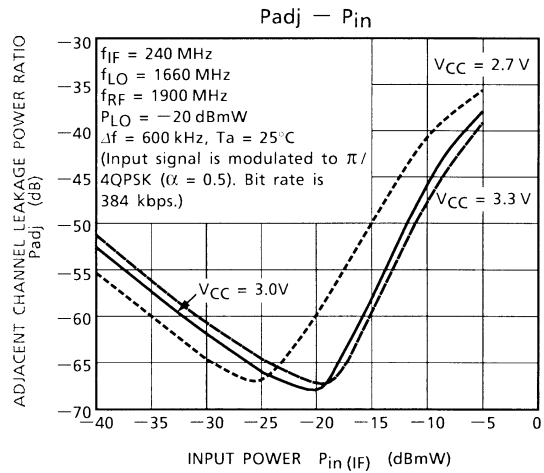
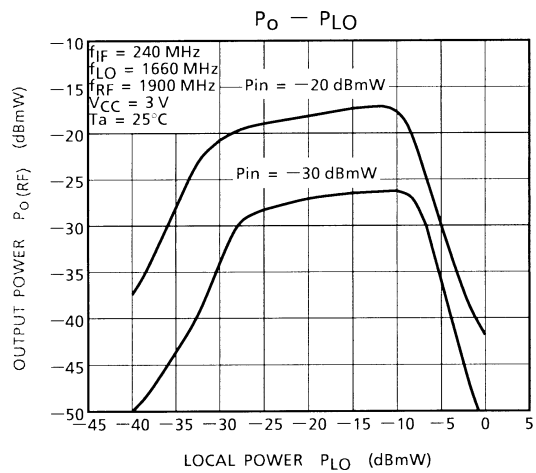
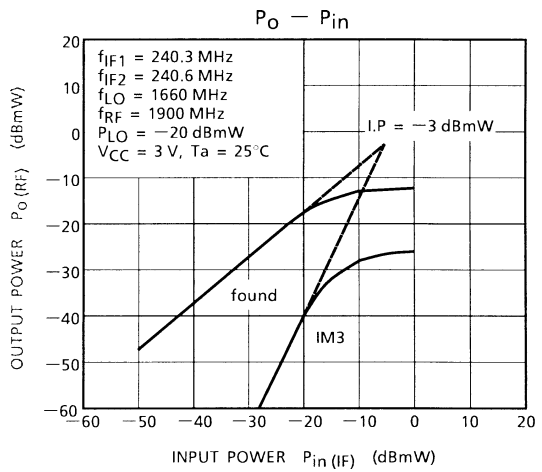
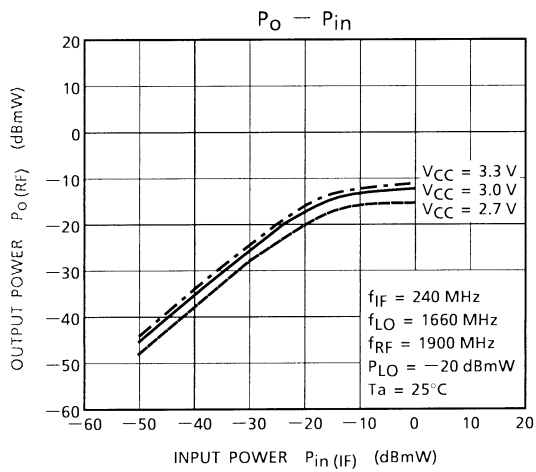
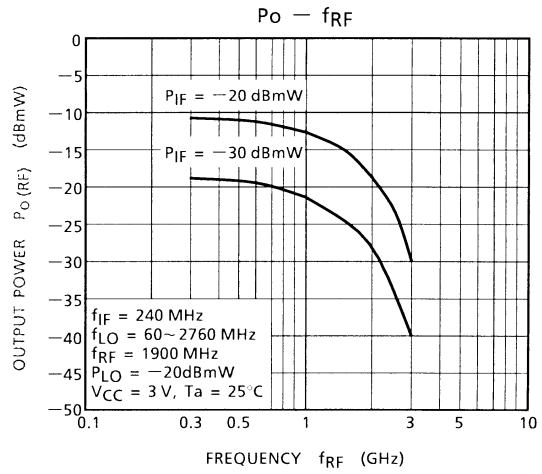
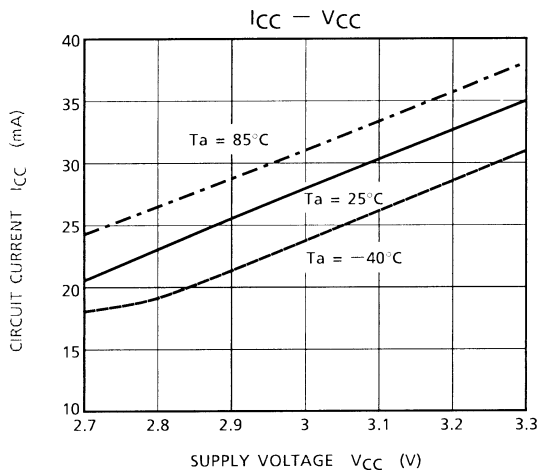
Notice

The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

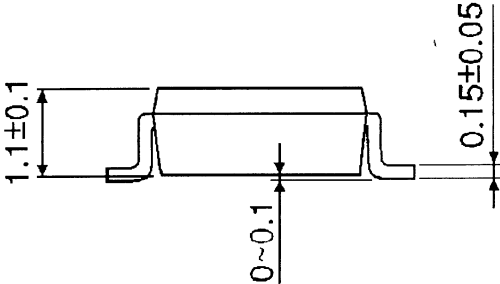
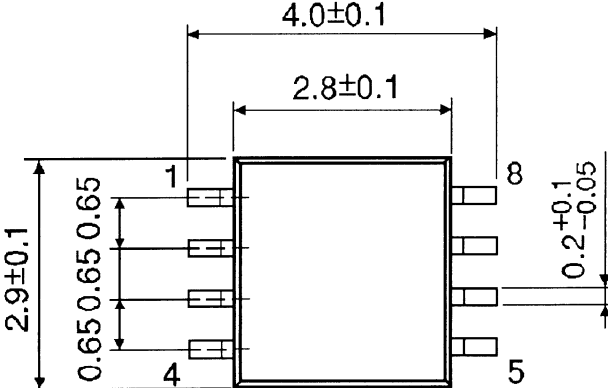
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Package Dimensions

SSOP8-P-0.65

Unit : mm



Weight : 0.02 g (Typ.)