TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

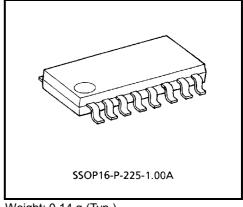
TA7733F

FUNCTIONAL BRIDGE DRIVER

The TA7733 is a low-voltage bridge driver IC offering four modes: Forward rotation, reverse rotation, stop, break.

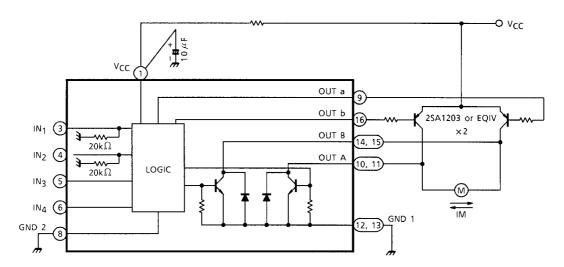
FEATURES

- Wide Operating Supply Voltage Range.
 V_{CC} (opr.) = 1.8~15 V
- Capsealded in Flat Package 16 pin.
- Forward and Reverse Rotation, Short Brake Modes are Available by Means of Rotation Control Signals.
- High Efficiency is Obtained.
- Can be Used as Interface Driver.



Weight: 0.14 g (Typ.)

BLOCK DIAGRAM



PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION				
1	V _{CC}	Power supply voltage terminal				
2	NC	No Connection				
3	IN ₁	Signal input terminal				
4	IN ₂	Signal input terminal	Truth table 1			
5	IN ₃	Signal input terminal	- Trutti table i			
6	IN ₄	Signal input terminal				
7	NC	No connection				
8	GND 2	Logic GND				
9	OUT a	Output A upper transistor drive terminal				
10	OUT A	Output A				
11	OUT A	Output A				
12	GND 1	Payer CND tarminal				
13	GND 1	Power GND terminal				
14	OUT B	Control B				
15	OUT B	Output B				
16	OUT b	Output B upper transistor drive terminal				

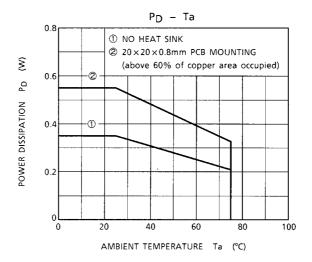
FUNCTION

(1) Bridge driver (Truth table 1)

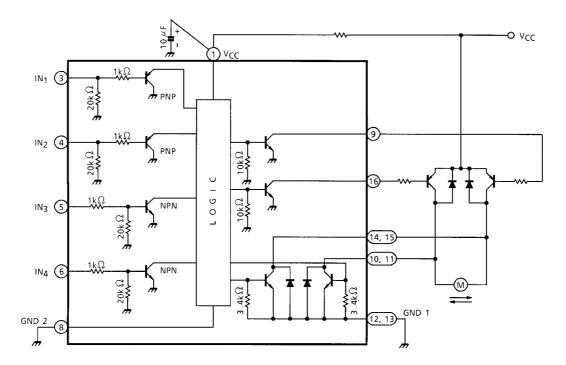
CONTROL	INPUT MODE				OUTPUT				OPERATING	NOTE	
	IN ₁	IN ₂	IN ₃	IN ₄	OUT A	OUT B	OUT a	OUT b	MODE	NOTE	
2 Input Control	Н	L	Н	Н	ON (-500 mA)	_	ON (-25 mA)	_	Forward Rotation		
	L	Н	Н	Н	_	ON (-500 mA)	_	ON (-25 mA)	Reverse Rotation		
	Н	Н	Н	Н	ON (-500 mA)	ON (-500 mA)	_	_	Brake	_	
	L	L	Н	Н	_	_	_	_	Stop		
1 Input Control	Н	L	L	Н	ON (-500 mA)	_	ON (-25 mA)	_	A ON		
	L	L	L	Н	_	ON (-500 mA)	_	ON (-25 mA)	B ON		
	H/L	Н	L	Н	ON (-500 mA)	ON (-500 mA)	_	_	AB ON	HIGH ≥ 1.2 V	
	_	_	_	L	_	_	_	_	INHIBIT	LOW ≤ 0.6 V	

(2) Interface driver application

If IN_3 and IN_4 connect to "HIGH" Out A and Out B can be used as a interface driver output for each inputs. (Connect Out (a) and Out (b) to GND)



INPUT-OUTPUT CIRCUIT



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Peak Supply Voltage	V _{CC}	18	V	
Output Current	I _O (AVE.)	0.5	Α	
Power Dissipation	P _D	350 (Note 1)	mW	
Tower Dissipation	ט י	550 (Note 2)	11100	
Operating Temperature	T _{opr}	-30~75	°C	
Storage Temperature	T _{stg}	-55~150	°C	

Note 1: No heat sink

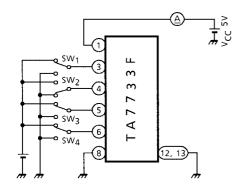
Note 2: This rating is obtained by mounting on $20 \times 20 \times 0.8$ mm PCB that occupied above 60% of copper area.

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, Ta = 25°C, V_{CC} = 5 V)

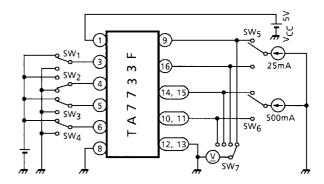
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Supply Current		I _{CC1}	1	Output open, CW / CCW mode	_	13	_		
		urrent I _{CC2}		Output open, Stop mode	_	7.2	_	mA	
				Output open, Brake mode	_	19.2	_		
		I _{CC4}	1	Inhibit (INPUT4 = "L")	-	2.4	_		
OUTPUT Saturation Voltage		V _{sat1}	2	I _{O1} = 500 mA, Lower side (Output A, B)	B) — 0.4 —		V		
		V _{sat2}	2	I _{O2} = 25 mA, Upper side (Output a, b)	-	0.5	_	V	
Output TR Leakage Current		ΙL	3	V _C = 15 V		_	50	μΑ	
Input Voltage	"H" Level	V _{IN 1, 2 (H)}	_	_	1.2	_	V _C C	V	
	"L" Level	V _{IN 1, 2 (L)}	_	_	1	_	0.3	V	
Input Current	"L" Level	I _{IN1, 2}	4	Input "L" (Source current)	-	_	20	μΑ	
Input Voltage	"H" Level	V _{IN 3, 4 (H)}	_	_	1.0	_	V _C C	V	
	"L" Level	V _{IN 3, 4 (L)}	_	_	_	_	0.3	V	
Input Current	"H" Level	I _{IN3, 4}	4	Input "H" (Sink current) V _{IN} = 1 V	_	_	30	μΑ	
Diode Forward Voltage		V _F	5	I _F = 0.5 A, V _{CC} = 0 V	_	1	_	V	

TEST CIRCUIT

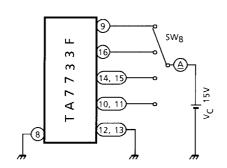
1. I_{CC1, 2, 3, 4}



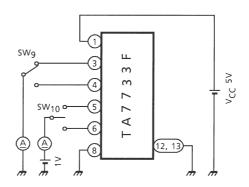
2. V_{sat1, 2}



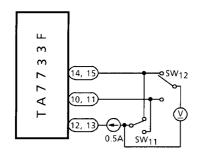
3. IL



4. IIN



5. VF



NOTES WHEN USING THE TA7733F

The functionable bridge driver, TA7733F, is an IC specifically developed to control rotation switching in brush motors. This IC has been carefully designed and strengthened to withstand counter-electromotive force or start up rush current, which are problems in driving brush motors.

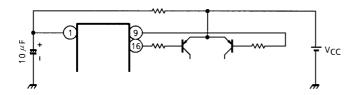
However, as with other power ICs, application circuits must be designed not to apply surge voltage or excess current that exceeds the standard values.

In addition, when designing PCBs, make sure the wiring pattern does not cause oscillation, which can result in equipment malfunction or destruction of the IC.

The following are notes on TA7733F use. These should be reflected at the design stage.

(1) Power supply voltage

To avoid the motor current affecting the TA7733F control-side power supply, we recommend you use two power supplies: an external transistor power supply, and a TA7733F control-side power supply. However, when using a single power supply, connect as in the diagram below.



(2) Maximum voltage and current

The maximum supply voltage (pin (1)) for TA7733F is 18 V. Operating supply voltage is in the range of 1.8~15 V. No voltage exceeding this range should be applied to pin (1).

The maximum current is 0.5 A (AVE.) or 1.5 A (PEAK). The circuit should be designed so that rush current at startup does not exceed peak current, and average current at steady operation does not exceed 0.5 A.

(3) External Diodes

As the block diagram shows, TA7733F has internal diodes.

The lower two diodes, which are the IC's internal parasitic diodes, have a relatively large capacitance. However, when a motor with a large reactance such as a core motor is driven, the upper two diodes may be damaged by the motor's counter-electromotive force. In such a case, connect external diodes in parallel. The lower diodes should not be subjected to high current. Therefore, in such cases as brake operation, external diodes should be connected.

(4) PCB Design

The following points concern the TA7733F pattern design around the power supply line (pin (1)) and the pattern design of the GND (pin (8), pin (12) / (13)).

- a) Ensure that the bypass capacitor between pin (1), and GND does not share impedance with other lines.
- b) The GND line should not be shared by other circuits.
- c) The capacitance of the bypass capacitor should be as large as possible.

(5) Oscillation Remedies

To prevent noise from sparks when using brush motors, a capacitor may be connected between both pins.

When using TA7733F, the capacitor is connected between output (pin (10) / (11), pin (14) / (15). This may cause oscillation.

Therefore, avoid connecting the capacitor where possible. If connection is necessary to overcome noise, connect resistors in series as shown in the technical data.

The values for the capacitor and resistors must be determined according to the motor.

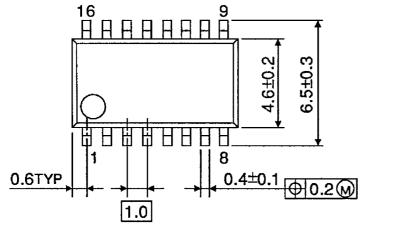
Note: Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

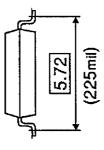
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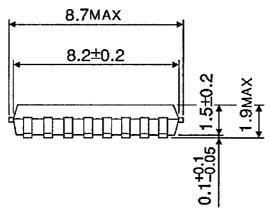
PACKAGE DIMENSIONS

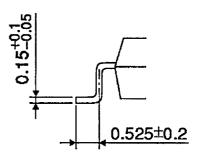
SSOP16-P-225-1.00A

Unit: mm









Weight: 0.14 g (Typ.)

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