<u>TOSHIBA</u>

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHI

TA8045BF

5V Voltage Regulator With Watchdog Timer

This IC is designed especially for microcomputer system in automobiles and is capable of providing output voltage $5\pm0.15V$ without any adjustment.

This IC incorporates the system reset function, that is, low voltage reset and watchdog timer which can self-diagnose the microcomputer system so that program runaway can be prevented.

This IC also has an over-voltage protection, current limiter and thermal protection to protect the IC from destruction caused of overstress and load accident.

In addition, this IC has a low standby current function, so it can be connected directly to an automotive battery.





FEATURES

- Operating DC Supply Voltage Range : 6~30V
- Transient Supply Voltage : 60V (Max.)
- Extremely low Quiescient Current in Standby Mode : $400 \,\mu$ A (Max.)
- High Accurate Output Voltage : 5±0.15V (3%)
- Output Current Capability : 50mA (Max.)
 - Dropout Voltage between VIN and Vout : 1V (Iout = 50mA)
- On-Chip System reset function
 - : Watchdog Timer, power On Reset Timer, Low Voltage Reset.
- On-Chip Protective function
 - : Current limiter (100mA Typ.), Over-Voltage Protection (28V Typ.), Thermal Protection (150°C Typ.).
- Wide Operating temperature : -40~105°C (125°C)
- Compact Package : Plastic SSOP 16 Pin Package.

BLOCK DIAGRAM



PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION				
2	GND	Grounded.				
4	RESET	Watchdog timer reset pin. – Generates a reset signal which is determined by the CR combination of the TC pin. – Inte <u>rmittently</u> generates reset pulses if no clock is supplied to the CK pin. The RESET signal is the output from the collector of an NPN transistor with a pull-up resistor.				
5	СК	Clock input pin for the watchdog timer. If it is used for a Power-on reset timer only, it is pulled up to RESET pin.				
7	тс	Pin for setting a time for the reset timer and watchdog timer. It connects to a resistor RT which leads to V_{CC} and a capacitor C_T which is grounded.				
9	V _{OUT}	Output pin for 5V constant-voltage power supply. It has a current capacity of up to 50mA (Max.). This pin is also a power supply pin for the reset timer.				
12	STBY	Standby mode setup pin. When the signal is low, the system is in standby mode in which the reset timer is off and the power current is limited to 0.4mA or less. When the signal is high, the system is in active mode in which the power supply and reset timer are active.				
15	V _{IN}	Power supply input pin.				
1, 3, 6, 8, 10, 11, 13, 14, 16	NC	Not connected. (Electrically, this pin is completely open.)				

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TIMING CHART



Note: See Electrical Characteristics for symbols in the timing chart.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	PIN	RATING	UNIT	
Input Voltage	V _{IN1}	Vini	30 (DC)	V	
input voltage	V _{IN2}	¥ IN	60 (<1s)		
Input Voltage	V _{IN3}	СК	-5~5	V	
input voitage	V _{IN4}	RESET, STBY, TC	-0.3~5		
Input Current	I _{IN}	CK, STBY, TC	-5~5	mA	
Output Current	lour	V _{OUT}	50	mA	
	1001	RESET	2		
Output Voltage	V _{OUT}	V _{OUT} , RESET	16	V	
Operation Temperature	T _{OPR}		-40~105	°C	
Junction Temperature	Tj		-40~150	°C	
Storage Temperature	T _{stg}		-40~150	°C	
Lead Temperature Time	T _{sol}		260 (10s)	°C	

THERMAL DATA

CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Dissipation	PD	0.6	W
Thermal Resistance Junction-ambient	Rθ _{J-A}	160 *	°C / W
Thermal Shutdown Junction-Temperature	T _{JSD}	150	°C

*: When mounted on Board (50×50×1.6mm 30% Cu)

AC CHARACTERISTICS

CHARACTERISTIC	SYMBOL	PIN	TEST CONDITION	MIN	TYP.	MAX	UNIT
Watchdog Timer	TWD	RESET		0.9× C _T ·R _T	1.1× C _T ·R _T	1.3× C _T ·R _T	ms
	T _{RST (1)}			1.3× C _T ·R _T	1.6× C _T ·R _T	1.9× C _T ·R _T	ms
	T _{RST (2)}			0.3× C _T	0.7 ×C _T	1.5× C _T	ms
Clock Pulse Width	TW	СК		3	—	—	μs
Transfer Delay Time	T _{DHL}	RESET	C1 = 0µF RESET "High" to "Low"	_	3	10	μs

Note: The unit for CT is μF , the unit for RT is $k\Omega$.

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ELECTRICAL CHARACTERISTICS

$\begin{pmatrix} V_{IN} = 6 \sim 18V, Ta = -40 \sim 105^{\circ}C, I_{LOAD} = 10mA \\ unless otherwise specified \end{pmatrix}$

CHARACTERISTIC	SYMBOL	PIN	TEST CONDITION	MIN	TYP	ΜΑΧ	UNIT	
	OTMBOL						UNIT	
Output Voltage	V _{REG}	V _{OUT}	I _{LOAD} = 0~35mA Ta = -40~85°C	4.85	5	5.15	V	
Line Regulation	V _{LINE}	V _{OUT}	V _{IN} = 6~25V	—	0.1	0.5	%	
Load Regulation	V _{LOAD}	V _{OUT}	Ta = 25°C, I _{LOAD} = 1~50mA	_	0.1	0.5	%	
Temperature Coefficient	$\Delta V_{REG} / \Delta_{OUT}$	V _{OUT}		_	0.01		% / °C	
Dropout Voltage	V _{DROP}	V _{OUT}	I _{LOAD} = 50mA Ta = 25°C	_	0.85	1.0	V	
Current Limiter Detection	ILIMIT	V _{OUT}		—	100		mA	
Reset Detect Voltage	V _{TH}	V _{OUT}		V _{REG} × 89%	V _{REG} × 92%	V _{REG} × 95%	V	
Output Voltage	V _{OL}	RESET	I _{OL} = 1mA		0.3	0.5	V	
Output Leakage Current	I _{LEAK}	RESET	V _{OUT} = 5V	-5	0	5	μA	
Input Current	I _{IN}	TC	V _{IN} = 0~3.5V	-3	0	3	μA	
Threshold Voltage	V _{TH-H}	тс	RESET "High" to "Low"	—	V _{REG} × 80%	-	V	
	V _{TH-L}		RESET "Low" to "High"	—	V _{REG} × 40%	-		
Input Current	I _{IN}	СК	V _{IN} = 5V	—	0.3	0.7	mA	
Input Datast Voltage	VIH	СК		2	—		V	
input Detect voltage	VIL	STBY		_	_	0.5	v	
Input Current	I _{IN}	STBY	V _{IN} = 5V	_	0.3	0.7	mA	
Standby Current	I _{ST}	V _{IN}	$\overline{\text{STBY}}$ = "L", V _{IN} = 14V	—	0.18	0.4	mA	
Current Consumption	I _{CC}	V _{IN}	$\overline{\text{STBY}}$ = "H", V _{IN} = 14V	—	1.2	2.4	mA	
Over-Voltage Detection	V _{SD}	V _{IN}		25	28	31	V	

EXAMPLE OF APPLICATION CIRCUIT



*: Caution for Wiring

C1 and C2 are for absorbing disturbance, noise, etc.
 Connect them as close to the IC as possible.
 Use capacitor C1 and C2 having less temperature coefficient (tantalum capacitor, etc).

RECOMMENDED CONDITIONS

PART NAME	MIN	MAX	UNIT
CT	0.01	100	μF
R _T	5	100	kΩ

CK INPUT APPLICATION CIRCUIT

Capacitor Coupling



Timing Chart

The capacitor coupling allows reset pulses to be supplied intermittently from the $\overline{\text{RESET}}$ pin whether the input level (IN) is high or low.

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PACKAGE DIMENSIONS







(225mil)

5.72

Weight: 0.14g (Typ.)

RESTRICTIONS ON PRODUCT USE

Handbook" etc..

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