TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# TA8400P

### DUAL BRIDGE DRIVER

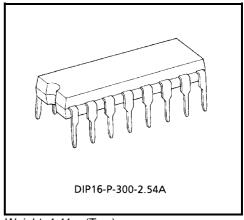
The TA8400P is Dual Bridge Driver designed especially for VCR cassette and tape loading motor drives.

### FEATURES

- 4 modes available (CW / CCW / STOP / BRAKE)
- Output current up to 0.4 A (AVE.) and 1.0 A (PEAK)
- Wide range of operating voltage: V<sub>CC</sub> (opr.) = 4.5~18 V

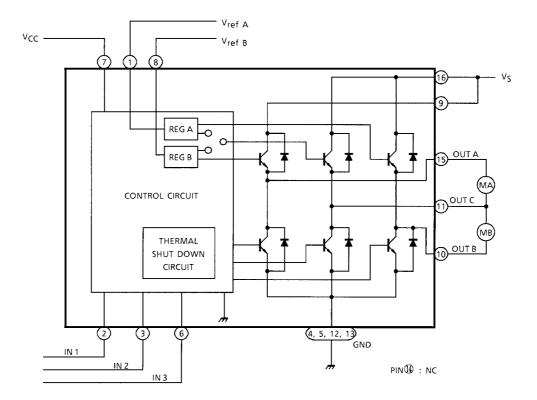
VS (opr.) = 0~22 V Vref (opr.) = 0~22 V

- Built-in thermal shutdown, over current protector and punch-through current restriction circuit.
- Hysteresis for all inputs.



Weight: 1.11 g (Typ.)

### **BLOCK DIAGRAM**



#### **PIN FUNCTION**

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION		
1	V <sub>ref A</sub>	Supply voltage terminal for control circuit		
2	IN 1	Logic input terminal		
3	IN 2	Logic input terminal		
4	GND	GND terminal		
5	GND	GND terminal		
6	IN 3	Logic input terminal		
7	V <sub>CC</sub>	Supply voltage terminal for logic		
8	V <sub>ref B</sub>	Supply voltage terminal for control circuit		
9	VS	Supply voltage terminal for motor driver		
10	OUT B	Output terminal		
11	OUT C	Output terminal		
12	GND	GND terminal		
13	GND	GND terminal		
14	NC	Non connection		
15	OUT A	Output terminal		
16	VS	Supply voltage terminal for motor driver		

### FUNCTION

INPUT			OUTPUT			MODE		
IN 1	IN 2	IN 3	OUT C	OUT A	OUT B	MA	MB	
0	0	1 / 0	8	8	8	STOP	STOP	
1	0	0	Н	L	8	CW / CCW	STOP	
1	0	1	L	Н	8	CCW / CW	STOP	
0	1	0	Н	80	L	STOP	CW / CCW	
0	1	1	L	80	Н	STOP	CCW / CW	
1	1	1 / 0	L	L	L	BRAKE	BRAKE	

∞: High impedance

Note: Inputs are all low active type.

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Supply Voltage		V <sub>CC</sub>	25	V	
Motor Drive Voltage		VS	25	V	
Reference Voltage		V <sub>ref</sub>	25	V	
	PEAK	I <sub>O (PEAK)</sub>	1.0 (Note 1)	A	
Output Current	AVE.	I <sub>O (AVE.)</sub>	0.4		
Power Dissipation		PD	1.4 (Note 2)	W	
Operating Temperature		T <sub>opr</sub>	-30~75	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C	

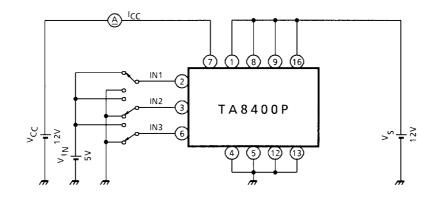
Note 1: Duty 1 / 10, 100 ms Note 2: No heat sink

## ELECTRICAL CHARACTERISTICS (Unless otherwise specified, Ta = $25^{\circ}$ C , V<sub>CC</sub> = 12 V, V<sub>S</sub> = 12 V)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Supply Current		I <sub>CC1</sub>	1	Output open, CW / CCW mode	_	25	38		
		I <sub>CC2</sub>	1	Output open, Brake mode	-	25	38	mA	
		I <sub>CC3</sub>	1	Output open, Stop mode	_	10	20		
Input Voltage	1 (High)	V <sub>IN 1</sub>	2	T <sub>j</sub> = 25°C, pin (2), (3), (6)	3.5	-	5.5	v	
	2 (Low)	V <sub>IN 2</sub>	2	T <sub>j</sub> = 25°C, pin (2), (3), (6)	GND	_	1.2		
Input Current		I <sub>IN</sub>	2	V <sub>IN</sub> = GND, source mode	6	12	60	μA	
Input Hysteresis Voltage		$\Delta V_T$	2		_	0.7	_	V	
	Upper	V <sub>SAT U-1</sub>	3	V <sub>ref</sub> = V <sub>S</sub> , I <sub>O</sub> = 0.4 A	_	1.0	1.5		
	Lower	V <sub>SAT L-1</sub>	3	$V_{ref} = V_S, I_O = 0.4 \text{ A}$	_	0.3	_		
Saturation Voltage	Upper	V <sub>SAT U-2</sub>	3	V <sub>ref</sub> = V <sub>S</sub> , I <sub>O</sub> = 1.0 A, ON LOAD: 20 ms	_	2.0	2.5	V	
	Lower	V <sub>SATL-2</sub>	3	V <sub>ref</sub> = V <sub>S</sub> , I <sub>O</sub> = 1.0 A, ON LOAD: 20 ms	_	0.8	1.3		
Output Voltage		VSAT U-1'	3	V <sub>ref</sub> = 8 V, I <sub>O</sub> = 0.4 A	8.2	8.8	9.3		
		V <sub>SAT U-2'</sub>	3	V <sub>ref</sub> = 8 V, I <sub>O</sub> = 1.0 A ON LOAD: 20 ms	8.1	8.6	9.2	V	
Output Transistor	Upper	ILU	—	V <sub>S</sub> = 25 V	_	—	200		
Leakage Current	Lower	۱ <sub>LL</sub>	—	V <sub>S</sub> = 25 V	—	—	200	μA	
Diode Forward Voltage	Upper	V <sub>FU</sub>	4	I <sub>F</sub> = 1.0 A	—	3.6	—	v	
	Lower	V <sub>FL</sub>	4	I <sub>F</sub> = 1.0 A	—	0.9	—		
Reference Current		I <sub>ref</sub>	2	V <sub>ref</sub> = 8 V, source mode	—	0.45	0.7	mA	
Thermal Shut Down Operating Temperature		T <sub>SD</sub>	—	Тј	110	130	150	°C	

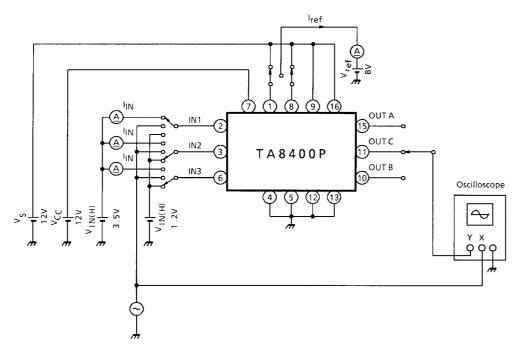
### **TEST CIRCUIT 1**

I<sub>CC1</sub>, 2, 3



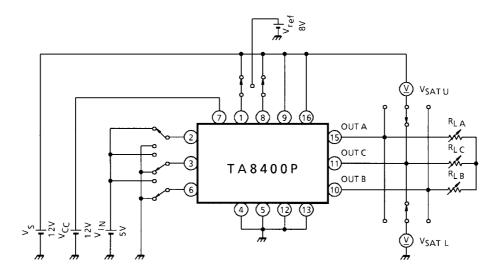
### **TEST CIRCUIT 2**

 $V_{IN1,\,2,}\,I_{IN},\,\Delta V_{T},\,I_{ref}$ 



### **TEST CIRCUIT 3**

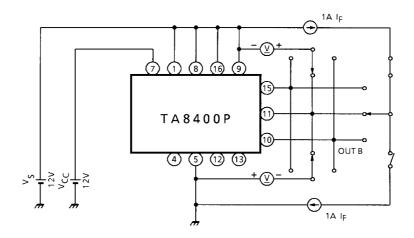
VSAT U-1, L-1, U-2, L-2, U-1', U-2'

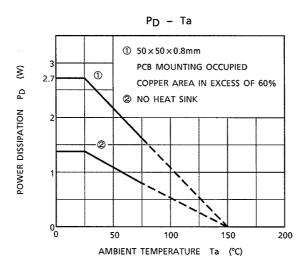


Note: Calibrate  $I_{OUT}$  to 0.4 / 1.0A by R<sub>LA</sub>, R<sub>LB</sub> and R<sub>LC</sub>.

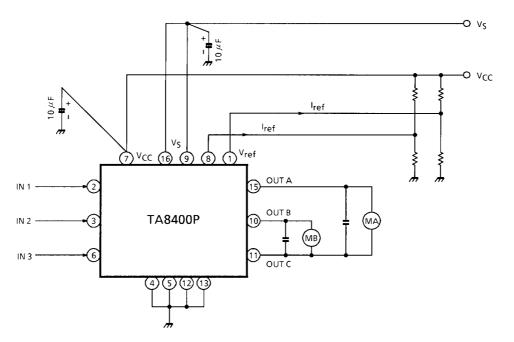
### **TEST CIRCUIT 4**

V<sub>FU,L</sub>





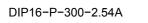
### **APPLICATION CIRCUIT**



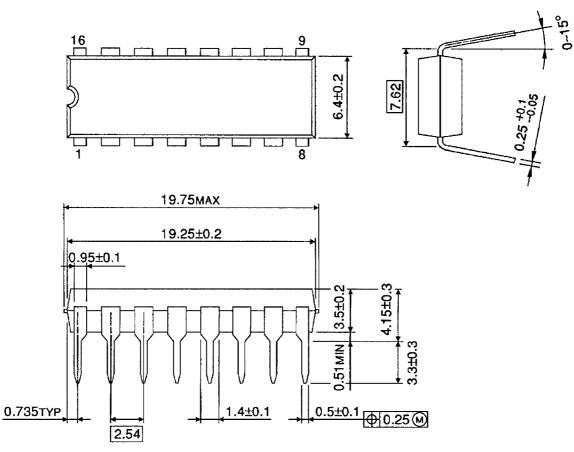
Pin (16) is required to connect to pin (9).

Note: Utmost care is necessary in the design of the output line, V<sub>S</sub> and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

### PACKAGE DIMENSIONS



Unit: mm



Weight: 1.11 g (Typ.)

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000707EBA

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