TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

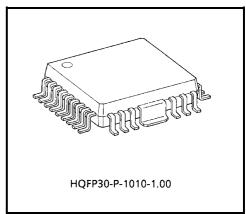
TA8463F

SINGLE CHIP 3 PHASE MOTOR DRIVER FOR FDD SPINDLE MOTOR.

The TA8463F is Single Chip Motor Driver IC for FDD Spindle Motor.

FEATURES

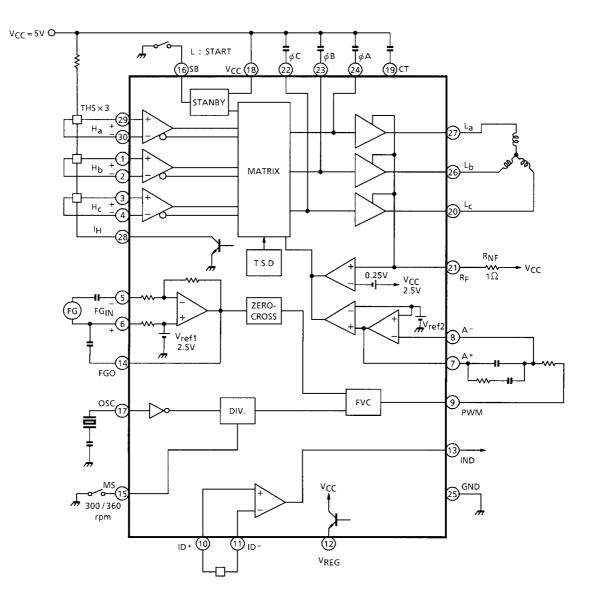
- 1 Chip motor driver with 3 phase semi-linear driving.
- Adjustment free with digital servo system.
- 300, 360 rpm are obtained.
- Built-in index pulse output current.
- Operating supply voltage range $: V_{CC} = 4.2 \sim 7V$
- Output current : IO(MAX) = 0.5 A (AVE.)
- Built-in thermal shutdown circuit.
- Built-in over current protection circuit.
- Built-in stand-by circuit.



Weight: 0.61 g (Typ.)



BLOCK DIAGRAM



PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION	EQUIVALENT CIRCUIT
29 30 1 2 3 4	H_a^+ H_a^- H_b^+ H_b^- H_c^+ H_c^-	 Hall Amp. + / - Input Terminal. The Hall Input Range is ; V_H = 50~300 [mV_{p-p}] CMR = 1.3~(V_{CC}-0.9) [V] 	
28	ΙΗ	 Hall Bias Negative Side Connecting Terminal. Open collector output. 	
5	FG _{IN} ⁻	• FG Amp. Negative Input Terminal.	
6 14	FG _{IN} ⁺ FGO	FG Amp. Positive Input Terminal.	
14	FGU	 FG Amp. Output Terminal. High Sensitivity of FG Amp. ; V_{HFG} = 2.5 mV 	
7	A^+	• Error Amp. Output Terminal.	\mathbf{t}
8	A	 Error Amp. Input Terminal. External Ports Value (C.R) is determined by matching between Motor and IC. 	
9	PWM	 F / V Converter Output Terminal Reference : No.7 and No.8 	

<u>TOSHIBA</u>

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION	EQUIVALENT CIRCUIT
10 11	ID⁺ ID⁻	 Index Positive Input Terminal. Index Negative Input Terminal. 	
13	IND	 Index Amp. Output Terminal. Reference : No.10 and No.11 	
15	MS	 Mode Select Terminal. 300 rpm: L 360 rpm: H 	
16	SB	 Stand-by Terminal. SB: H ST: L 	
17	OSC	 Oscillation Terminal. The correct value of the exterior condenser constant differs depending on the type of ceramic oscillator used. To determine the constant, refer to the oscillator manufacturer. External CK Pulse is used, connect Resistor (min. 20 kΩ) in series. 	

<u>TOSHIBA</u>

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION	EQUIVALENT CIRCUIT
18	V _{CC}	Supply Voltage Input Terminal.	
19	СТ	 Phase Compensation Terminal. Connect Capacitor between pin (19) and GND. 	
20	L _c	Output Terminals.	
26	L _b		<u>⊢</u> •. ⊁–
27	La		
21	RF	 Power Supply Voltage Input Terminal. By connecting resistors between V_{cc} terminal and pin (21), Current Limiter is available. I_{LIM} = V_{ISD}/R_{INF} V_{ISD} = 0.14V I_{O (MAX.)} = 0.5A 	
22	φC	 Capacitor Connect Terminal for prevention of oscillation. 	
23	φΒ	prevention of oscillation.	+ ^ ^
24	φΑ		
25	GND	• GND	
Fin			

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	8	V
Output Current	Ι _Ο	0.6	А
Power Dissipation	PD	1.0	w
	U '	1.5 (Note)	vv
Operating Temperature	T _{opr}	-30~75	°C
Storage Temperature	T _{stg}	-55~150	°C

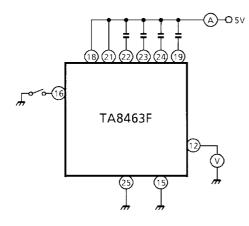
Note: With Heat-Sink (60 × 60 × 1.6 mm Cu 50%)

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{CC} = 5 V, Ta = 25°C)

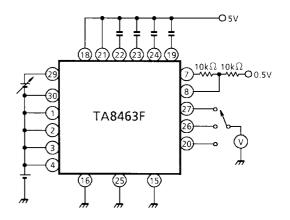
CHARACTERISTIC			SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current		I _{CC1}	1	SB = OPEN, output open	-	125	200	μA	
			I _{CC2}	1	SB = GND, output open	_	23.9	36	mA
	Gain		G _{HO}	_	Output connection state	_	31	_	dB
Hall Amp.	Input Sensitivity		V _H	2		50	_	300	mV _{p-p}
·	Common Mode Voltage Range		V _{CMRH}	2		1.3	_	V _{CC} -0.9	V
PG Amp.	Closed Loop Gain		G _{FGO}	3		40	46	50	dB
	Reference Voltage		V _{ref}	3		2.15	2.6	2.9	V
	Input Sensitivity		V _{HFG}	3		_	2.5		mV _{p-p}
	Input Offset Voltage		V _{OFG}	3		_	1		mV
	Output Voltage	High	V _{INT-H}	4		3.4	3.8	4.7	V
Integrator		Low	V _{INT-L}	4		0.4	1.0	1.6	V
Amp.	A-Input Current		I _A -	4		_	_	0.4	μA
	Open Loop Gain		G _{INT}	_	-3 dB point	_	55		dB
Speed Changing	Input Switching Voltage		N	5	H: 360 rpm	3.0	—	V _{CC}	v
			V _{MS} -th		L: 300 rpm	0	—	2.0	
	Input Current		I _{MS}	5	V _{MS} = GND	_	-2.5	0.1	μA
OSC Frequency Range			f _{osc}	_	T _j = −30~125°C	300	490	600	kHz

CHARACTERISTIC			SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
PWM Output Voltage Hiddle		V _{PWM H}	6	I _{OH} = –100 μA (fx / 8192) < FG	_	V _{CC} - 0.1	_	V	
		Hiddle	V _{PWM M}	6	OUTPUT-V _{CC} : 50 kΩ OUTPUT-GND : 50 kΩ (fx / 8192) = FG	_	V _{CC} / 2	_	V
	Low		V _{PWM L}	6	I _{OL} = −100 μA (fx / 8192) > FG	_	0.1	_	V
	Static Voltage		V _{MID}	_		_	2.2	_	V
	Output Refference Voltage-1	ial	V _{M-diff1}	_	$G_{V (INT)} = 1$ V = (V _{CC} / 2) + 1 V	_	10	60	mV
Output Stage	Output Refferencial Voltage-2		V _{M-diff2}	_		_	0.4	_	V
	Saturation	Upper	V _{sat U}	7	I _O = 500 mA		1.1	1.35	V
	Voltage	Lower	V _{sat L}	7	I _O = 500 mA		0.5	0.75	V
	Switching Voltage		V _{ST-th}	5	H : Stand-by Mode	2.4	_	V _{CC}	V
Stand-by Input					L : Enable Mode	0	_	0.8	
•	Input Current		I _{ST}	5	V _{ST} = GND	—	0.05	1.0	μA
Hall Bias Str	oration Voltage		V _{SB-SAT}	7	I _{IH} = 10 mA	_	0.11	0.3	V
	Station Voltage				I _{IH} = 20 mA	—	0.19	0.5	
Current Limi	t Operating Voltage	е	V _{ISD}	—	R _f Voltage	—	140	—	mV
	Input Current		I _{IDX}	8		—	—	3	μA
	Common Mode Voltage Range		V _{CMRI}	8		1.5	_	V _{CC} - 0.3	V
Index	Hysteresis Width		V _{hys}	_		_	2.5	_	mV
Stage	Output Voltage	Low	V _{IDXL}	8	I _O = 1.0 mA	_	1.0	0.4	V
		High	V _{IDXH}	8	I _O = 1.0 mA	—	V _{CC}	_	V
	Maximum Input		V _{INI}	8		_	—	0.3	V _{p-p}
Index Sensor Bias			V _{REG}	1	R _L = 1 kΩ	2.1	2.5	2.9	V
Thermal Shutdown Operating Temperature		TSD	_		150	_	_	°C	

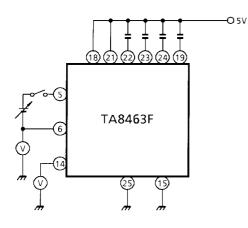
TEST CIRCUIT 1 I_{CC1} , I_{CC2} , V_{REG}



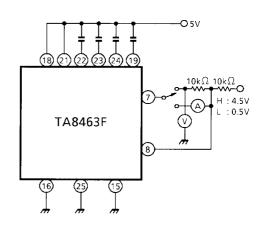
TEST CIRCUIT 2 VH, VCMRH



TEST CIRCUIT 3 GFGO, Vref, VHFG, VOFG

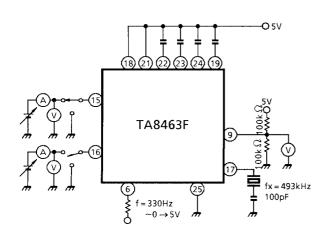


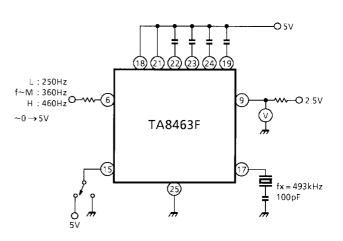
TEST CIRCUIT 4 VINT-H, VINT-L, IA-



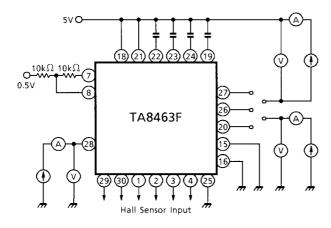
 $TEST\ CIRCUIT\ 5\quad V_{MS-th},\ I_{MS},\ V_{ST-th},\ I_{ST}$

TEST CIRCUIT 6 VPWM H, VPWM M, VPWM L

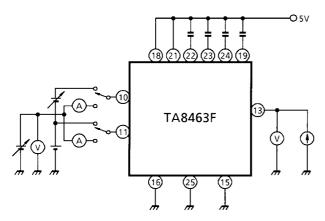




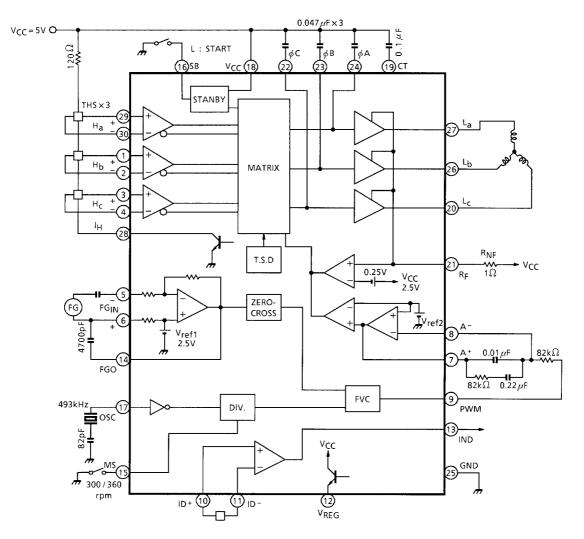
TEST CIRCUIT 7 Vsat U, Vsat L, VSB-SAT



 $\textbf{TEST CIRCUIT 8} \quad \textbf{I}_{\text{IDX}}, \, \textbf{V}_{\text{CMRI}}, \, \textbf{V}_{\text{TH},} \, \textbf{V}_{\text{IDXL}}, \, \textbf{V}_{\text{IDXH},} \, \textbf{V}_{\text{INI}}$



APPLICATION CIRCUIT

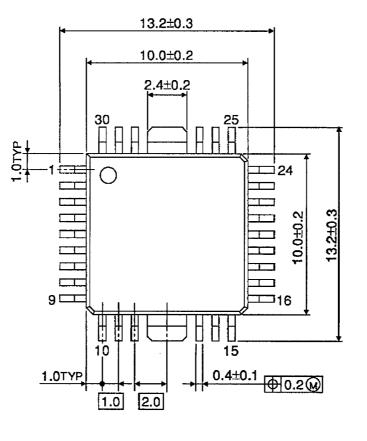


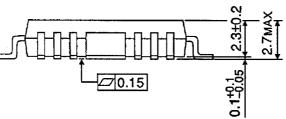
Note: Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

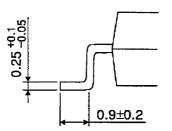
PACKAGE DIMENSIONS

HQFP30-P-1010-1.00

Unit : mm







Weight: 0.61 g (Typ.)

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