TC1070
TC1071
TC1187

EVALUATION KIT AVAILABLE

50mA, 100mA, and 150mA Adjustable CMOS LDOs with Shutdown

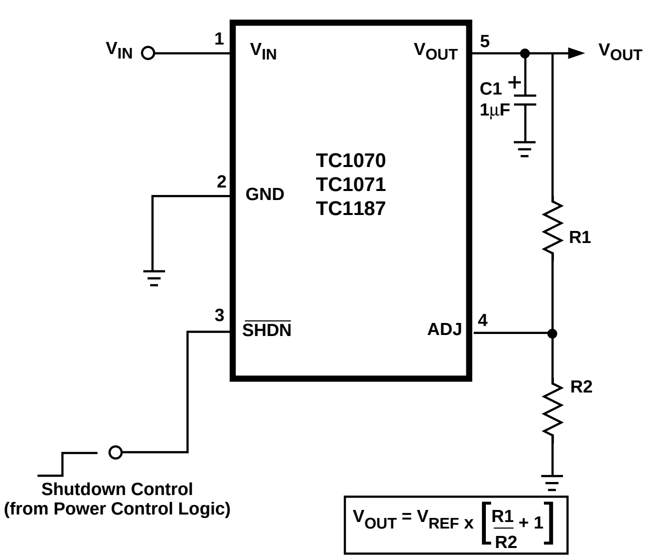
FEATURES

- **Zero Ground Current for Longer Battery Life!**
- **Adjustable Output Voltage**
- **Very Low Dropout Voltage**
- **Guaranteed 50mA, 100mA, 150mA Output (TC1070, TC1071, TC1187, Respectively)**
- **Power-Saving Shutdown Mode**
- **Over-Current and Over-Temperature Protection**
- **Space-Saving 5-Pin SOT-23A Package**
- **Pin Compatible with Bipolar Regulators**

APPLICATIONS

- **Battery Operated Systems**
- **Portable Computers**
- **Medical Instruments**
- **Instrumentation**
- **Cellular / GSM / PHS Phones**
- **Linear Post-Regulator for SMPS**
- **Pagers**

TYPICAL APPLICATION



GENERAL DESCRIPTION

The TC1070, TC1071, and TC1187 are adjustable LDOs designed to supersede a variety of older (bipolar) voltage regulators. Total supply current is typically 50µA at full load (*20 to 60 times lower than in bipolar regulators*!).

The devices' key features include ultra low noise operation, very low dropout voltage — typically 85mV (TC1070); 180mV (TC1071); and 270mV (TC1187) at full load, and fast response to step changes in load. Supply current is reduced to 0.5 µA (max) when the shutdown input is low. The devices incorporate both over-temperature and over-current protection. Output voltage is programmed with a simple resistor divider from V_{OUT} to ADJ to GND.

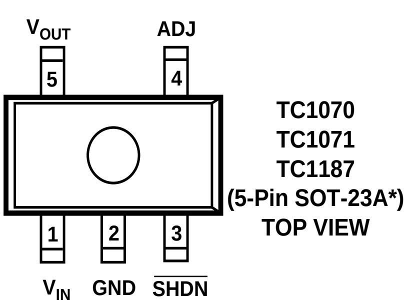
The TC1070, TC1071, and TC1187 are stable with an output capacitor of only 1µF and have a maximum output current of 50mA, 100mA, and 150mA, respectively. For higher output versions, please see the TC1174 (I_{OUT} = 300mA) data sheet.

ORDERING INFORMATION

Part No.	Output Voltage **(V)	Package	Junction Temp. Range
TC1070VCT	Adjustable	5-Pin SOT-23A*	– 40°C to +125°C
TC1071VCT	Adjustable	5-Pin SOT-23A*	– 40°C to +125°C
TC1187VCT	Adjustable	5-Pin SOT-23A*	– 40°C to +125°C
TC1015EV	**Evaluation Kit for CMOS LDO Family**		

NOTE: *5-Pin SOT-23A is equivalent to the EIAJ (SC-74A)
** Other output voltages available. Please contact Microchip Technology for details

PIN CONFIGURATION



NOTE: *5-Pin SOT-23A is equivalent to the EIAJ (SC-74A)

ABSOLUTE MAXIMUM RATINGS*

Input Voltage 6.5V
Output Voltage .... (– 0.3) to (V_{IN} + 0.3)
Power Dissipation .... Internally Limited (Note 5)
Operating Temperature .... – 40°C < T_J < 125°C
Storage Temperature .... – 65°C to +150°C
Maximum Voltage on Any Pin .... V_{IN} + 0.3V to – 0.3V
Lead Temperature (Soldering, 10 Sec.) .... +260°C

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS: $V_{IN} = V_{OUT}$ + 1V, I_L = 0.1mA, C_L = 3.3µF, $\overline{SHDN} > V_{IH}$, T_A = 25°C, unless otherwise specified. **BOLDFACE** type specifications apply for junction temperatures of – 40°C to +125°C.

Symbol	Parameter		Test Conditions	Min	Typ	Max	Units
V_{IN}	Input Operating Voltage			—	—	**6.0**	V
I_{OUTMAX}	Maximum Output Current		TC1070 TC1071 TC1187	**50** **100** **150**	— — —	— — —	mA
V_{OUT}	Adjustable Output Voltage Range			V_{REF}	—	5.5	V
V_{REF}	Reference Voltage			**1.165**	1.20	**1.235**	V
$\Delta V_{REF}/\Delta T$	V_{REF} Temperature Coefficient		Note 1	—	**40**	—	ppm/°C
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation		$(V_R + 1V) \leq V_{IN} \leq 6V$	—	0.05	**0.35**	%
$\Delta V_{OUT}/V_{OUT}$	Load Regulation	TC1070; TC1071 TC1187	I_L = 0.1mA to I_{OUTMAX} I_L = 0.1mA to I_{OUTMAX} (Note 2)	— —	0.5 0.5	**2** **3**	%
$V_{IN} – V_{OUT}$	Dropout Voltage	 TC1071; TC1187 TC1187	I_L = 0.1mA I_L = 20mA I_L = 50mA I_L =100mA I_L = 150mA (Note 3)	— — — — —	2 65 85 180 270	— — **120** **250** **400**	mV
I_{IN}	Supply Current		$\overline{SHDN} = V_{IH}$, $I_L = 0$	—	50	80	µA
I_{INSD}	Shutdown Supply Current		$\overline{SHDN}$ = 0V	—	0.05	0.5	µA
PSRR	Power Supply Rejection Ratio		$F_{RE} \leq$ 1KHz	—	64	—	dB
I_{OUTSC}	Output Short Circuit Current		V_{OUT} = 0V	—	300	450	mA
$\Delta V_{OUT}/\Delta P_D$	Thermal Regulation		Note 4	—	0.04	—	V/W
T_{SD}	Thermal Shutdown Die Temperature			—	160	—	°C
ΔT_{SD}	Thermal Shutdown Hysteresis			—	10	—	°C
eN	Output Noise		$I_L = I_{OUTMAX}$	—	260	—	$nV/\sqrt{Hz}$
$\overline{SHDN}$ Input							
V_{IH}	$\overline{SHDN}$ Input High Threshold		V_{IN} = 2.5V to 6.5V	**45**	—	—	$\%V_{IN}$
V_{IL}	$\overline{SHDN}$ Input Low Threshold		V_{IN} = 2.5V to 6.5V	—	—	**15**	$\%V_{IN}$

ELECTRICAL CHARACTERISTICS: $V_{IN} = V_{OUT} + 1V$, $I_L = 0.1$ mA, $C_L = 3.3\ \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = 25°C$, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
ADJ Input						
I_{ADJ}	Adjust Input Leakage Current		—	50	—	pA

NOTES: 1. TC $V_{OUT} = \dfrac{(V_{OUTMAX} - V_{OUTMIN}) \times 10^6}{V_{OUT} \times \Delta T}$

2. Regulation is measured at a constant junction temperature using low duty cycle pulse testing. Load regulation is tested over a load range from 0.1mA to the maximum specified output current. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
3. Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value.
4. Thermal Regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a current pulse equal to I_{LMAX} at V_{IN} = 6V for T = 10msec.
5. The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction-to-air (i.e. T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation causes the device to initiate thermal shutdown. Please see *Thermal Considerations* section of this data sheet for more details.

PIN DESCRIPTION

Pin No. (5-Pin SOT-23A)	Symbol	Description
1	V_{IN}	Unregulated supply input.
2	GND	Ground terminal.
3	$\overline{SHDN}$	Shutdown control input. The regulator is fully enabled when a logic high is applied to this input. The regulator enters shutdown when a logic low is applied to this input. During shutdown, output voltage falls to zero and supply current is reduced to 0.5µA (max).
4	ADJ	Output voltage adjust terminal. Output voltage setting is programmed with a resistor divider from V_{OUT} to this input. A capacitor may also be added to this input to reduce output noise (See Text).
5	V_{OUT}	Regulated voltage output.

DETAILED DESCRIPTION

The TC1070, TC1071, and TC1187 are adjustable fixed output voltage regulators. (If a fixed version is desired, please see the TC1014/TC1015/C1185 data sheet.) Unlike bipolar regulators, the TC1070, TC071, and TC1187 supply current does not increase with load current. In addition, V_{OUT} remains stable and within regulation at very low load currents (an important consideration in RTC and CMOS RAM battery back-up applications).

Figure 1 shows a typical application circuit. The regulator is enabled any time the shutdown input ($\overline{SHDN}$) is at or above V_{IH}, and shutdown (disabled) when $\overline{SHDN}$ is at or below V_{IL}. $\overline{SHDN}$ may be controlled by a CMOS logic gate, or I/O port of a microcontroller. If the $\overline{SHDN}$ input is not required, it should be connected directly to the input supply. While in shutdown, supply current decreases to 0.05µA (typical) and V_{OUT} falls to zero volts.

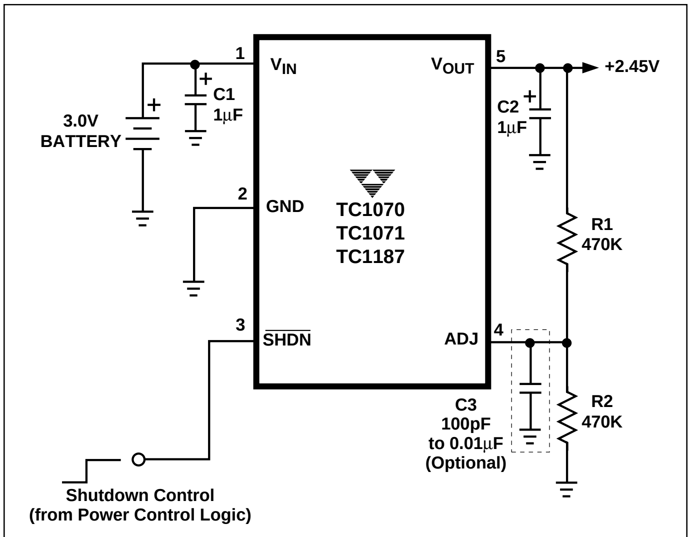


Figure 1. 2.45 Battery-Operated Supply

Adjust Input

The output voltage setting is determined by the values of R1 and R2 (Figure 1). The ohmic values of these resistors should be between 470K and 3M to minimize bleeder current.

The output voltage setting is calculated using the following equation.

$$V_{OUT} = V_{REF} \times \left[\frac{R1}{R2} + 1\right]$$

Equation 1.

The voltage adjustment range of the TC1070, TC1071, and TC1187 is from V_{REF} to (V_{IN} – 0.05V). If so desired, a small capacitor (100 pF to 0.01 µF) may be added to the ADJ input to further reduce output noise.

Output Capacitor

A 1µF (min) capacitor from V_{OUT} to ground is recommended. The output capacitor should have an effective series resistance of 5Ω or less, and a resonant frequency above 1MHz. A 1µF capacitor should be connected from V_{IN} to GND if there is more than 10 inches of wire between the regulator and the AC filter capacitor, or if a battery is used as the power source. Aluminum electrolytic or tantalum capacitor types can be used. (Since many aluminum electrolytic capacitors freeze at approximately – 30°C, solid tantalums are recommended for applications operating below – 25°C.) When operating from sources other than batteries, supply-noise rejection and transient response can be improved by increasing the value of the input and output capacitors and employing passive filtering techniques.

Thermal Considerations

Thermal Shutdown

Integrated thermal protection circuitry shuts the regulator off when die temperature exceeds 160°C. The regulator remains off until the die temperature drops to approximately 150°C.

Power Dissipation

The amount of power the regulator dissipates is primarily a function of input and output voltage, and output current. The following equation is used to calculate worst case *actual* power dissipation:

$$P_D \approx (V_{IN_{MAX}} - V_{OUT_{MIN}}) I_{LOAD_{MAX}}$$

Where:

P_D = Worst case actual power dissipation
$V_{IN_{MAX}}$ = Maximum voltage on V_{IN}
$V_{OUT_{MIN}}$ = Minimum regulator output voltage
$I_{LOAD_{MAX}}$ = Maximum output (load) current

Equation 2.

The maximum *allowable* power dissipation (Equation 2) is a function of the maximum ambient temperature (T_{AMAX}), the maximum allowable die temperature (125°C) and the thermal resistance from junction-to-air (θ_{JA}). 5-Pin SOT-23A package has a θ_{JA} of approximately *220°C/Watt* when mounted on a single layer FR4 dielectric copper clad PC board.

$$P_{D\,MAX} = \frac{(T_{JMAX} - T_{JMAX})}{\theta_{JA}}$$

Where all terms are previously defined.

Equation 3.

Equation 2 can be used in conjunction with Equation 3 to ensure regulator thermal operation is within limits. For example:

Given:

$V_{IN_{MAX}} = 3.0V \pm 10\%$
$V_{OUT_{MIN}} = 2.7V - 2\%$
$I_{LOAD} = 40mA$
$T_{AMAX} = 55°C$

Find: 1. Actual power dissipation
2. Maximum allowable dissipation

Actual power dissipation:

$$P_D \approx (V_{IN_{MAX}} - V_{OUT_{MIN}}) I_{LOAD_{MAX}}$$
$$= [(3.0 \times 1.10) - (2.7 \times .0.98)]40 \times 10^{-3}$$
$$= 26.2mW$$

Maximum allowable power dissipation:

$$\frac{P_{D_{MAX}} = (T_{J_{MAX}} - T_{A_{MAX}})}{\theta_{JA}}$$

$$= \frac{(125 - 55)}{220}$$

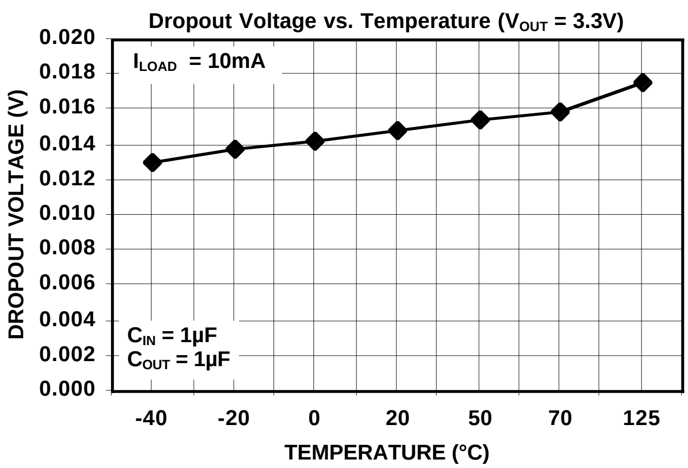
$$= 318mW$$

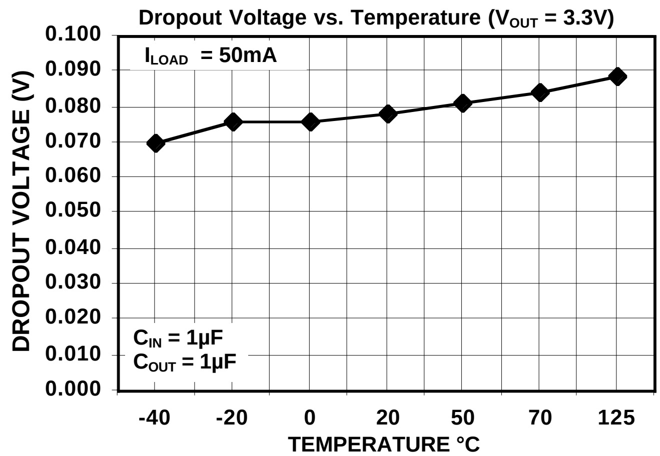
In this example, the TC1070 dissipates a maximum of only 26.2mW; far below the allowable limit of 318mW. In a similar manner, Equation 2 and Equation 3 can be used to calculate maximum current and/or input voltage limits.

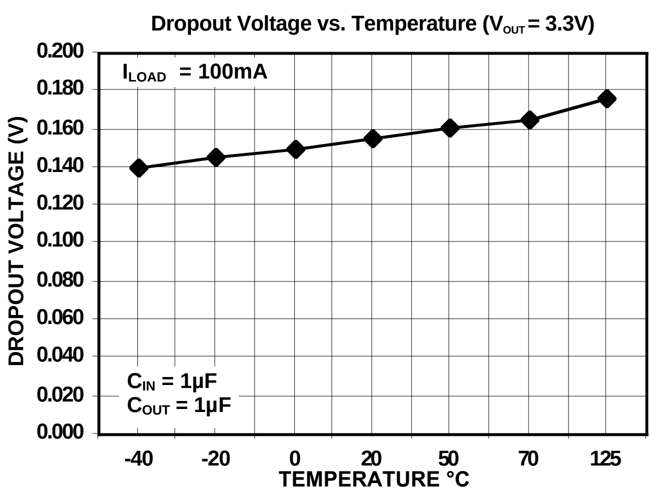
Layout Considerations

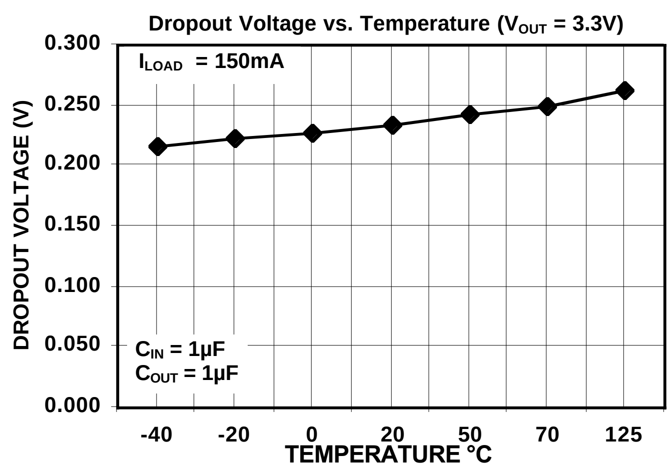
The primary path of heat conduction out of the package is via the package leads. Therefore, layouts having a ground plane, wide traces at the pads, and wide power supply bus lines combine to lower θ_{JA} and therefore increase the maximum allowable power dissipation limit.

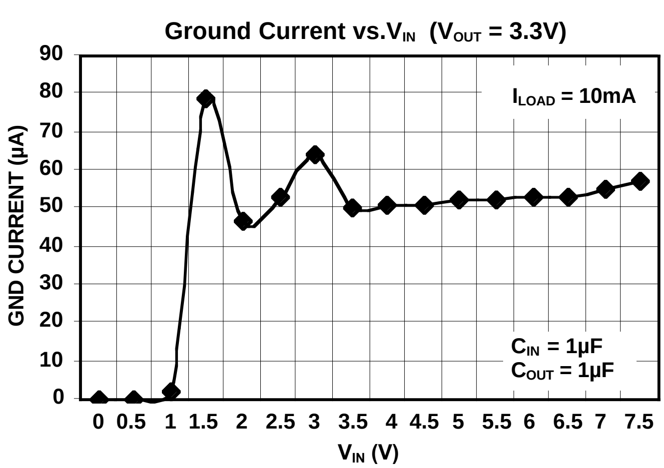
TYPICAL CHARACTERISTICS: (Unless otherwise specified, all parts are measured at Temperature = 25°C)

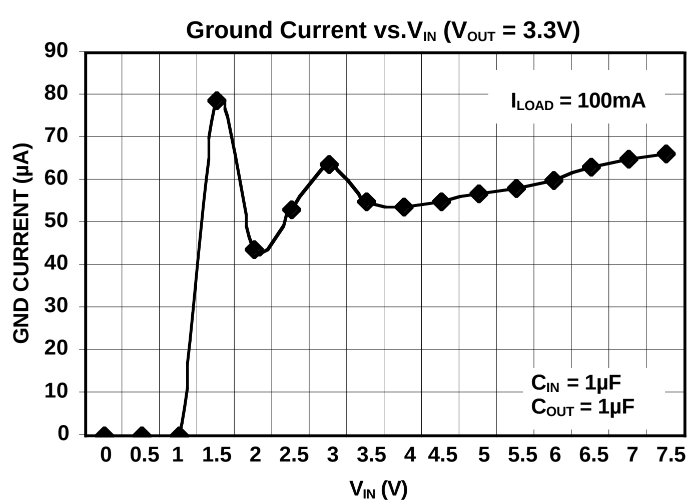




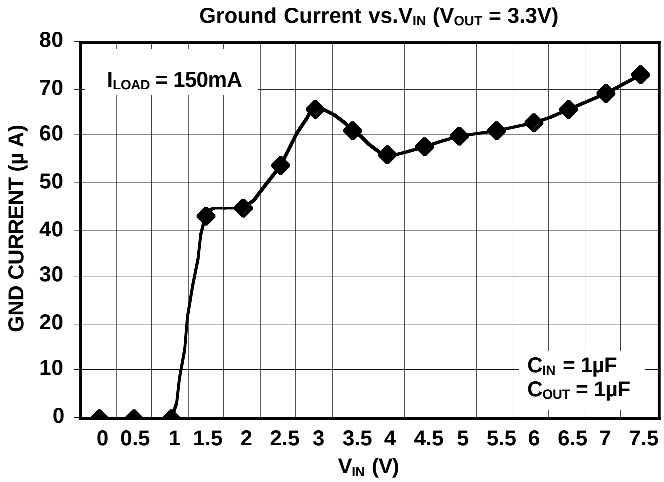


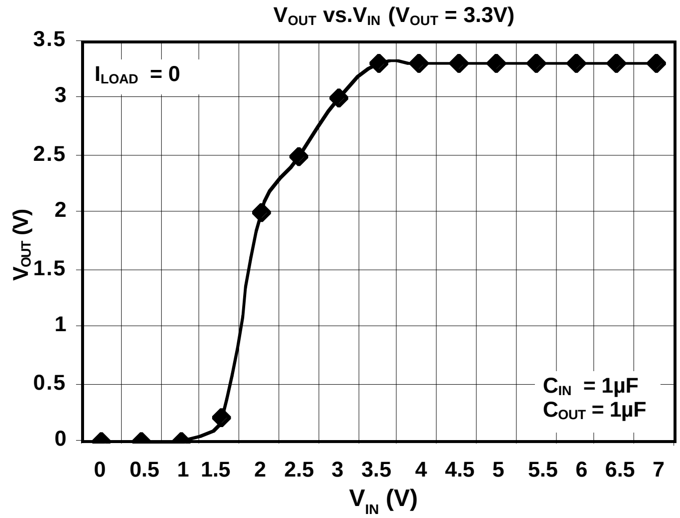


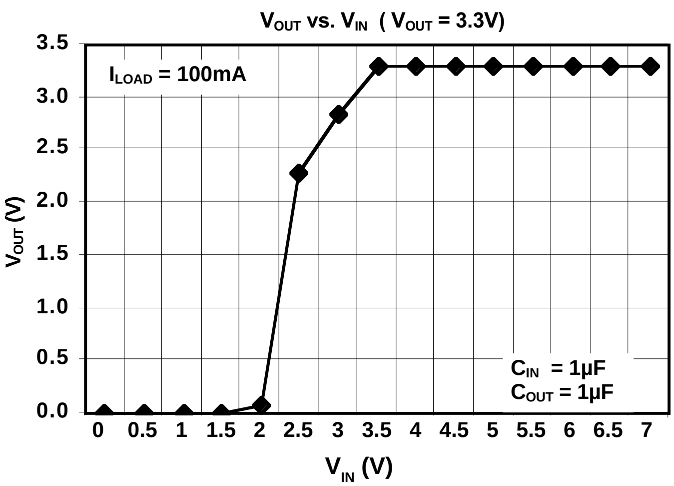


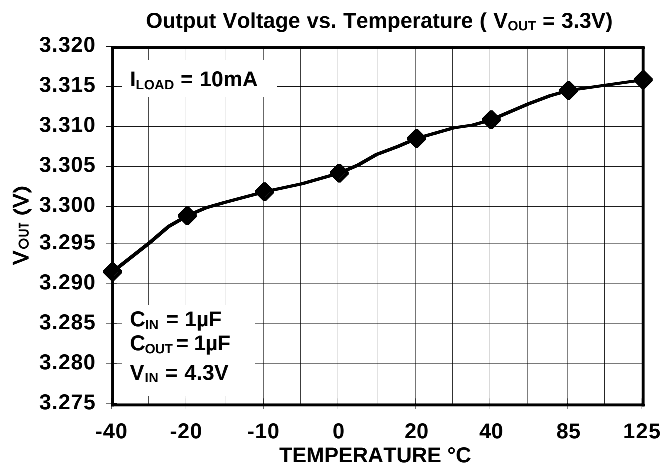


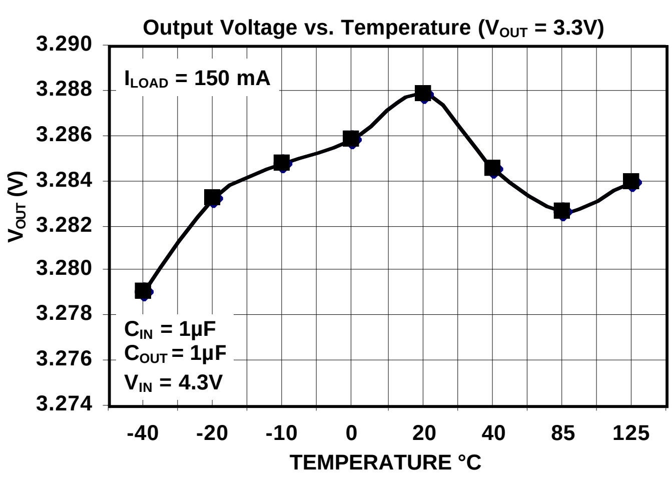
TYPICAL CHARACTERISTICS: (Unless otherwise specified, all parts are measured at Temperature = 25°C



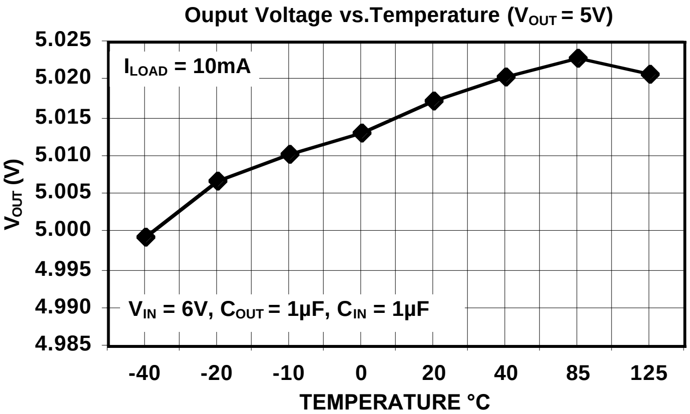


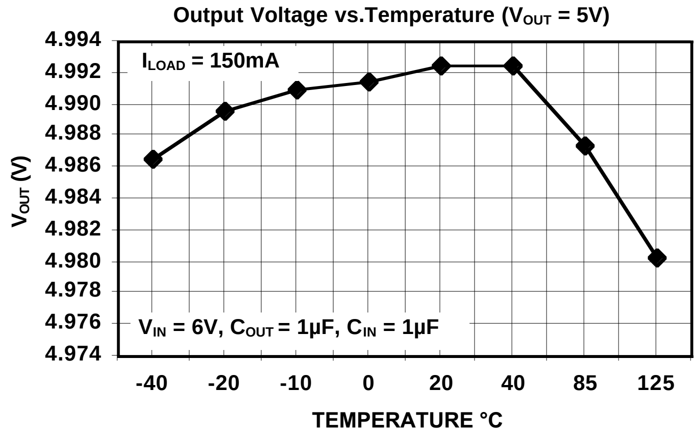


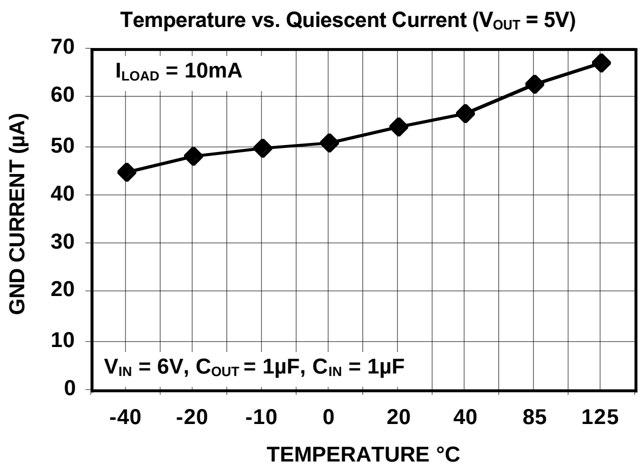


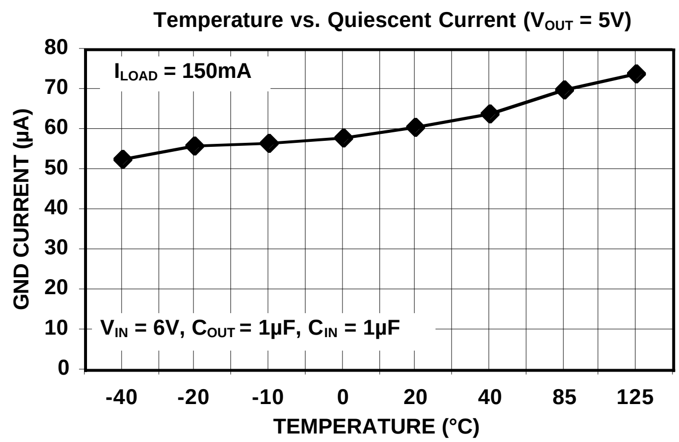


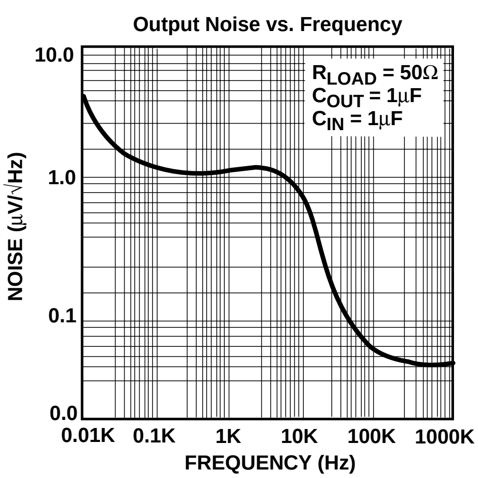
TYPICAL CHARACTERISTICS

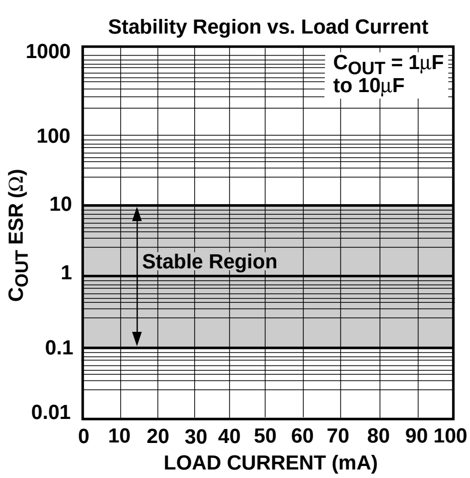


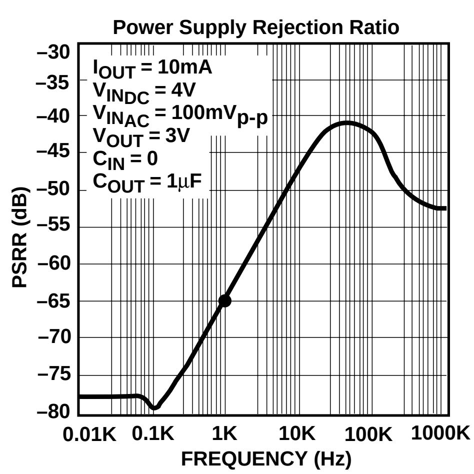








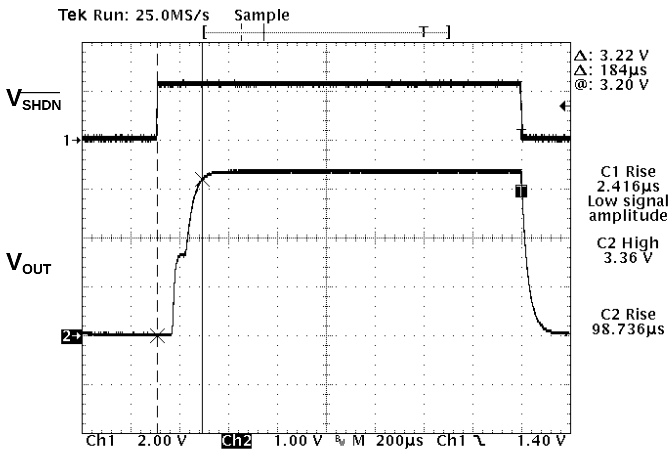




TYPICAL CHARACTERISTICS

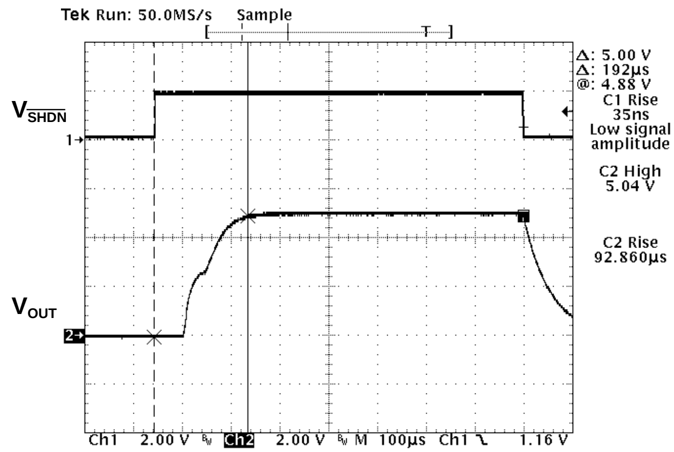
Measure Rise Time of 3.3V LDO

Conditions: C_{IN} = 1µF, C_{OUT} = 1µF, I_{LOAD} = 100mA, V_{IN} = 4.3V, Temp = 25°C, Rise Time = 184µS



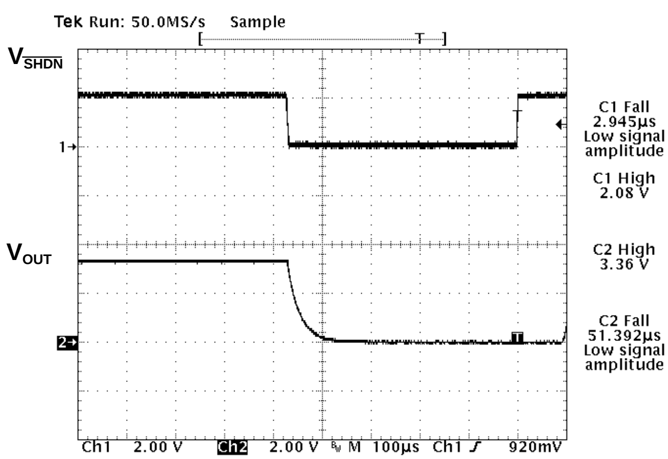
Measure Rise Time of 5.0V LDO

Conditions: C_{IN} = 1µF, C_{OUT} = 1µF, I_{LOAD} = 100mA, V_{IN} = 6V, Temp = 25°C, Rise Time = 192µS



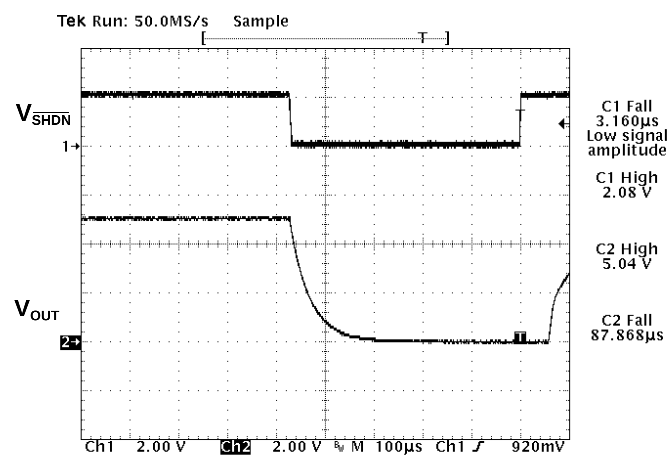
Measure Fall Time of 3.3V LDO

Conditions: C_{IN} = 1µF, C_{OUT} = 1µF, I_{LOAD} = 100mA, V_{IN} = 4.3V, Temp = 25°C, Fall Time = 52µS



Measure Fall Time of 5.0V LDO

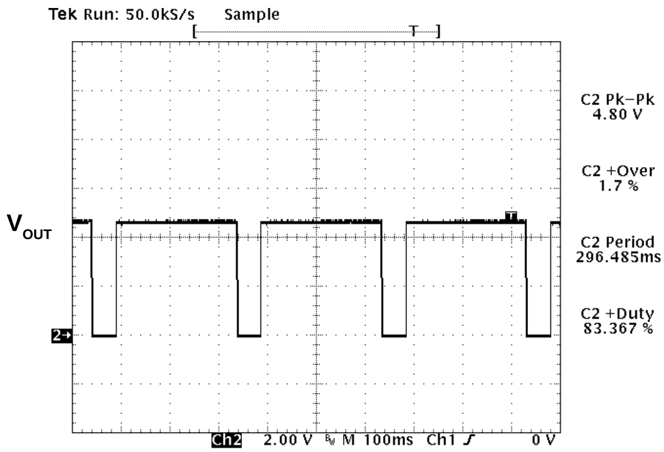
Conditions: C_{IN} = 1µF, C_{OUT} = 1µF, I_{LOAD} = 100mA, V_{IN} = 6V, Temp = 25°C, Fall Time = 88µS



TYPICAL CHARACTERISTICS

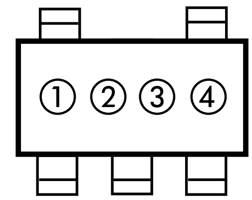
Thermal Shutdown Response of 5.0V LDO

Conditions: V_{IN} = 6V, C_{IN} = 0µF, C_{OUT} = 1µF



I_{LOAD} was increased until temperature of die reached about 160°C, at which time integrated thermal protection circuitry shuts the regulator off when die temperature exceeds approximately 160°C. The regulator remains off until die temperature drops to approximately 150°C.

MARKING



① & ② = part number code + temperature range and voltage

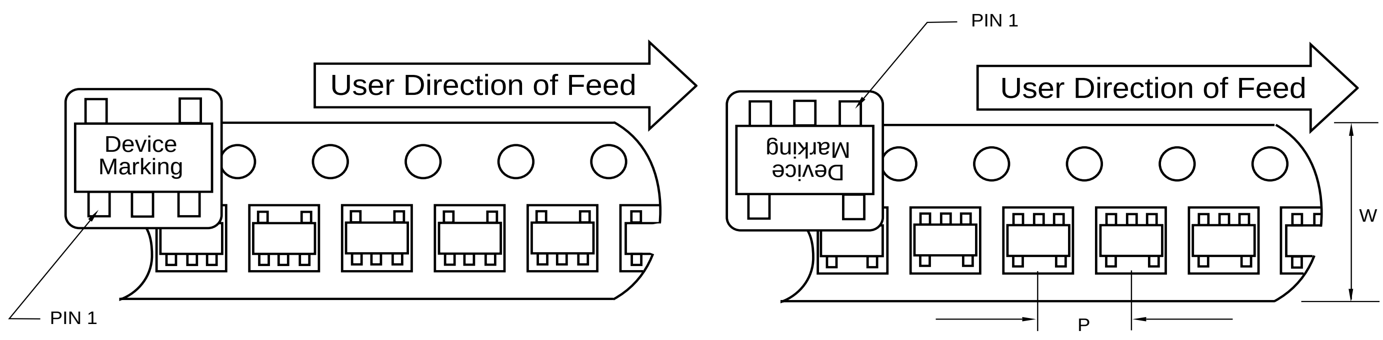
(V)	TC1070 Code	TC1071 Code	TC1187 Code
Adjustable	BA	BB	R9

③ represents year and quarter code

④ represents lot ID number

TAPING FORM

Component Taping Orientation for 5-Pin SOT-23A (EIAJ SC-74A) Devices



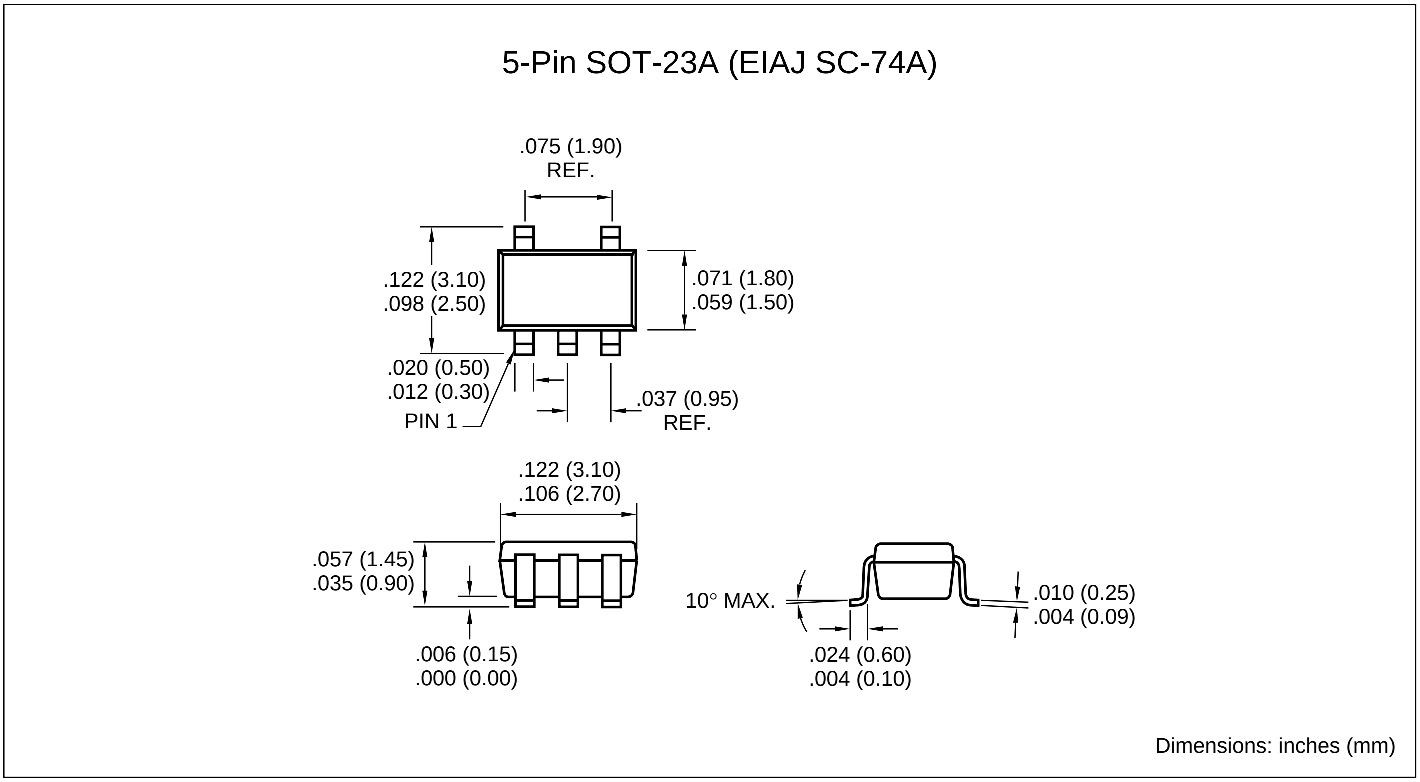
Standard Reel Component Orientation
TR Suffix Device
(Mark Right Side Up)

Reverse Reel Component Orientation
RT Suffix Device
(Mark Upside Down)

Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
5-Pin SOT-23A	8 mm	4 mm	3000	7 in

PACKAGE DIMENSIONS



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