

**TC74HCU04AP, TC74HCU04AF, TC74HCU04AFN**

**HEX INVERTER**

The TC74HCU04A is a high speed CMOS INVERTER fabricated with silicon gate C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

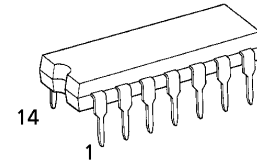
Since the internal circuit is composed of a single stage inverter, it can be used in analog applications such as crystal oscillators.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

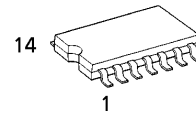
**FEATURES:**

- High Speed..... $t_{pd} = 4\text{ns}(\text{typ.})$  at  $V_{CC} = 5\text{V}$
- Low Power Dissipation..... $I_{CC} = 1\mu\text{A}(\text{Max.})$  at  $T_a = 25^\circ\text{C}$
- High Noise Immunity..... $V_{NIH} = V_{NIH} = 10\%V_{CC}$  (Min.)
- Output Drive Capability..... 10 LSTTL Loads
- Symmetrical Output Impedance...  $|I_{OH}| = I_{OL} = 4\text{mA}(\text{Min.})$
- Balanced Propagation Delays..... $t_{pLH} \approx t_{pHL}$
- Wide Operating Voltage Range...  $V_{CC}(\text{opr.}) = 2\text{V} \sim 6\text{V}$
- Pin and Function Compatible with 74LS04

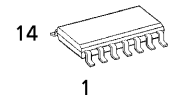
(Note) The JEDEC SOP (FN) is not available in Japan.



P (DIP14-P-300-2.54)  
Weight : 0.96g (Typ.)

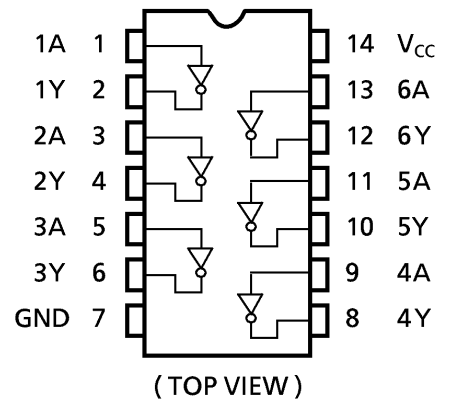


F (SOP14-P-300-1.27)  
Weight : 0.18g (Typ.)

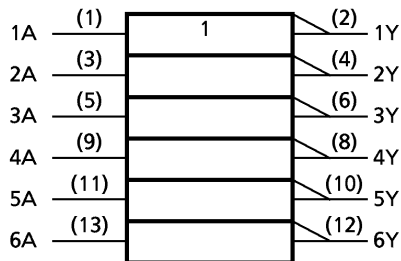


FN (SOL14-P-150-1.27)  
Weight : 0.12g (Typ.)

**PIN ASSIGNMENT**



**IEC LOGIC SYMBOL**



**TRUTH TABLE**

A	Y
L	H
H	L

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	$V_{CC}$	-0.5~7	V
DC Input Voltage	$V_{IN}$	-0.5~ $V_{CC}+0.5$	V
DC Output Voltage	$V_{OUT}$	-0.5~ $V_{CC}+0.5$	V
Input Diode Current	$I_{IK}$	$\pm 20$	mA
Output Diode Current	$I_{OK}$	$\pm 20$	mA
DC Output Current	$I_{OUT}$	$\pm 25$	mA
DC $V_{CC}$ / Ground Current	$I_{CC}$	$\pm 50$	mA
Power Dissipation	$P_D$	500 (DIP)* / 180 (SOP)	mW
Storage Temperature	$T_{stg}$	-65~150	°C

\*500mW in the range of  $T_a = -40^{\circ}\text{C} \sim 65^{\circ}\text{C}$ . From  $T_a = 65^{\circ}\text{C}$  to  $85^{\circ}\text{C}$  a derating factor of  $-10\text{mW}/^{\circ}\text{C}$  shall be applied until 300mW.

## RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	$V_{CC}$	2~6	V
Input Voltage	$V_{IN}$	0~ $V_{CC}$	V
Output Voltage	$V_{OUT}$	0~ $V_{CC}$	V
Operating Temperature	$T_{opr}$	-40~85	°C

## DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	$V_{CC}$ (V)	$T_a = 25^{\circ}\text{C}$			$T_a = -40 \sim 85^{\circ}\text{C}$		UNIT	
				MIN.	TYP.	MAX.	MIN.	MAX.		
High - Level Input Voltage	$V_{IH}$		2.0	1.7	—	—	1.7	—	V	
			4.5	3.6	—	—	3.6	—		
			6.0	4.8	—	—	4.8	—		
Low - Level Input Voltage	$V_{IL}$		2.0	—	—	0.3	—	0.3	V	
			4.5	—	—	0.9	—	0.9		
			6.0	—	—	1.2	—	1.2		
High - Level Output Voltage	$V_{OH}$	$V_{IN} = V_{IL}$	$I_{OH} = -20\mu\text{A}$	2.0	1.8	2.0	—	1.9	V	
				4.5	4.0	4.5	—	4.4		—
				6.0	5.5	5.9	—	5.9		—
		$V_{IN} = \text{GND}$	$I_{OH} = -4 \text{ mA}$ $I_{OH} = -5.2 \text{ mA}$	4.5	4.18	4.31	—	4.13		—
				6.0	5.68	5.80	—	5.63		—
				6.0	—	—	—	—		—
Low - Level Output Voltage	$V_{OL}$	$V_{IN} = V_{IH}$	$I_{OL} = 20\mu\text{A}$	2.0	—	0.0	0.2	—	0.2	V
				4.5	—	0.0	0.5	—	0.5	
				6.0	—	0.1	0.5	—	0.5	
		$V_{IN} = V_{CC}$	$I_{OL} = 4 \text{ mA}$ $I_{OL} = 5.2 \text{ mA}$	4.5	—	0.17	0.26	—	0.33	
				6.0	—	0.18	0.26	—	0.33	
				6.0	—	—	—	—	—	
Input Leakage Current	$I_{IN}$	$V_{IN} = V_{CC}$ or GND	6.0	—	—	$\pm 0.1$	—	$\pm 1.0$	$\mu\text{A}$	
Quiescent Supply Current	$I_{CC}$	$V_{IN} = V_{CC}$ or GND	6.0	—	—	1.0	—	10.0		

AC ELECTRICAL CHARACTERISTICS (  $C_L = 15\text{pF}$ ,  $V_{CC} = 5\text{V}$ ,  $T_a = 25^\circ\text{C}$ , Input  $t_r = t_f = 6\text{ns}$  )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition Time	$t_{TLH}$		—	4	8	ns
	$t_{THL}$					
Propagation Delay Time	$t_{pLH}$		—	4	8	
	$t_{pHL}$					

AC ELECTRICAL CHARACTERISTICS (  $C_L = 50\text{pF}$ , Input  $t_r = t_f = 6\text{ns}$  )

PARAMETER	SYMBOL	TEST CONDITION	$T_a = 25^\circ\text{C}$			$T_a = -40\text{--}85^\circ\text{C}$		UNIT	
			$V_{CC}$ (V)	MIN.	TYP.	MAX.	MIN.		MAX.
Output Transition Time	$t_{TLH}$		2.0	—	30	75	—	95	ns
	$t_{THL}$		4.5	—	8	15	—	19	
			6.0	—	7	13	—	16	
Propagation Delay Time	$t_{pLH}$		2.0	—	18	60	—	75	ns
	$t_{pHL}$		4.5	—	6	12	—	15	
			6.0	—	5	10	—	13	
Input Capacitance	$C_{IN}$		—	9	15	—	15	pF	
Power Dissipation Capacitance	$C_{PD}$ (1)		—	13	—	—	—		

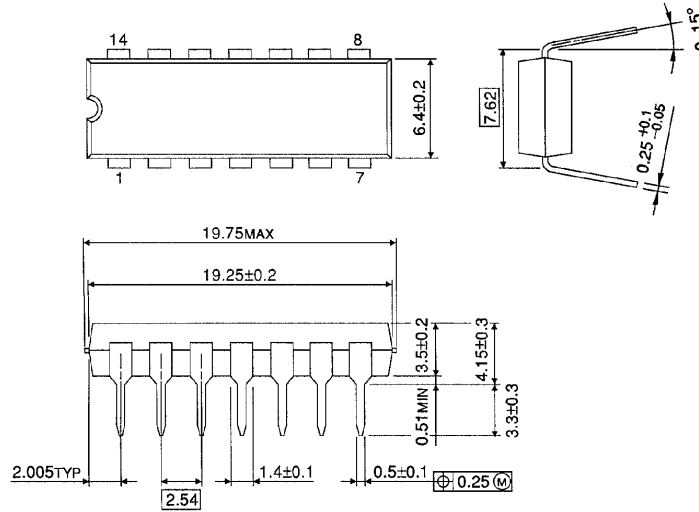
Note (1)  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation :

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 6 \text{ (per Gate)}$$

**DIP 14PIN PACKAGE DIMENSIONS (DIP14-P-300-2.54)**

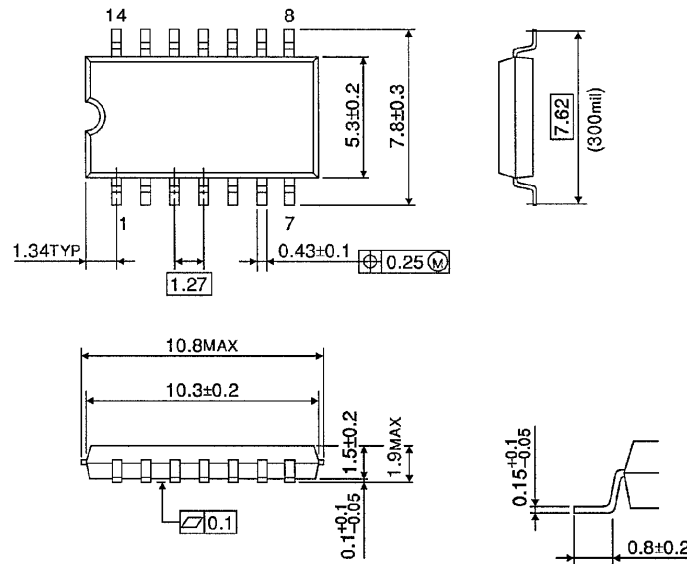
Unit in mm



Weight : 0.96g (Typ.)

**SOP 14PIN (200mil BODY) PACKAGE DIMENSIONS (SOP14-P-300-1.27)**

Unit in mm

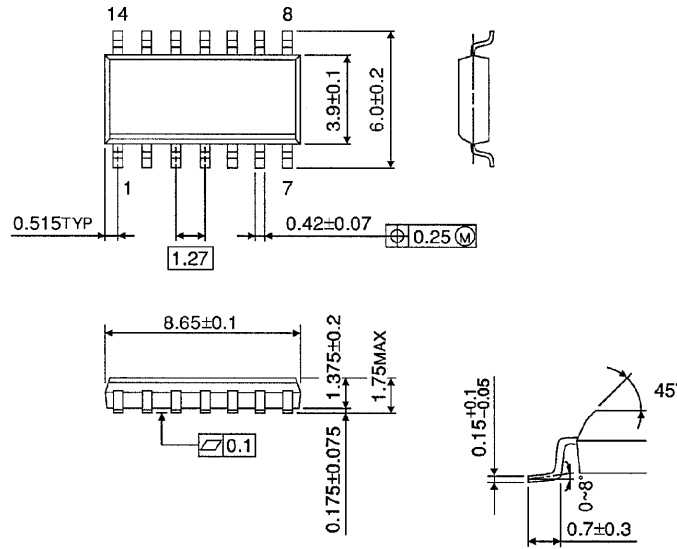


Weight : 0.18g (Typ.)

**SOP 14PIN (150mil BODY) PACKAGE DIMENSIONS (SOL14-P-150 -1.27)**

Unit in mm

(Note) This package is not available in Japan.



Weight : 0.12g (Typ.)

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