TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62309P,TD62309F

#### 6CH LOW SATURATION HIGH-CURRENT SINK DRIVER

The TD62309P, TD62309F are comprised of six NPN low saturation drivers.

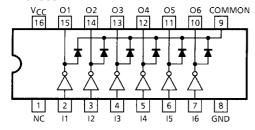
All units feature integral clamp diodes for switching inductive loads. These devices are specifically designed for relay, lamp and LED drive in low voltage systems.

#### **FEATURES**

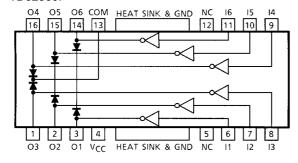
- Low saturation output :  $V_{CE (sat)} = 0.8 \text{ V (Max.)}$  $@I_{OUT} = 450 \text{ mA}$
- Output rating (single output) 20 V (Min.) / 700 mA (Max.)
   Output clamp diodes
- Inputs compatible with TTL and 3~6 V CMOS
- Package type-P: DIP-16 pinPackage type-F: PFP-16 pin

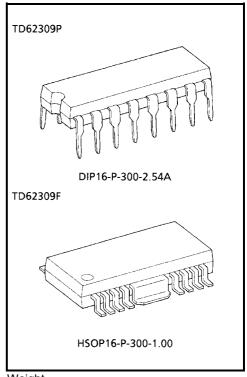
### **PIN CONNECTION (TOP VIEW)**

#### TD62309P



#### TD62309F

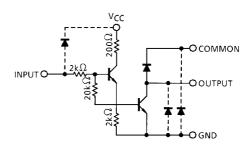




Weight

DIP16-P-300-2.54A : 1.11 g (Typ.) HSOP16-P-300-1.00 : 0.50 g (Typ.)

### **SCHEMATICS (EACH DRIVER)**



Note: The input and output parasitic diodes cannot be used as clamp diodes.

### MAXIMUM RATINGS (Ta = 25°C)

**TOSHIBA** 

CHARACTERIS	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	10	V	
Output Sustaining Voltage	V <sub>CE</sub> (SUS)	20	V	
Output Current	lout	700	mA / ch	
Input Voltage	V <sub>IN</sub>	10	V	
Input Current	I <sub>IN</sub>	10	mA	
Clamp Diode Reverse Voltage		V <sub>R</sub>	20	V
Clamp Diode Forward Current		l <sub>F</sub>	700	mA
Power Dissipation	Р	P <sub>D</sub>	1.47	W
	F	רט	1.4 (Note)	V V
Operating Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C

Note: On Glass Epoxy PCB (30 × 30 × 1.6 mm Cu 30%)

### RECOMMENDED OPERATING CONDITIONS (Ta = $-40 \sim 85$ °C)

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage		$V_{CC}$	_	3	5	7	V
Output Sustaining Voltage		V <sub>CE (SUS)</sub>	_	_	_	20	V
Output Current		lout	DC 1 circuit	0	_	700	mA
			Tpw = 25 ms, 6 circuits	0	_	200	
Input Voltage		V <sub>IN</sub>	_	0	_	V <sub>CC</sub>	V
Clamp Diode Reverse Voltage		V <sub>R</sub>	_	_	_	20	V
Clamp Diode Forward Current		IF	_	_	_	700	mA
Power Dissipation	Р	P <sub>D</sub>	_	_	_	0.52	W
	F		(Note)	_	_	0.5	VV

2

Note: On Glass Epoxy PCB (30 × 30 × 1.6 mm Cu 30%)

## **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

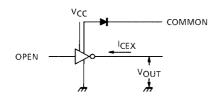
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current		I <sub>CEX</sub>	1	V <sub>OUT</sub> = 20 V, Ta = 85°C	_	_	100	μΑ
Output Saturation Voltage		V <sub>CE (sat)</sub>	2	V <sub>CC</sub> = 5 V, I <sub>OUT</sub> = 450 mA	_	_	0.8	V
				V <sub>CC</sub> = 5 V, I <sub>OUT</sub> = 200 mA	_	_	0.45	
Input Current (Output On)		I <sub>IN (ON)</sub>	3	V <sub>IN</sub> = 3.2V	_	0.84	1.4	mA
D.C Forward Current Transfer Ratio		h <sub>FE</sub>	2	V <sub>CE</sub> = 4 V, V <sub>CC</sub> = 6 V I <sub>OUT</sub> = 300 mA	3000	_	-	
Supply Current	Output On	I <sub>CC (ON)</sub>	6	V <sub>CC</sub> = 7 V, V <sub>IN</sub> = 3.2 V 6 circuits	_	120	300	mA
	Output Off	I <sub>CC (OFF)</sub>	6	V <sub>CC</sub> = 7 V	_	_	10	μΑ
Clamp Diode Reverse Current		I <sub>R</sub>	4	V <sub>R</sub> = 20 V	_	_	100	μΑ
Clamp Diode Forward Voltage		V <sub>F</sub>	5	I <sub>F</sub> = 350 mA	_	_	2.7	V
Turn-On Delay		t <sub>ON</sub>	7	V <sub>CC</sub> = 5.0 V, R <sub>L</sub> = 36 Ω C <sub>L</sub> = 15 pF, V <sub>OUT</sub> = 20 V	_	0.1	_	116
Turn-Off Delay		t <sub>OFF</sub>				0.2		μs

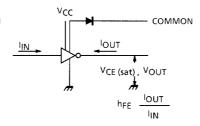
### **TEST CIRCUIT**

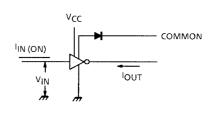
### 1. I<sub>CEX</sub>

# 2. h<sub>FE</sub>, V<sub>CE (sat)</sub>

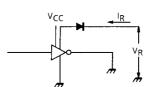
3. I<sub>IN</sub> (ON)



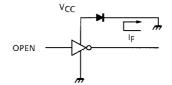


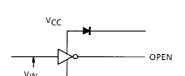


### 4. I<sub>R</sub>



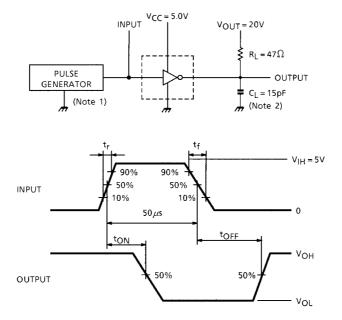
5. V<sub>F</sub>





6. I<sub>CC</sub> (ON), I<sub>CC</sub> (OFF)

#### 7. ton, toff



Note 1: Pulse Width 50 µs, duty cycle 10%

Output impedance 50  $\Omega$ ,  $t_r \le 5$  ns,  $t_f \le 10$  ns

Note 2: C<sub>L</sub> includes probe and jig capacitance.

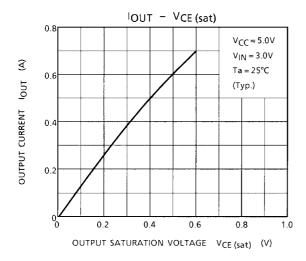
#### **PRECAUTIONS for USING**

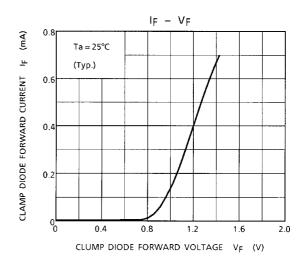
This IC does not include built-in protection circuits for excess current or overvoltage.

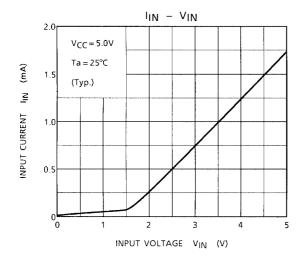
If this IC is subjected to excess current or overvoltage, it may be destroyed.

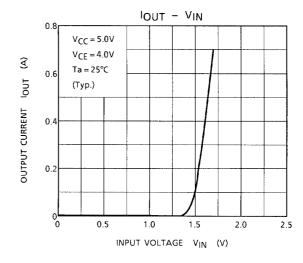
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

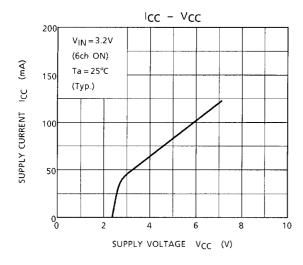
Utmost care is necessary in the design of the output line, VCC, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

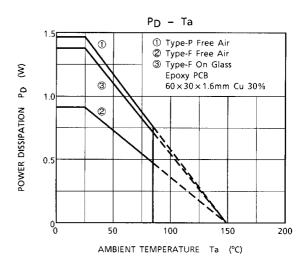












### **PACKAGE DIMENSIONS**

DIP16-P-300-2.54A

Unit: mm

19.75MAX

19.25±0.2

0.735TYP

2.54

1.4±0.1

0.5±0.1

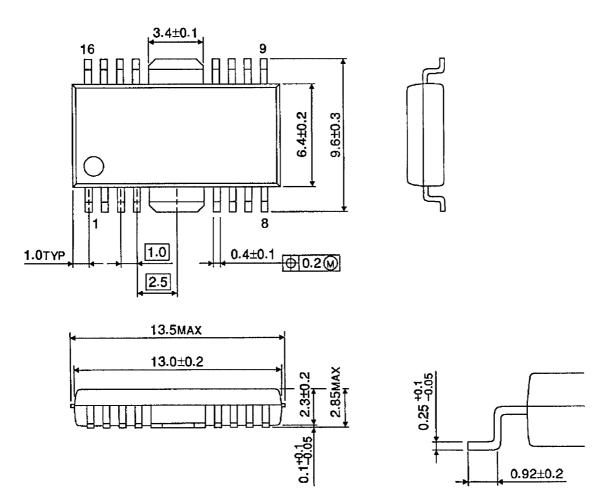
0.5±0.1

0.5±0.1

Weight: 1.11 g (Typ.)

### **PACKAGE DIMENSIONS**

HOSP16-P-300-1.00 Unit: mm



Weight: 0.50 g (Typ.)

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