



110CNQ045A

SCHOTTKY RECTIFIER
New GenIII D-61 Package

110 Amp

Major Ratings and Characteristics

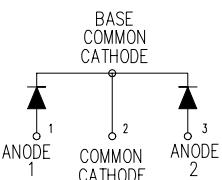
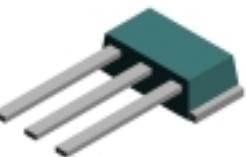
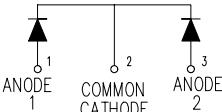
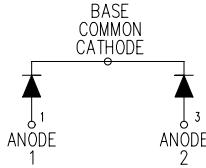
Characteristics	110CNQ045A	Units
$I_{F(AV)}$ Rectangular waveform	110	A
V_{RRM}	45	V
I_{FSM} @ $t_p = 5\mu s$ sine	5400	A
V_F @ $55A_{pk}, T_J = 125^\circ C$ (per leg)	0.5	V
T_J range	-55 to 150	°C

Description/Features

The 110CNQ045A center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to $150^\circ C$ junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

- $150^\circ C T_J$ operation
- Center tap module
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Low profile, small footprint, high current package
- *New fully transfer-mold low profile, small footprint, high current package*

Case Styles

110CNQ045A	110CNQ045ASM	110CNQ045ASL
  D61-8	  D61-8-SM	  D61-8-SL

110CNQ045A

Bulletin PD-20629 rev. A 09/01

International
 Rectifier

Voltage Ratings

Part number		110CNQ045A	
V_R Max. DC Reverse Voltage (V)		45	
V_{RWM} Max. Working Peak Reverse Voltage (V)			

Absolute Maximum Ratings

Parameters	110CNQ	Units	Conditions		
$I_{F(AV)}$ Max. Average Forward Current Per Leg * See Fig. 5	55	A	50% duty cycle @ $T_J = 125^\circ\text{C}$, rectangular waveform		
Per Device	110				
I_{FSM} Max. Peak One Cycle Non-Repetitive Surge Current (Per Leg) * See Fig. 7	5400	A	5μs Sine or 3μs Rect. pulse	Following any rated load condition and with rated V_{RRM} applied	
	800		10ms Sine or 6ms Rect. pulse		
E_{AS} Non-Repetitive Avalanche Energy (Per Leg)	54	mJ	$T_J = 25^\circ\text{C}$, $I_{AS} = 8$ Amps, $L = 1.7$ mH		
I_{AR} Repetitive Avalanche Current (Per Leg)	8	A	Current decaying linearly to zero in 1μsec Frequency limited by T_J max. $V_A = 1.5 \times V_R$ typical		

Electrical Specifications

Parameters	110CNQ	Units	Conditions		
V_{FM} Max. Forward Voltage Drop (Per Leg) * See Fig. 1 (1)	0.54	V	@ 55A	$T_J = 25^\circ\text{C}$	
	0.7	V	@ 110A		
	0.5	V	@ 55A	$T_J = 125^\circ\text{C}$	
	0.69	V	@ 110A		
I_{RM} Max. Reverse Leakage Current (Per Leg) * See Fig. 2 (1)	3	mA	$T_J = 25^\circ\text{C}$	$V_R = \text{rated } V_R$	
	350	mA	$T_J = 125^\circ\text{C}$		
C_T Max. Junction Capacitance (Per Leg)	3800	pF	$V_R = 5V_{DC}$, (test signal range 100Khz to 1Mhz) 25°C		
L_S Typical Series Inductance (Per Leg)	5.5	nH	Measured lead to lead 5mm from package body		
dv/dt Max. Voltage Rate of Change (Rated V_R)	10,000	V/ μs			

(1) Pulse Width < 300μs, Duty Cycle <2%

Thermal-Mechanical Specifications

Parameters	110CNQ	Units	Conditions	
T_J Max. Junction Temperature Range	-55 to 150	°C		
T_{stg} Max. Storage Temperature Range	-55 to 150	°C		
R_{thJC} Max. Thermal Resistance Junction to Case (Per Leg)	0.5	°C/W	DC operation	* See Fig. 4
R_{thJC} Max. Thermal Resistance Junction to Case (Per Package)	0.25	°C/W	DC operation	
R_{thCS} Typical Thermal Resistance, Case to Heatsink (D61-8 Only)	0.30	°C/W	Mounting surface, smooth and greased Device flatness < 5 mils	
wt Approximate Weight	7.8(0.28)	g(oz.)		
T Mounting Torque (D61-8 Only)	Min.	40(35)	Kg-cm	
	Max.	58(50)	(lbf-in)	

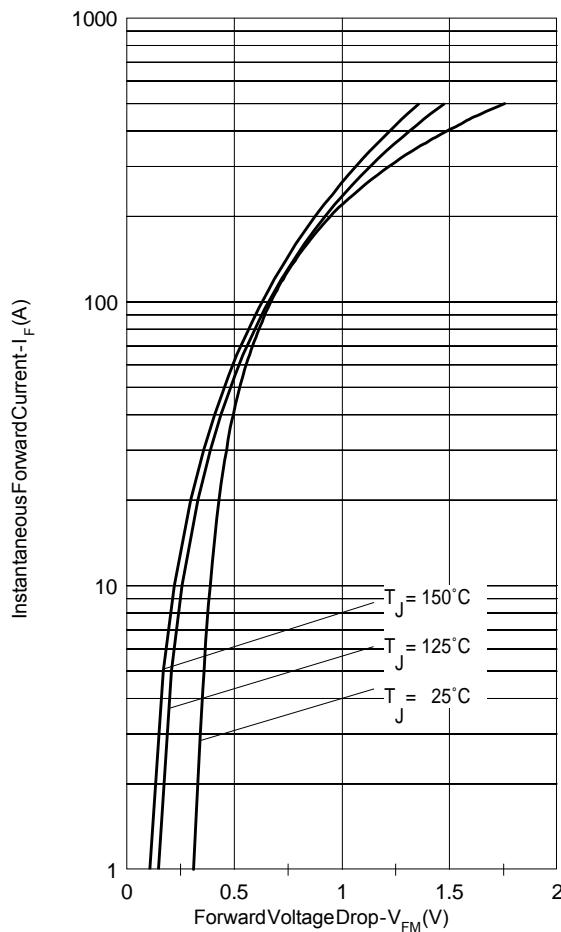


Fig.1-Max. Forward Voltage Drop Characteristics
 (PerLeg)

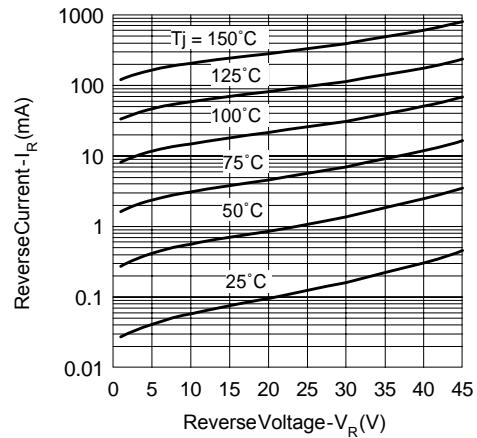


Fig.2-Typical Values Of Reverse Current
 Vs. Reverse Voltage (PerLeg)

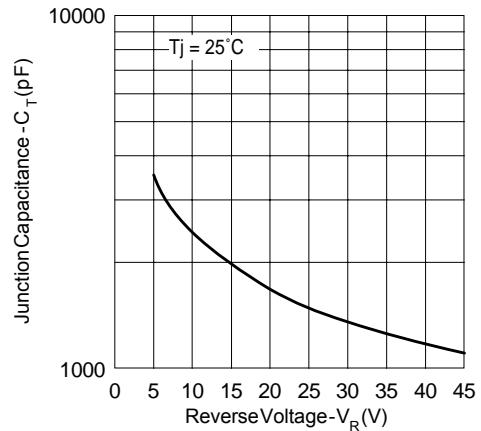


Fig.3-Typical Junction Capacitance
 Vs. Reverse Voltage (PerLeg)

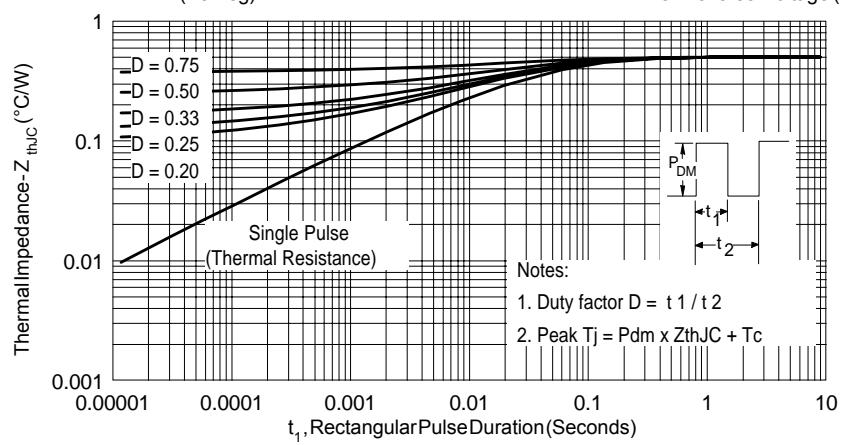


Fig.4-Max. Thermal Impedance Z_{thJC} Characteristics (PerLeg)

110CNQ045A

Bulletin PD-20629 rev. A 09/01

International
IR Rectifier

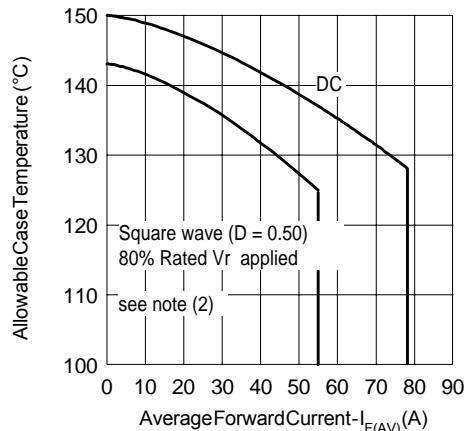


Fig.5-Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

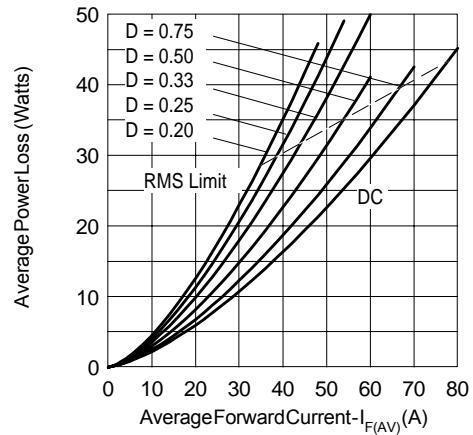


Fig.6-Forward Power Loss Characteristics (Per Leg)

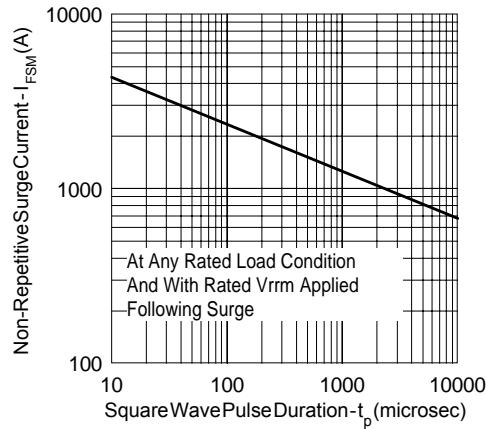


Fig.7-Max. Non-Repetitive Surge Current (Per Leg)

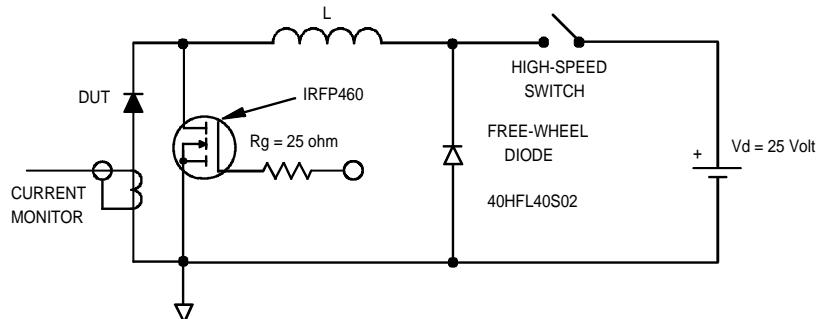


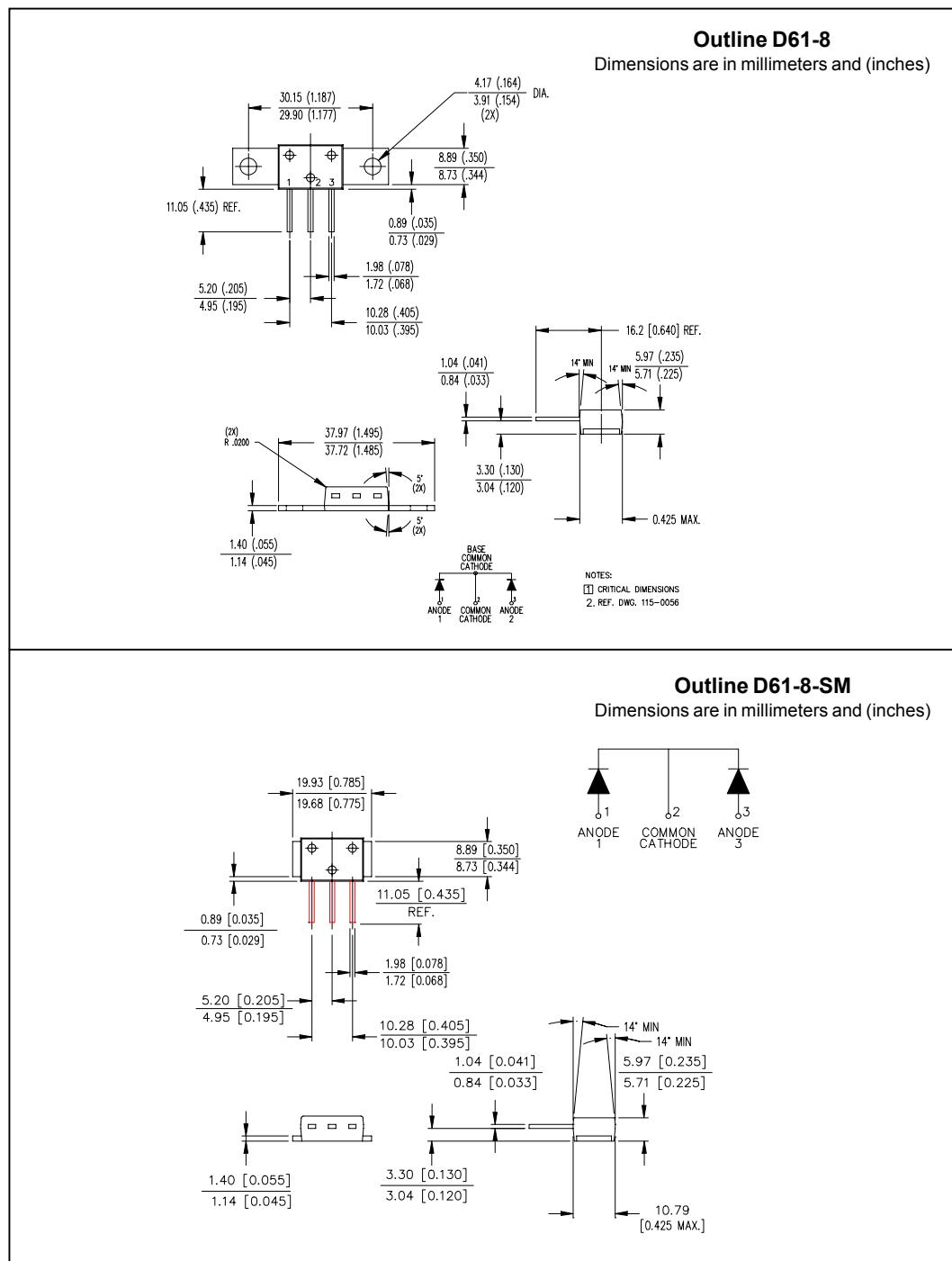
Fig.8-Unclamped Inductive Test Circuit

(2) Formula used: $T_c = T_j - (P_d + P_{dREV}) \times R_{thJC}$;

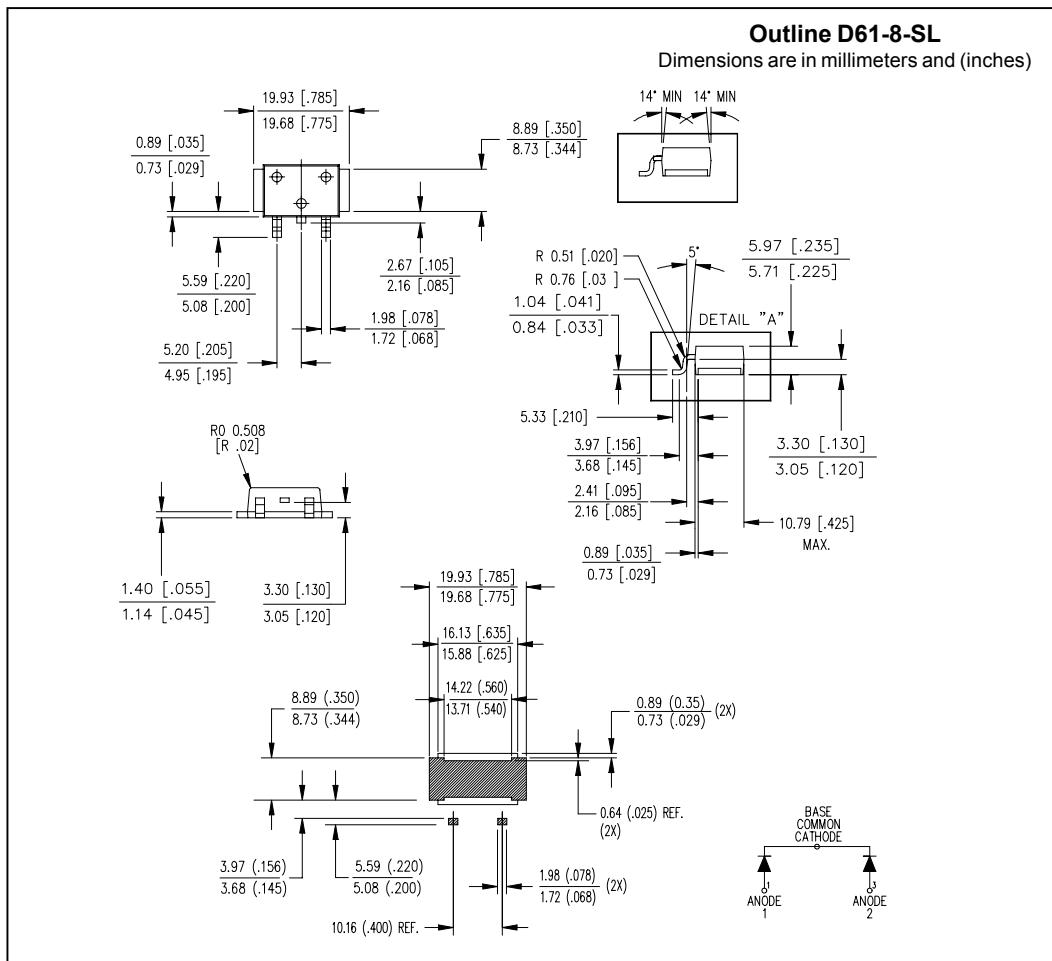
P_d = Forward Power Loss = $I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);

P_{dREV} = Inverse Power Loss = $V_{R1} \times I_R @ (1 - D)$; $I_R @ V_{R1} = 80\%$ rated V_R

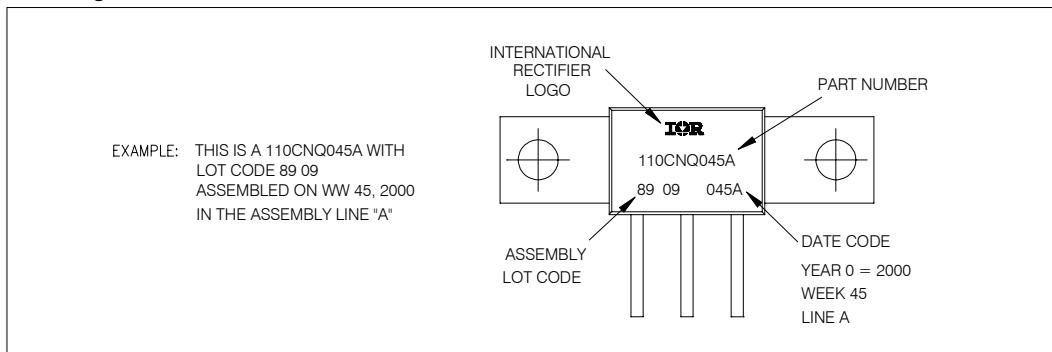
Outline Table



Outline Table



Marking Information



Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

International
IR Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7309
Visit us at www.irf.com for sales contact information. 09/01