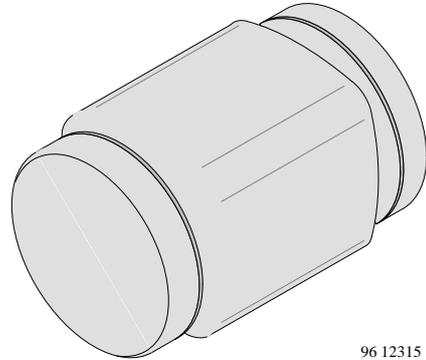TEMIC
Semiconductors

MCL4148.MCL4448

Silicon Epitaxial Planar Diodes

Features

- Saving space
- Hermetic sealed parts
- Fits onto SOD 323 / SOT 23 footprints
- Electrical data identical with the devices 1N4148 and 1N4448 respectively
- Micro Melf package

Applications

Extreme fast switches

96 12315

Absolute Maximum Ratings

$T_j = 25°C$

Parameter	Test Conditions	Type	Symbol	Value	Unit
Repetitive peak reverse voltage			V_{RRM}	100	V
Reverse voltage			V_R	75	V
Peak forward surge current	$t_p=1\mu s$		I_{FSM}	2	A
Repetitive peak forward current			I_{FRM}	450	mA
Forward current			I_F	200	mA
Average forward current	$V_R=0$		I_{FAV}	150	mA
Power dissipation			P_V	500	mW
Junction temperature			T_j	175	°C
Storage temperature range			T_{stg}	−65...+175	°C

Maximum Thermal Resistance

$T_j = 25°C$

Parameter	Test Conditions	Symbol	Value	Unit
Junction ambient	mounted on epoxy–glass hard issue, Fig. 1, 35μm copper clad, 0.9 mm^2 copper area per electrode	R_{thJA}	500	K/W

Characteristics

$T_j = 25°C$

Parameter	Test Conditions	Type	Symbol	Min	Typ	Max	Unit
Forward voltage	I_F=5mA	MCL4448	V_F	0.62		0.72	V
	I_F=50mA	MCL4148	V_F		0.86	1	V
	I_F=100mA	MCL4448	V_F		0.93	1	V
Reverse current	V_R=20V		I_R			25	nA
	V_R=20V, T_j=150°C		I_R			50	µA
	V_R=75V		I_R			5	µA
Breakdown voltage	I_R=100µA, t_p/T=0.01, t_p=0.3ms		$V_{(BR)}$	100			V
Diode capacitance	V_R=0, f=1MHz, V_{HF}=50mV		C_D			4	pF
Rectification efficiency	V_{HF}=2V, f=100MHz		η_r	45			%
Reverse recovery time	I_F=I_R=10mA, i_R=1mA		t_{rr}			8	ns
	I_F=10mA, V_R=6V, i_R=0.1xI_R, R_L=100Ω		t_{rr}			4	ns

Typical Characteristics ($T_j = 25°C$ unless otherwise specified)
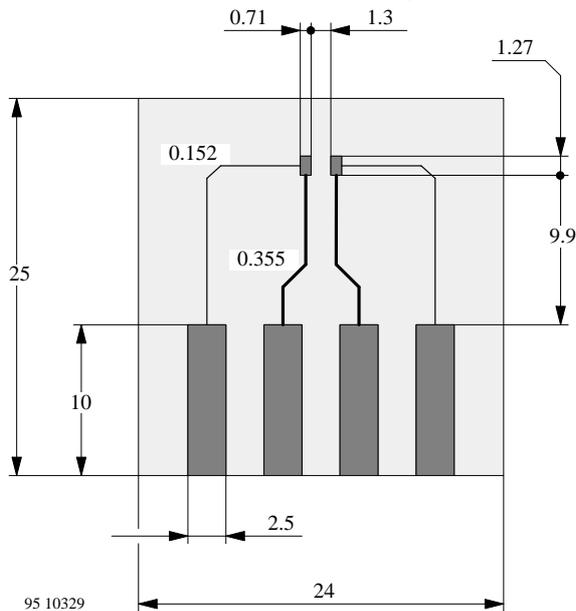
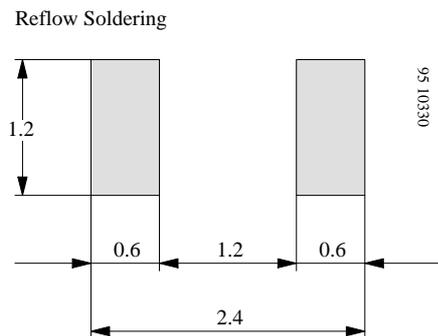Figure 1. Board for R_{thJA} definition (in mm)
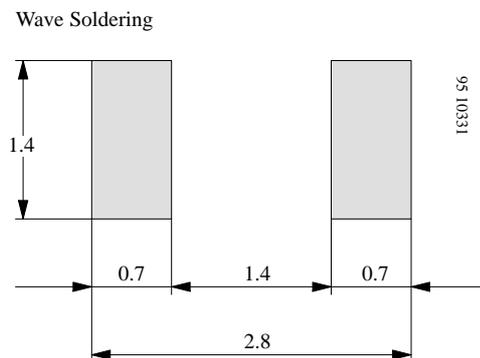
95 10329

Figure 2. Recommended foot pads (in mm)

Reflow Soldering

95 10330

Figure 3. Recommended foot pads (in mm)

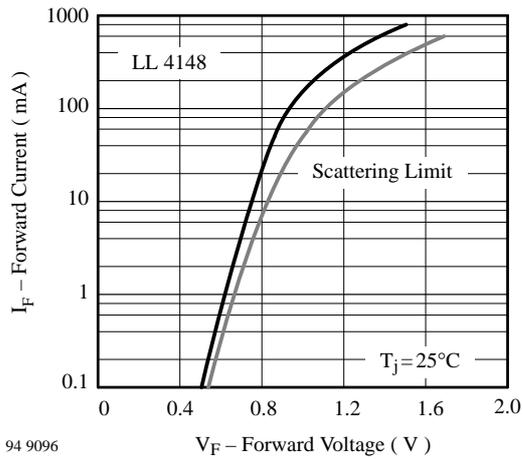Wave Soldering

95 10331

94 9096

Figure 4. Forward Current vs. Forward Voltage
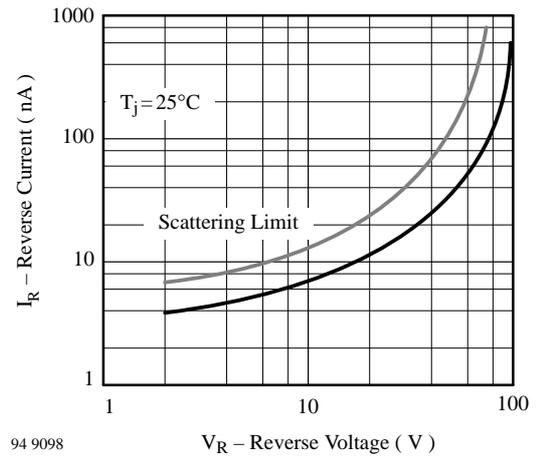

94 9098

Figure 6. Reverse Current vs. Reverse Voltage

94 9097
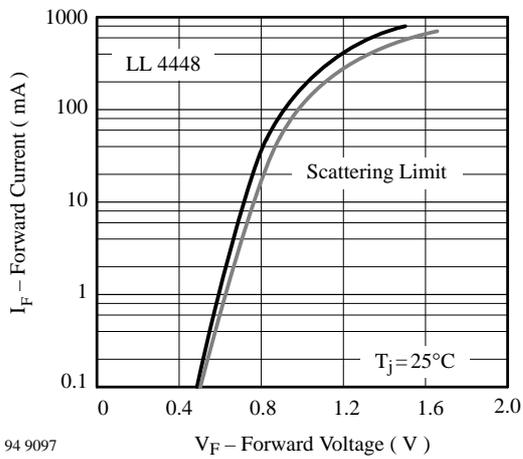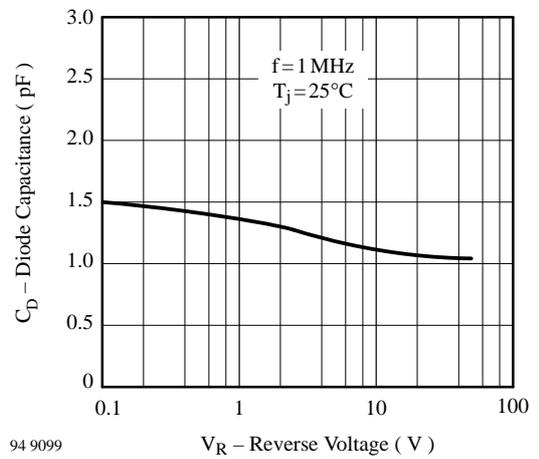
Figure 5. Forward Current vs. Forward Voltage

94 9099

Figure 7. Diode Capacitance vs. Reverse Voltage

Dimensions in mm

Cathode indification

Ø1.25 −0.05

1.9 ±0.1

Glass case
Micro MELF

1.35
Glass

R≥2.5
Glass

96 12072

technical drawings
according to DIN
specifications
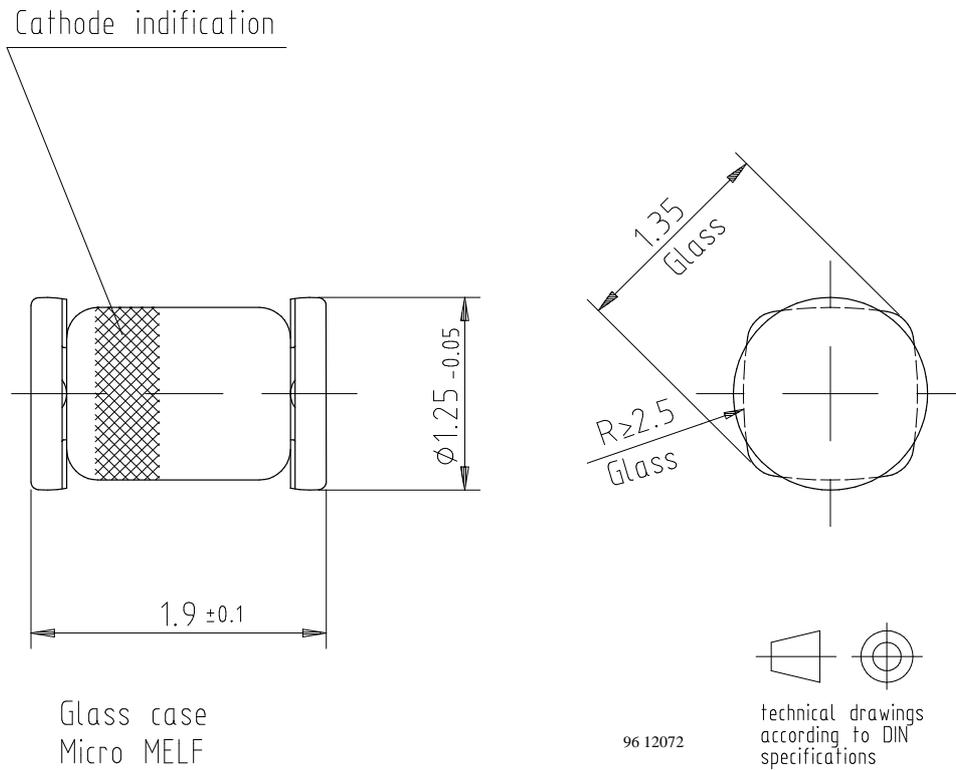
Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

1. Meet all present and future national and international statutory requirements.

2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively

2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA

3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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