

HIGH EFFICIENCY FAST RECOVERY RECTIFIER DIODES

FEATURES

- SUITED FOR SMPS
- VERY LOW FORWARD LOSSES
- NEGLIGIBLE SWITCHING LOSSES
- HIGH SURGE CURRENT CAPABILITY
- HIGH AVALANCHE ENERGY CAPABILITY
- SURFACE MOUNT DEVICE

DESCRIPTION

Single chip rectifier suited for Switch Mode Power Supply and high frequency DC to DC converters. Packaged in SOD6, this surface mount device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications.



SOD6
(Plastic)

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
$I_{F(RMS)}$	RMS forward current	10	A
$I_{F(AV)}$	Average forward current	2	A
I_{FSM}	Non repetitive surge peak forward current	$t_p=10\text{ms}$ sinusoidal	A
T_{stg} T_j	Storage and junction temperature range	- 40 to + 150 - 40 to + 150	°C °C

Symbol	Parameter	SMBYW02-				Unit
		50	100	150	200	
V_{RRM}	Repetitive peak reverse voltage	50	100	150	200	V

THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
$R_{th} (j-l)$	Junction-leads	25	°C/W

ELECTRICAL CHARACTERISTICS
STATIC CHARACTERISTICS

Symbol	Test Conditions		Min.	Typ.	Max.	Unit
V_F *	$T_j = 25^\circ C$	$I_F = 6 A$			1.25	V
	$T_j = 100^\circ C$	$I_F = 2 A$			0.85	
I_R **	$T_j = 25^\circ C$	$V_R = V_{RRM}$			20	μA
	$T_j = 100^\circ C$				0.5	mA

Pulse test : * $t_p = 380 \mu s$, duty cycle < 2 %** $t_p = 5 ms$, duty cycle < 2 %**RECOVERY CHARACTERISTICS**

Symbol	Test Conditions		Min.	Typ.	Max.	Unit
trr	$T_j = 25^\circ C$	$I_F = 1A$ $V_R = 30V$			35	ns
tfr	$T_j = 25^\circ C$	$I_F = 1A$ $V_{FR} = 1.1 \times V_F$		30		ns
V_{FP}	$T_j = 25^\circ C$	$I_F = 1A$	$tr = 10 ns$		5	V

To evaluate the conduction losses use the following equation :

$$P = 0.7 \times I_{F(AV)} + 0.075 \times I_{F(RMS)}$$

Voltage (V)	50	100	150	200
Marking	A05	A10	A15	A20

Laser marking

Logo indicates cathode

Fig.1 : Low frequency power losses versus average current.

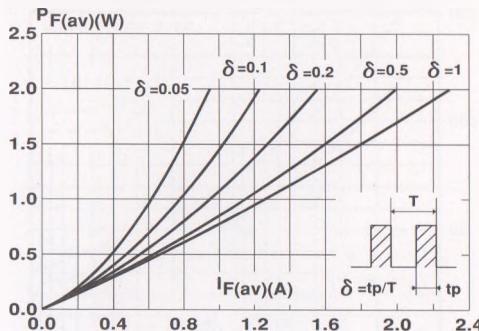


Fig.3 : Non repetitive surge peak forward current versus overload duration.

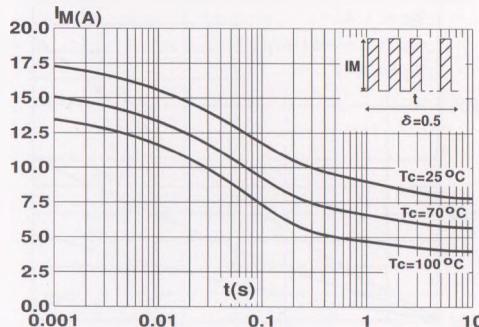


Fig.5 : Voltage drop versus forward current. (Maximum values)

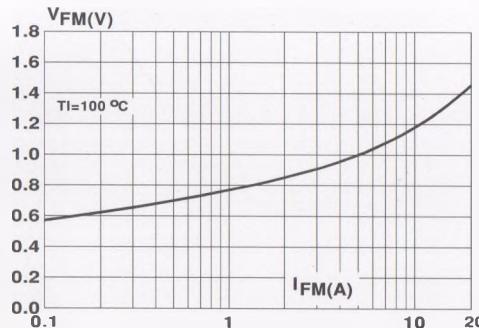


Fig.2 : Peak current versus form factor.

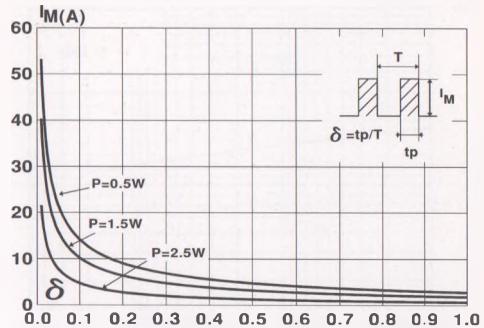


Fig.4 : Relative variation of thermal impedance junction to lead versus pulse duration.

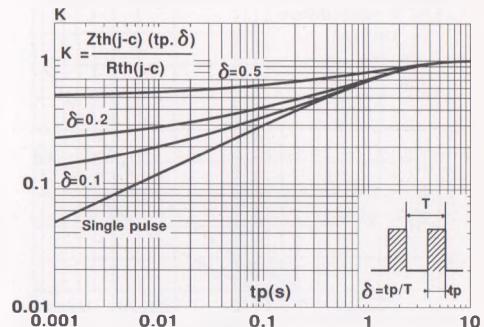


Fig.6 : Average current versus ambient temperature. (duty cycle : 0.5)

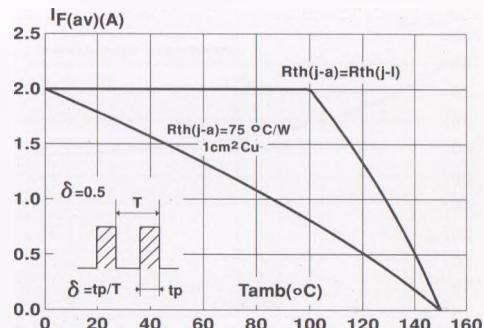


Fig.7 : Capacitance versus reverse voltage applied.

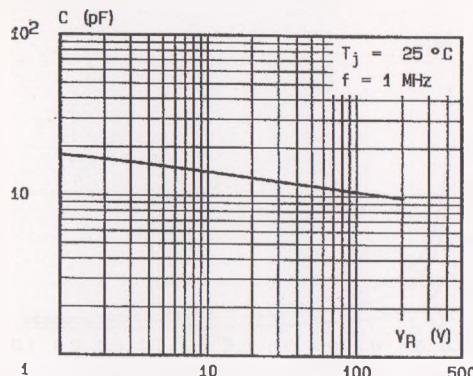


Fig.9 : Peak reverse current versus dI_F/dt.

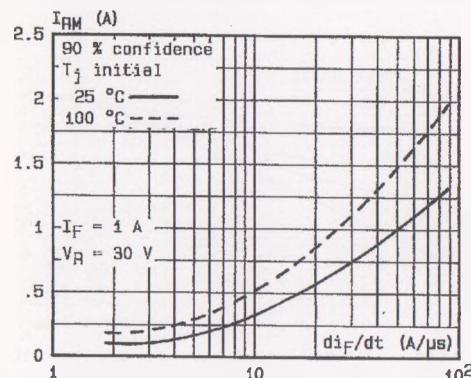


Fig.11 : Thermal resistance junction to ambient versus copper surface under each lead.

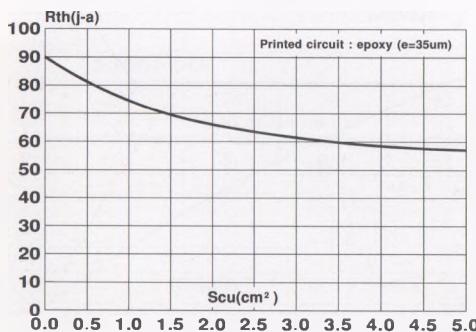


Fig.8 : Recovery time versus dI_F/dt.

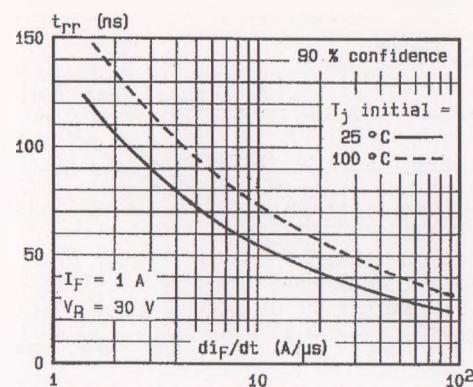


Fig.10 : Dynamic parameters versus junction temperature.

