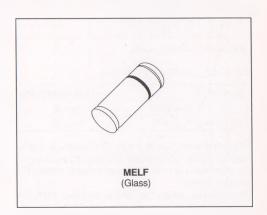
TMBYV 10-60

SMALL SIGNAL SCHOTTKY DIODE



DESCRIPTION

Metal to silicon rectifier diode in glass case featuring very low forward voltage drop and fast recovery time, intended for low voltage switching mode power supply, polarity protection and high frequency circuits.

ABSOLUTE RATINGS (limiting values)

Symbol	Parameter	Value	Unit	
V _{RRM}	Repetitive Peak Reverse Voltage	60	V	
I _{F(AV)}	Average Forward Current	T ₁ = 25°C	1	А
I _{FSM}	Surge non Repetitive Forward Current	$T_1 = 25$ °C $t_p = 10$ ms	20 Sinusoidal Pulse	A
		$T_1 = 25^{\circ}C$ $t_p = 300\mu s$	40 Rectangular Pulse	
T _{stg} T _i	Storage and Junction Temperature Range		- 65 to 150 - 65 to 125	∞ ℃
TL	Maximum Temperature for Soldering during 15s		260	°C

THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
R _{th (j-l)}	Junction-leads	110	°C/W

ELECTRICAL CHARACTERISTICS

STATIC CHARACTERISTICS

Symbol	Test Conditions		Min.	Тур.	Max.	Unit
l _A *	T ₁ = 25°C	$V_{R} = V_{RRM}$			0.5	mA
	T _j = 100°C				10	
V _F *	IF = 1A	$T_j = 25^{\circ}C$			0.7	V
	I _F = 3A				1	

Pulse test: t_o ≤ 300µs δ < 2%.

DYNAMIC CHARACTERISTICS

Symbol	Test Conditions		Тур.	Max.	Unit
С	$T_j = 25^{\circ}C$ $V_R = 0$		150		ρF
	$T_1 = 25^{\circ}C$ $V_R = 5V$		40		

Forward current flow in a schottky rectifier is due to majority carrier conduction. So reverse recovery is not affected by stored charge as in conventional PN iunction diodes.

Nevertheless, when the device switches from forward biased condition to reverse blocking state, current is required to charge the depletion capacitance of the diode.

This current depends only of diode capacitance and external circuit impedance. Satisfactory circuit behaviour analysis may be performed assuming that schottky rectifier consits of an ideal diode in parallel with a variable capacitance equal to the junction capacitance (see fig. 5 page 4/4).

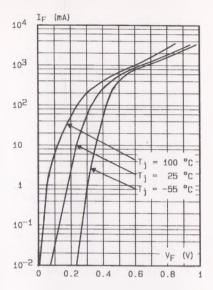


Fig.1 - Forward current versus forward voltage at low level (typical values).

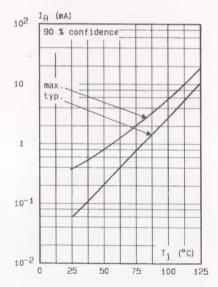


Fig.3 - Reverse current versus junction temperature.

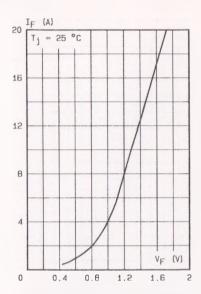


Fig.2 - Forward current versus forward voltage at high level (typical values).

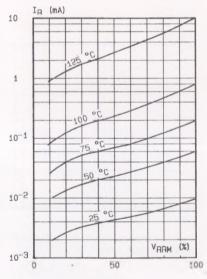


Fig.4 – Reverse current versus $V_{\mbox{\footnotesize{RRM}}}$ in per cent.

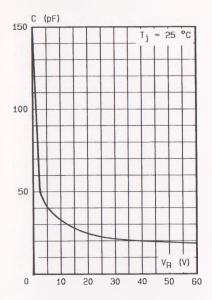
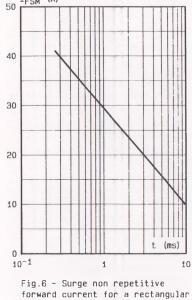


Fig.5 - Capacitance C versus reverse applied voltage $V_{\mbox{\scriptsize R}}$ (typical values).



IFSM (A)

pulse with t \leq 10 ms.

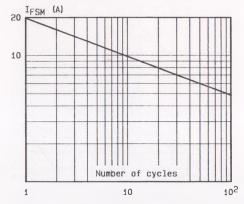


Fig.7 - Surge non repetitive forward current versus number of cycles.

