XL7900

# VIBRATING CAPACITOR

Vibrating membrane capacitor in evacuated envelope to be driven by a high-frequency electric field.

Application: D.C. to A.C. converter, e.g. in dosemeters, pH meters and electrometer equipment, where a very high input resistance is of paramount importance.

Equipment measuring currents of 500 electrons per second have been realised.

QUICK REFERENCE DATA		
Contact potential	-50 to +50	mV
Short term drift of contact potential	< 100	
Insulation	> 10 <sup>15</sup>	Ω
Outline dimensions:		
overall length	max. 64.7	mm
di am <b>eter</b>	max. 30.2	mm

#### MECHANICAL DATA

Base: Magnoval, gold plated pins

C<sub>m</sub> = measuring capacitor

Cdr = driving capacitor

Operating position: any





Dimensions in mm



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PRINCIPLE OF OPERATION



The D.C. voltage to be measured is connected to capacitor  $C_m$ . The earthed membrane vibrates in its own resonance frequency as a result of an H.F. electrical field between the electrodes of capacitor  $C_{dr}$ . So the D.C. voltage on capacitor  $C_m$  is modulated in the resonance frequency of the membrane. Capacitor  $C_1$  insulates the D.C. source from the A.C. amplifier.

#### LIMITING VALUES (Absolute max. rating system)

D.C. voltage on $C_m$	max. 25	V
Conversion efficiency		
R.M.S. output voltage	max. 40	$\sigma$ h
D.C. input voltage	max. 40	70 - <b>)</b>

#### ELECTRICAL DATA

Contact potential over C <sub>m</sub>	-50 to $+50$	mV
Short term drift (within 1 day)	0.1	mV
Long term drift (within 1 month)	1	mV
Temperature dependance	20	µV/ºC

Conversion efficiency:

At a certain driving voltage the conversion efficiency will show a max. spread of  $\pm60\%$  (1:4)

Above 40 % it is possible that two capacitor plates will touch each other and will be damaged.

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## ELECTRICAL DATA (continued)

Driving voltage:

There can always be found a value of the H.F. driving voltage at which all capacitors have a conversion efficiency between 10% and 40%.<sup>1</sup>)

Insulation resistance between any two capacitor terminals	> 10 <sup>15</sup>	Ω <sup>2</sup> )
Resonance frequency of the membrane	5.3 to 6.3	kHz
Drift	1.5	%
Temperature dependance	± 1	Hz/ºC
Capacitances of C <sub>m</sub> and C <sub>dr</sub>	35	pF
Temperature dependance between -10 and +60 °C	$\Delta C$ 1	pF

## SHOCK AND VIBRATION RESISTANCE

The following test conditions are applied to assess the mechanical quality of the tube. These conditions are not intended to be used as normal operating conditions.

Shock

The tube is subjected 5 times in each of 4 positions to an acceleration of 500 g supplied by an NRL shock machine with the hammer lifted over an angle of  $30^{\circ}$ .

Vibration

The tube is subjected to a vibration of 15 to 1500 Hz with an acceleration of 2.5 g.

#### APPLICATION NOTES

The capacitive drive opens the possibility to use as driving signal for the membrane a high frequency signal amplitude-modulated with the resonance frequency of the vibrating membrane.

Since in that case there is a great difference between the frequency of the driving signal and the modulation frequency of the voltage to be measured, the stray influences of the driving signal can easily be kept away from the measuring amplifier. In addition, a high frequency drive simplifies design and execution of the driving oscillator.

2) Under standard atmospheric conditions as defined in I.E.C. publication 68-1, i.e. any combination of temperature, humidity and pressure within the following limits:

Temperature	+15 to +35	°C
Relative humidity	45 to 75	%
Air pressure	860 to 1060	mbar



<sup>&</sup>lt;sup>1</sup>) For instance in an apparatus realised with the circuit shown in Fig.2, it turned out that all capacitors have a conversion efficiency between 10 and 40% at a voltage over L<sub>1</sub> of 1 VRMS.

## EXAMPLE OF A DRIVING OSCILLATOR

### Operating principle



Fig.1

The driving capacitor (Cdr) is incorporated in an impedance bridge that determines the feedback to the amplifier transistor. Capacitance Cl has been given a slightly larger value than that of capacitor Cdr in its quiescent state. Due to this the fed-back A.C. voltage Vl has the proper phase and amplitude to cause the circuit to oscillate in a frequency that is mainly determined by the circuit  $L_2$  Cl Cdr.

The electric attractive force between the capacitor plates of  $C_{\rm dr}$  makes the membrane move towards the fixed plate of  $C_{\rm dr}$  as a result of which its capacitance increases, the transistor receives less feedback and the oscillator voltage decreases.

The phases and amplitudes of the electrical and the mechanical forces on the membrane and of the feedback factor are such that the membrane begins to vibrate in its resonance frequency, while the H.F. voltage is modulated in amplitude with this frequency.

Since it is very difficult to realize this circuit in such a way that a stable operation is ensured, it is advisable to add some components for automatical adjustment of the capacitance  $C_1$ .

See the following circuit.

# EXAMPLE OF A DRIVING OSCILLATOR (continued)

# Practical circuit



Fig.2

$C_1 = 12 \text{ pF mica}$	$R_1 = 68 k\Omega$	$L_2 = 1.3 \text{ mH}$
C <sub>2</sub> = 1500 pF	$R_2 = 3.3 \text{ k}\Omega$	$L_3 = 1.3 \text{ mH R.F. choke}$
C <sub>3</sub> = 10 pF mica	$R_3 = 4.7 \ k\Omega$	$N_2/N_1 = 20$
C <sub>4</sub> = 2200 pF	$R_4 = 1 k\Omega$	TR = BCY70
$C_5 = 0.1  \mu F$	$R_5 = 1 M\Omega$	D = BA102
$C_t = 25 \text{ pF max}.$		

# PHILIPS

# Data handbook



Electronic components and materials

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