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JEDEC Type: 2N2757, 2758, 2759, 2760, 2761, 2762,
2N2763, 2764, 2765, 2766, 2767, 2768,
2N2769, 2770, 2771, 2772, 2773, 2774,
2N2775, 2776, 2777, 2778, 2779, 2780

JOINT ELECTRON DEVICE ENGINEERING COUNCIL
REGISTRATION DATA
SILICON POWER TRANSISTOR
POWER SWITCHING

I. General Description

This transistor is a NPN silicon power transistor designed primarily for high power switching applications and inverters at or below the collector current given in Table I. It is intended for Industrial and Military class of service.

II. Mechanical Data

A. The transistor has an outline as per Figure 1

B. Terminal location is shown in Figure 1

C. Maximum Stud Torque 40 in. lbs. Non Lubricated.
Minimum Stud Torque 30 in. lbs. Non Lubricated.

III. Maximum Ratings

A. Temperature

1. Storage Temperature Range T_{stg} -65°C to +175°C
2. Operating Case Temperature Range T_C -65°C to +175°C

B. Voltage

1. V_{CEX} See Table I
2. V_{CBO} See Table I
3. V_{EBO} 15V D.C.
4. V_{CEO} See Table I (Same as $V_{CEO(sus)}$)

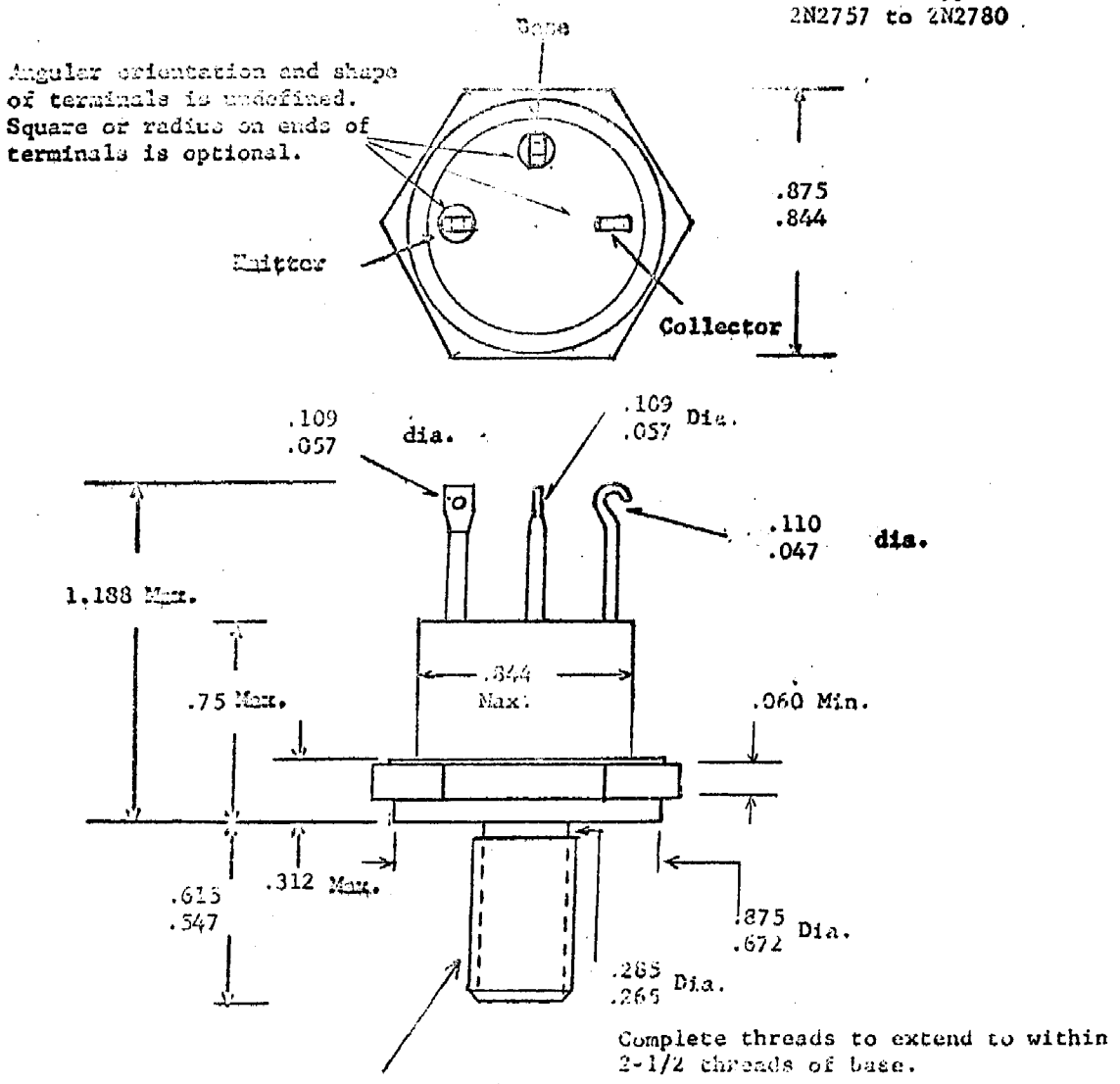
C. Current

1. Continuous Collector Current - 30 a.
2. Continuous Base Current 7.5 Amps.

D. 1. Maximum Continuous Power Dissipation - 200 Watts at a T_C of 75°C Max.

2. Linear Derating Factor 2.0W/°C

JEDEC Type
2N2757 to 2N2780



5/16-24 UNF-2A Plated Thread

.2854/.2806 Pitch Dia.

Ref. Screw Thread Standards for Federal Services Handbook H28, 1957 Pl