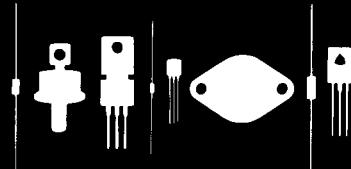


Central Semiconductor Corp.
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145 Adams Avenue
Hauppauge, New York 11788



2N4036
2N4037

SILICON PNP TRANSISTOR

JEDEC TO-39 CASE

DESCRIPTION

The CENTRAL SEMICONDUCTOR 2N4036, 2N4037 are Silicon PNP Epitaxial Planar Transistors designed for small signal and medium power general purpose industrial applications.

MAXIMUM RATINGS ($T_C=25^\circ\text{C}$ unless otherwise noted)

	<u>SYMBOL</u>	<u>2N4036</u>	<u>2N4037</u>	<u>UNIT</u>
Collector-Base Voltage	V_{CBO}	90	60	V
Emitter-Base Voltage	V_{EBO}	7.0	7.0	V
Collector-Emitter Voltage	V_{CEO}	65	40	V
Collector-Emitter Voltage	V_{CEV}, V_{CER}	85	60	V
Collector Current, Continuous	I_C	1.0	1.0	A
Base Current	I_B	0.5	0.5	A
Power Dissipation	P_D	7.0	7.0	W
Power Dissipation ($T_A=25^\circ\text{C}$)	P_D	1.0	1.0	W
Operating and Storage	T_J, T_{Stg}	-65 TO +200		$^\circ\text{C}$
Junction Temperature				

ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$)

<u>SYMBOL</u>	<u>TEST CONDITIONS</u>	<u>2N4036</u>		<u>2N4037</u>		<u>UNIT</u>
		<u>MIN</u>	<u>MAX</u>	<u>MIN</u>	<u>MAX</u>	
I_{CBO}	$V_{CB}=60\text{V}$			0.02	0.25	μA
I_{EBO}	$V_{EB}=5\text{V}$			0.02	1.0	μA
I_{CEO}	$V_{CE}=30\text{V}$			0.5	5.0	μA
BV_{CBO}	$I_C=0.1\text{mA}$	90		60		V
BV_{EBO}	$I_E=0.1\text{mA}$		7.0		7.0	V
BV_{CEO}	$I_C=100\text{mA}$	65		40		V
BV_{CER}	$I_C=100\text{mA}, R_{BE}=200 \text{ OHMS}$	85		60		V
BV_{CEV}	$V_{BE}=1.5\text{V}, I_C=100\text{mA}$	85		60		V
$V_{CE(s)}$	$I_C=150\text{mA}, I_B=15\text{mA}$			0.65	1.4	V
$V_{BE(on)}$	$V_{CE}=10\text{V}, I_C=150\text{mA}$			1.1	1.5	V
h_{FE}	$V_{CE}=10\text{V}, I_C=0.1\text{mA}$	20		--		--
h_{FE}	$V_{CE}=10\text{V}, I_C=1.0\text{mA}$	--		15		--
h_{FE}	$V_{CE}=10\text{V}, I_C=150\text{mA}$	40	140	50	250	--
h_{FE}	$V_{CE}=10\text{V}, I_C=500\text{mA}$	20		--		--
f_T	$V_{CE}=10\text{V}, I_C=50\text{mA}, f=20 \text{ MHz}$	60		60		MHz
C_{ob}	$V_{CB}=10\text{V}, I_E=0$			30	30	pF
t_{on}	$V_{CE}=30\text{V}, I_C=150\text{mA}, I_B1=15\text{mA}$			110	--	nSEC
t_{off}	$V_{CE}=30\text{V}, I_C=150\text{mA}, I_B2=15\text{mA}$			700	--	nSEC