2SC4453



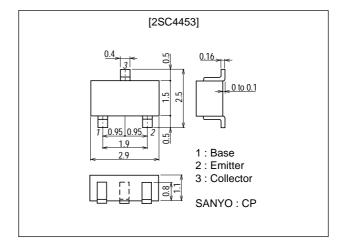
# **High-Speed Switching Applications**

#### **Features**

- · Fast switching speed.
- · Low collector saturation voltage.
- · High gain-bandwidth product.
- · Small collector capacity.
- Ultrasmall-sized package permitting the 2SC4453applied sets to be made small and slim.

#### **Package Dimensions**

unit : mm 2018B



### **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		40	V
Collector-to-Emitter Voltage	VCES		40	V
Collector-to-Emitter Voltage	VCEO		15	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		200	mA
Collector Current (Pulse)	ICP		500	mA
Base Current	IB		40	mA
Collector Dissipation	PC		200	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Syllibol		min	typ	max	J Oill
Collector Cutoff Current	ICBO	V <sub>CB</sub> =20V, I <sub>E</sub> =0			0.1	μΑ
Emitter Cutoff Current	IEBO	VEB=3V, IC=0			0.1	μΑ
DC Current Gain	hFE	V <sub>CE</sub> =1V, I <sub>C</sub> =10mA	50*	90	200*	

\*: The 2SC4453 is classified by 5mA hFE as follows:

Marking: ST

Rank 2 3 4
hFE 50 to 100 70 to 140 100 to 200

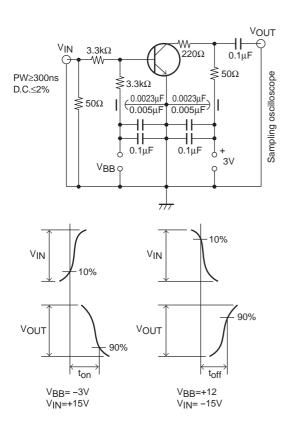
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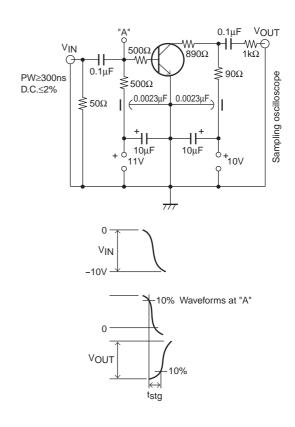
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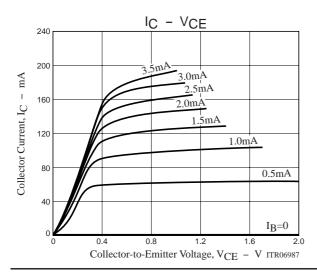
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Gain-Bandwidth Product	fT	V <sub>CE</sub> =10V, I <sub>C</sub> =10mA	450	750		MHz
Output Capacitance	Cob	V <sub>CB</sub> =5V, f=1MHz		1.4	4.0	pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=10mA, IB=1mA		0.13	0.25	V
Base-to-Emitter Saturation Voltage	V <sub>BE</sub> (sat)	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA		0.80	0.85	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I <sub>C</sub> =10μA, I <sub>E</sub> =0	40			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	15			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I <sub>E</sub> =10μA, I <sub>C</sub> =0	5			V
Turn ON Time	ton	See specified Test Circuit.		8.0		ns
Storage Time	tstg	See specified Test Circuit.		6.0		ns
Fall Time	tf	See specified Test Circuit.		12		ns

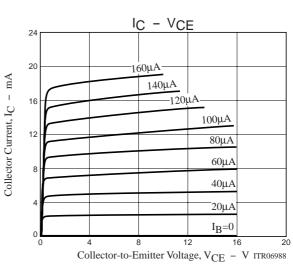
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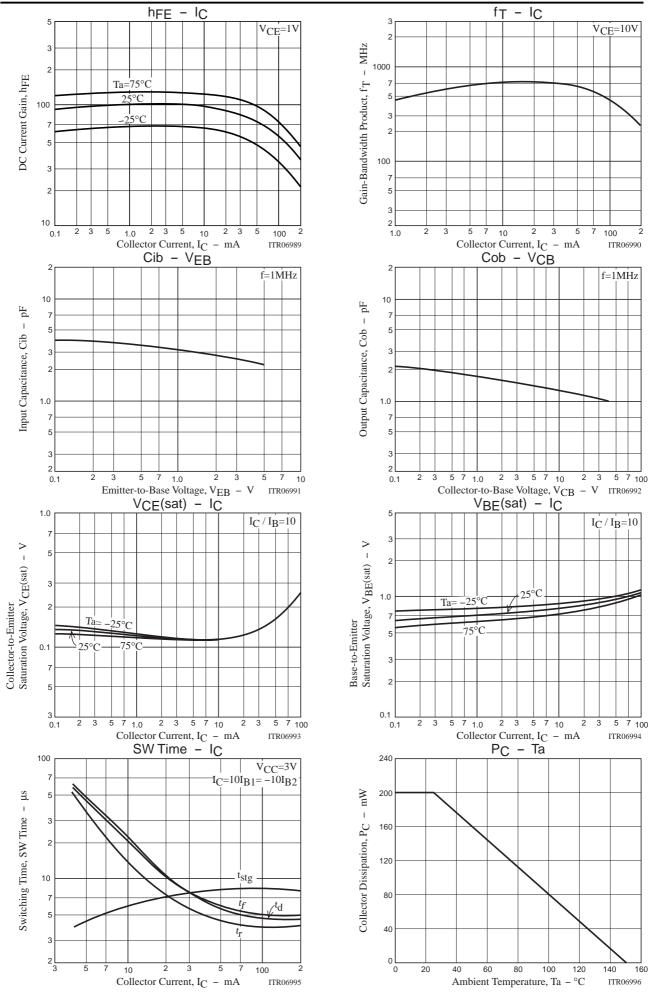
#### tstg Test Circuit











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