



# **Ultrahigh-Speed Switching Applications**

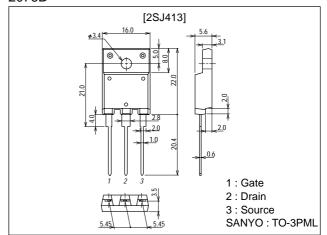
### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.
- · Micaless package facilitating mounting.

## **Package Dimensions**

unit:mm

2076B



# **Specifications**

## **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	I <sub>D</sub>		-50	А
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	-200	Α
Allowable Power Dissipation	D-		3.0	W
	PD	Tc=25°C	70	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Drain-to-Source Breakdown Voltage	V <sub>(BR)</sub> DSS	$I_D=-1$ mA, $V_{GS}=0$	-60			V
Gate-to-Source Breakdown Voltage	V <sub>(BR)</sub> GSS	$I_{G}=\pm 100 \mu A, V_{DS}=0$	±20			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0			-100	μA
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0		-2.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-25A	27	45		S
Static Drain-to-Source On-State Resistance	R <sub>DS(on)</sub> 1	I <sub>D</sub> =-25A, V <sub>GS</sub> =-10V		15	20	mΩ
	R <sub>DS(on)</sub> 2	I <sub>D</sub> =-25A, V <sub>GS</sub> =-4V		20	30	mΩ

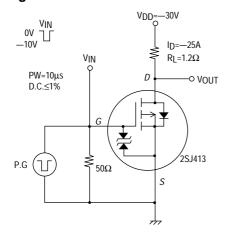
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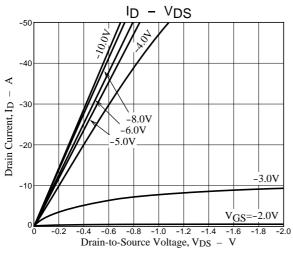
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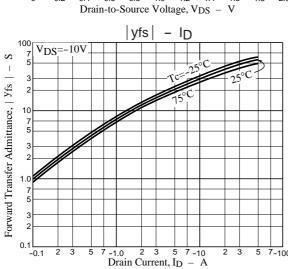
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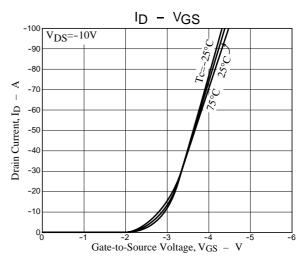
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	] Oill
Input Capacitance	Ciss	V <sub>DS</sub> =-20V, f=1MHz		7600		pF
Output Capacitance	Coss	V <sub>DS</sub> =-20V, f=1MHz		2400		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-20V, f=1MHz		600		pF
Turn-ON Delay Time	<sup>t</sup> d(on)	See specified Test Circuit		60		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		250		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		900		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		350		ns
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =-50A, V <sub>GS</sub> =0		-1.0	-1.5	V

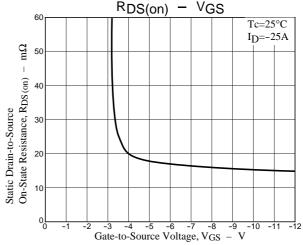
## **Switching Time Test Circuit**

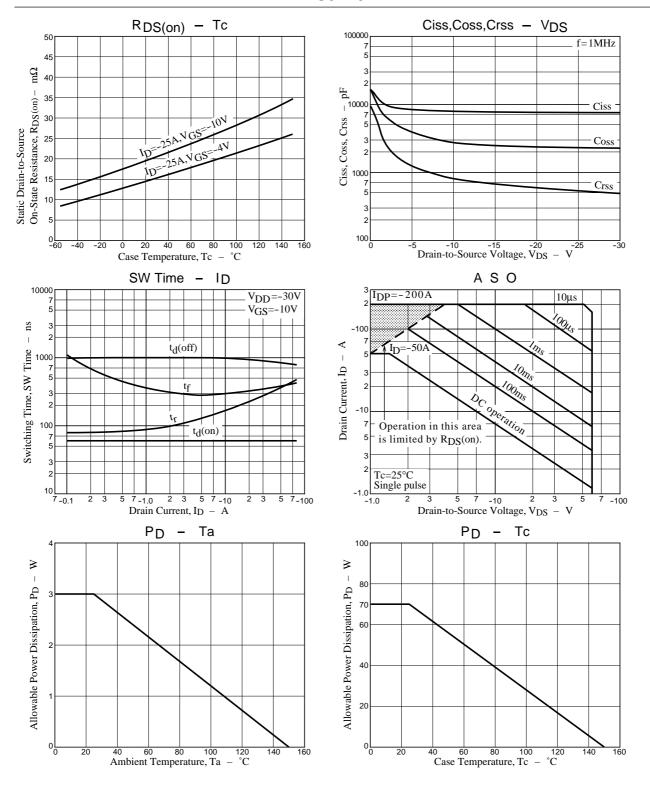












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