Silicon N-Channel MOS FET

HITACHI

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1315	V _{DSS}	450	V
	2SK1316		500	
Gate to source voltage		V _{GSS}	±30	V
Drain current		I _D	8	А
Drain peak current		L *1 D(pulse)	32	А
Body to drain diode reverse drain current		I _{DR}	8	А
Channel dissipation		Pch*2	60	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW 10 µs, duty cycle 1%

2. Value at $T_c = 25^{\circ}C$

Electrical Characteristics (Ta = 25°C)

breakdown voltage 2 Gate to source breakdovoltage Gate to source leak cur Zero gate voltage 2 drain current 2 Gate to source cutoff vo Static Drain to source 2	rrent 2SK1315	V _{(BR)DSS} V _{(BR)GSS}	450 500 ±30		—	V	$I_{\rm D}$ = 10 mA, $V_{\rm GS}$ = 0
Gate to source breakdovoltage Gate to source leak cur Zero gate voltage drain current Gate to source cutoff vo Static Drain to source 2	own rrent 2SK1315			- 			
voltageGate to source leak curZero gate voltagedrain currentZero gate to source cutoff voltageGate to source cutoff voltageStatic Drain to source 2	rrent 2SK1315		±30	_			
Zero gate voltage2drain current2Gate to source cutoff voStatic Drain to source2	2SK1315	I _{GSS}			_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
drain current 2 Gate to source cutoff vo Static Drain to source 2		200		_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Gate to source cutoff vo Static Drain to source 2		I _{DSS}	_	_	250	μA	$V_{\rm DS} = 360$ V, $V_{\rm GS} = 0$
Static Drain to source	2SK1316						$V_{\rm DS} = 400 \text{ V}, V_{\rm GS} = 0$
-	oltage	$V_{GS(off)}$	2.0	—	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
on state resistance			_	0.55	0.7		$I_{\rm D} = 4$ A, $V_{\rm GS} = 10$ V * ¹
	2SK1316		_	0.60	0.8		
Forward transfer admitt	tance	yfs	4.5	7.5	—	S	$I_{D} = 4 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance		Ciss	_	1150	—	pF	$V_{\text{DS}} = 10 \text{ V}, V_{\text{GS}} = 0,$
Output capacitance		Coss	_	340	—	pF	f = 1 MHz
Reverse transfer capac	citance	Crss	_	55	_	pF	
Turn-on delay time		t _{d(on)}	_	17	—	ns	$I_{D} = 4 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time		t,	_	55	_	ns	R _L = 7.5
Turn-off delay time		$t_{d(off)}$	_	100	—	ns	
Fall time		t _f	_	45	_	ns	
Body to drain diode forv voltage	ward	V_{DF}	_	0.9	_	V	$I_{F} = 8 A, V_{GS} = 0$
Body to drain diode rev recovery time	IOREO	t _{rr}	_	350	_	ns	$I_{F} = 8 A, V_{GS} = 0,$

Note: 1. Pulse test

See characteristic curves of 2SK1159, 2SK1160.



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