## Silicon N-Channel MOS FET

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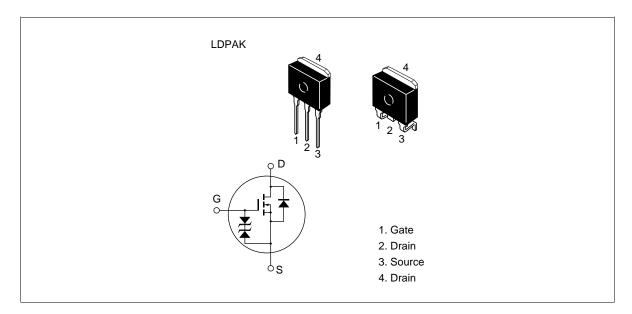
### Application

High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

### Outline





## Absolute Maximum Ratings (Ta = $25^{\circ}$ C)

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1315	V <sub>DSS</sub>	450	V
	2SK1316		500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		I <sub>D</sub>	8	А
Drain peak current		L *1 D(pulse)	32	А
Body to drain diode reverse drain current		I <sub>DR</sub>	8	А
Channel dissipation		Pch*2	60	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW 10 µs, duty cycle 1%

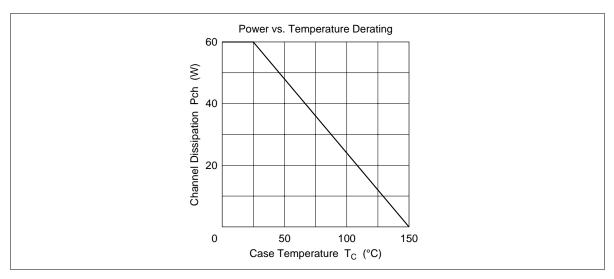
2. Value at  $T_c = 25^{\circ}C$ 

## **Electrical Characteristics** (Ta = 25°C)

breakdown voltage 2 Gate to source breakdovoltage Gate to source leak cur Zero gate voltage 2 drain current 2 Gate to source cutoff vo Static Drain to source 2	rrent 2SK1315	V <sub>(BR)DSS</sub> V <sub>(BR)GSS</sub>	450 500 ±30		—	V	$I_{\rm D}$ = 10 mA, $V_{\rm GS}$ = 0
Gate to source breakdovoltage Gate to source leak cur Zero gate voltage drain current Gate to source cutoff vo Static Drain to source 2	own rrent 2SK1315			- 			
voltageGate to source leak curZero gate voltagedrain currentZero gate to source cutoff voltageGate to source cutoff voltageStatic Drain to source 2	rrent 2SK1315		±30	_			
Zero gate voltage2drain current2Gate to source cutoff voStatic Drain to source2	2SK1315	I <sub>GSS</sub>			_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
drain current 2 Gate to source cutoff vo Static Drain to source 2		200		_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Gate to source cutoff vo Static Drain to source 2		I <sub>DSS</sub>	_	_	250	μA	$V_{\rm DS} = 360$ V, $V_{\rm GS} = 0$
Static Drain to source	2SK1316						$V_{\rm DS} = 400 \text{ V}, V_{\rm GS} = 0$
-	oltage	$V_{GS(off)}$	2.0	—	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
on state resistance			_	0.55	0.7		$I_{\rm D} = 4$ A, $V_{\rm GS} = 10$ V * <sup>1</sup>
	2SK1316		_	0.60	0.8		
Forward transfer admitt	tance	yfs	4.5	7.5	—	S	$I_{D} = 4 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance		Ciss	_	1150	—	pF	$V_{\text{DS}} = 10 \text{ V}, V_{\text{GS}} = 0,$
Output capacitance		Coss	_	340	—	pF	f = 1 MHz
Reverse transfer capac	citance	Crss	_	55	_	pF	
Turn-on delay time		t <sub>d(on)</sub>	_	17	—	ns	$I_{D} = 4 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time		t,	_	55	_	ns	R <sub>L</sub> = 7.5
Turn-off delay time		$t_{d(off)}$	_	100	—	ns	
Fall time		t <sub>f</sub>	_	45	_	ns	
Body to drain diode forv voltage	ward	$V_{\text{DF}}$	_	0.9	_	V	$I_{F} = 8 A, V_{GS} = 0$
Body to drain diode rev recovery time	IOREO	t <sub>rr</sub>	_	350	_	ns	$I_{F} = 8 A, V_{GS} = 0,$

Note: 1. Pulse test

See characteristic curves of 2SK1159, 2SK1160.



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#### Hitachi, Ltd.

Semiconductor & IC Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

#### For further information write to:

Hitachi America, Ltd. Semiconductor & IC Div. 2000 Sierra Point Parkway Brisbane, CA. 94005-1835 U S A Tel: 415-589-8300 Fax: 415-583-4207 Hitachi Europe GmbH Electronic Components Group Continental Europe Dornacher Straße 3 D-85622 Feldkirchen München Tel: 089-9 91 80-0 Fax: 089-9 29 30 00 Hitachi Europe Ltd. Electronic Components Div. Northern Europe Headquarters Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA United Kingdom Tel: 0628-585000 Fax: 0628-778322 Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 0104 Tel: 535-2100 Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd. Unit 706, North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: 27359218 Fax: 27306071

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