

2SK1338

Silicon N-Channel MOS FET

HITACHI

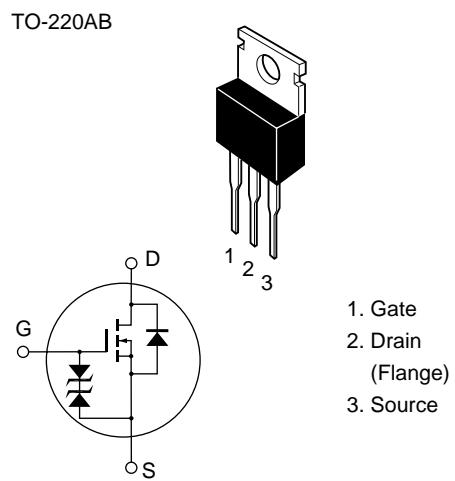
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	900	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	2	A
Drain peak current	I _{D(pulse)} ^{*1}	6	A
Body to drain diode reverse drain current	I _{DR}	2	A
Channel dissipation	Pch ^{*2}	50	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{tsg}	-55 to +150	°C

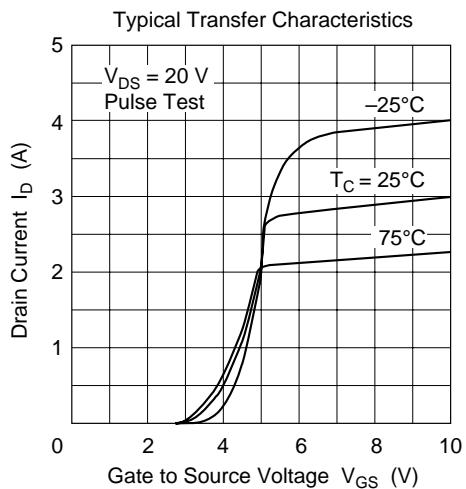
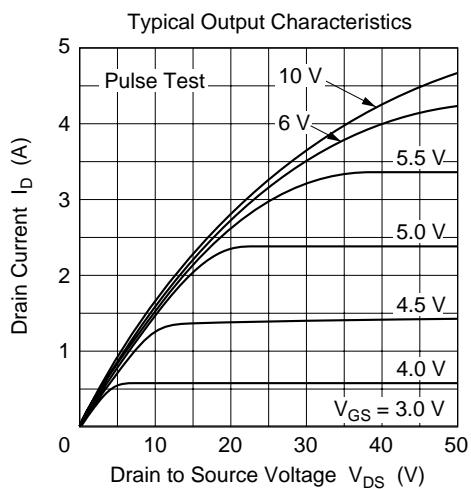
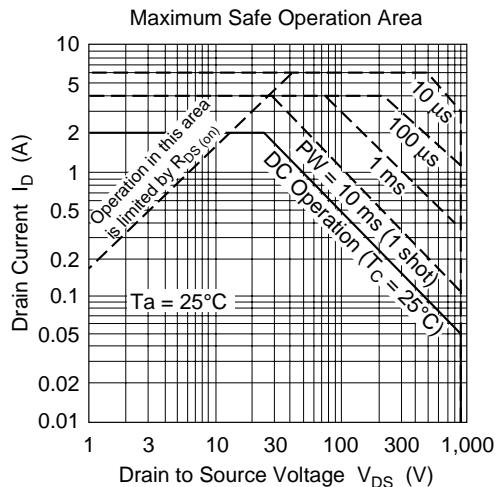
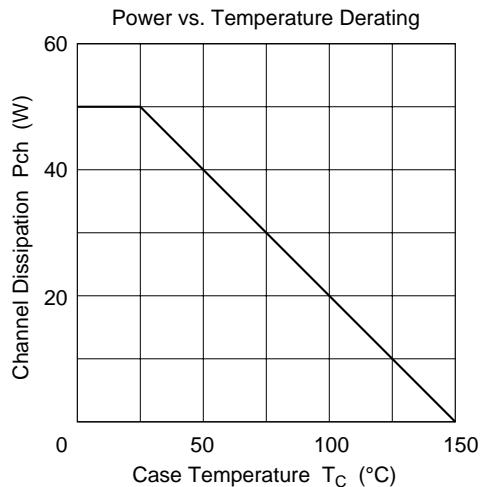
Notes: 1. PW 10 µs, duty cycle 1%

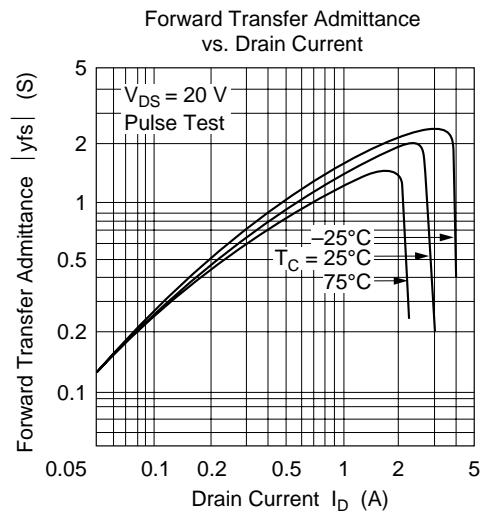
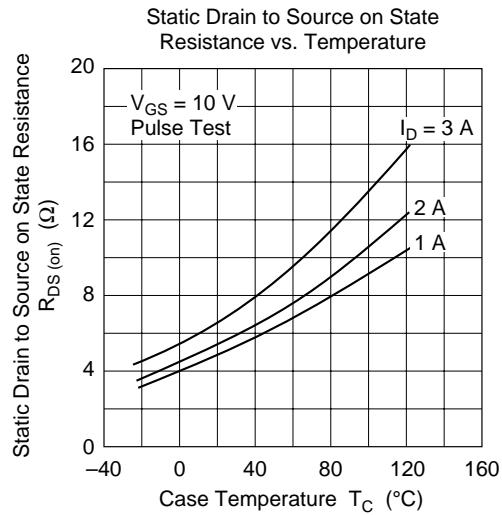
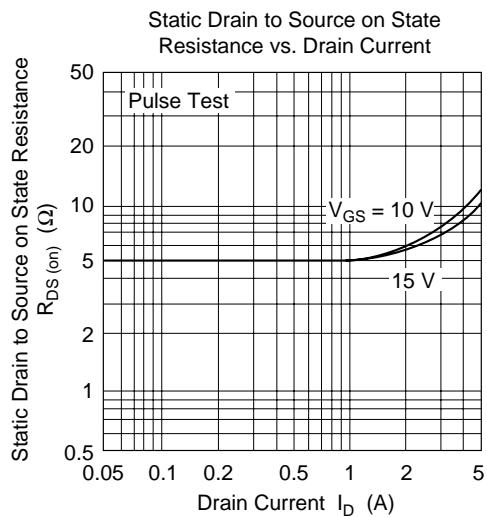
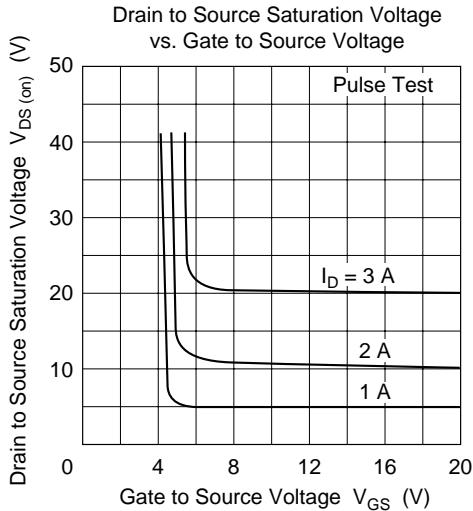
2. Value at T_C = 25°C

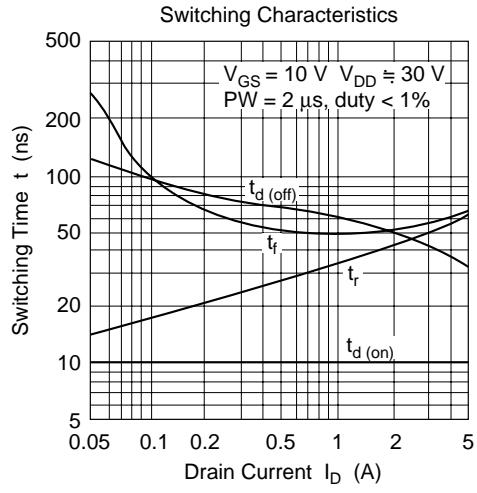
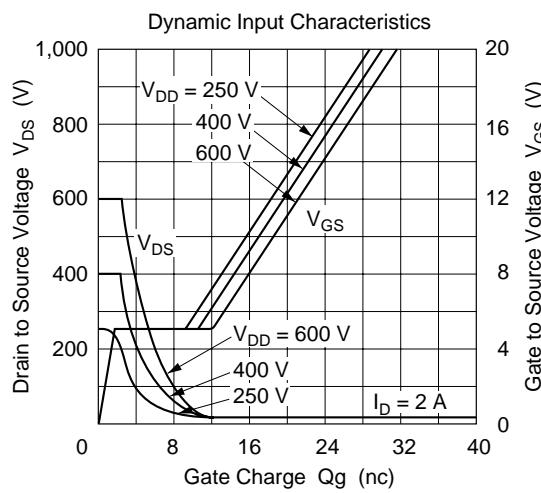
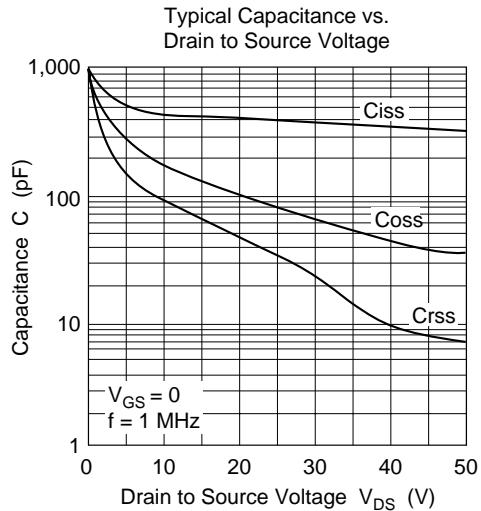
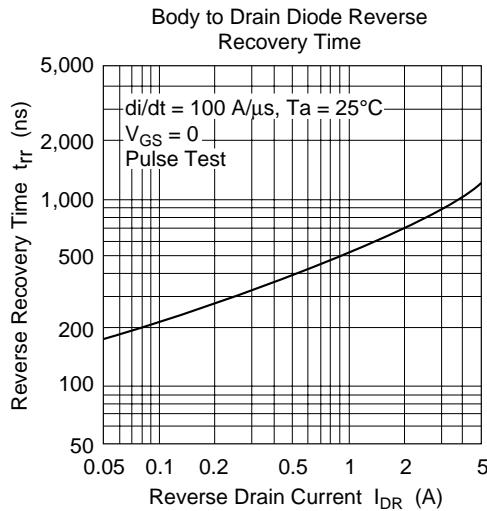
Electrical Characteristics (Ta = 25°C)

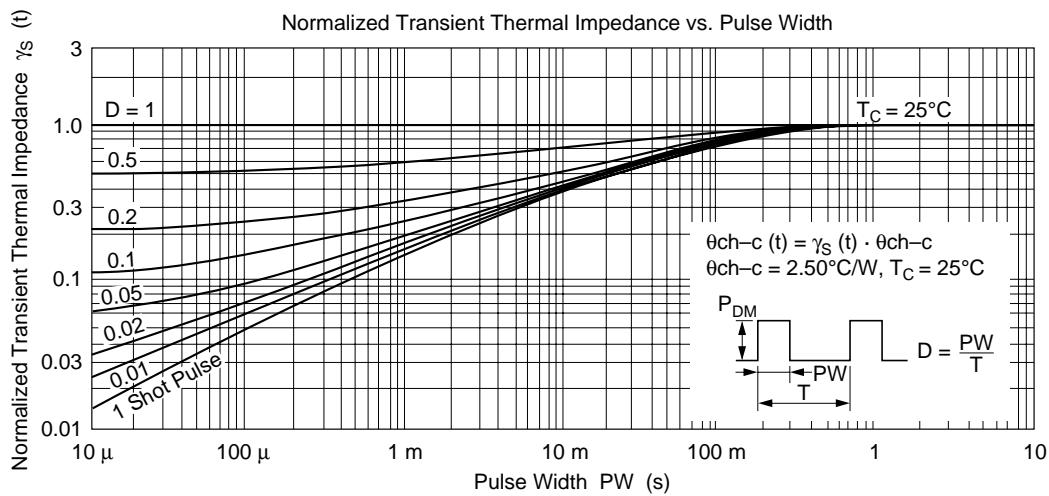
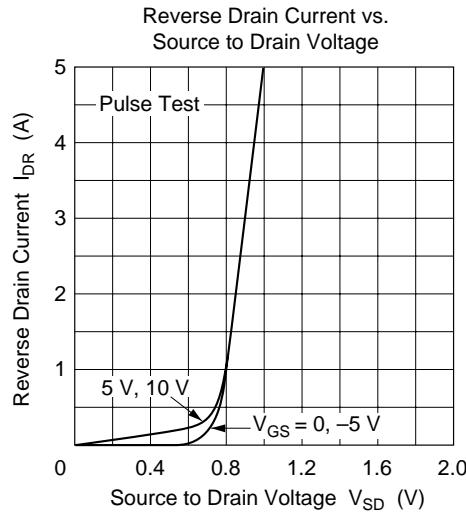
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	900	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±30	—	—	V	I _G = ±100 µA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	µA	V _{GS} = ±25 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	250	µA	V _{DS} = 720 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	—	3.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	5.0	7.0	—	I _D = 1 A, V _{GS} = 10 V ^{*1}
Forward transfer admittance	y _{fs}	0.9	1.5	—	S	I _D = 1 A, V _{DS} = 20 V ^{*1}
Input capacitance	C _{iss}	—	425	—	pF	V _{DS} = 10 V, V _{GS} = 0,
Output capacitance	C _{oss}	—	175	—	pF	f = 1 MHz
Reverse transfer capacitance	C _{rss}	—	85	—	pF	
Turn-on delay time	t _{d(on)}	—	10	—	ns	I _D = 1 A, V _{GS} = 10 V,
Rise time	t _r	—	35	—	ns	R _L = 30
Turn-off delay time	t _{d(off)}	—	60	—	ns	
Fall time	t _f	—	50	—	ns	
Body to drain diode forward voltage	V _{DF}	—	0.9	—	V	I _F = 2 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _r	—	700	—	ns	I _F = 2 A, V _{GS} = 0, di _F /dt = 100 A/µs

Note: 1. Pulse test

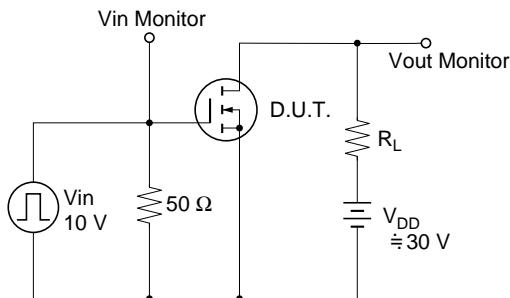




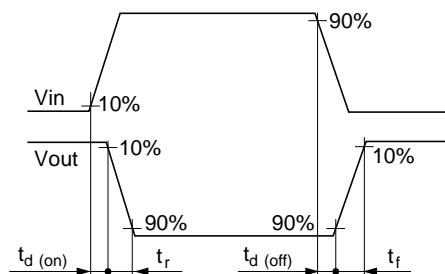




Switching Time Test Circuit



Waveforms



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