2SK1374

Silicon N-Channel MOS FET

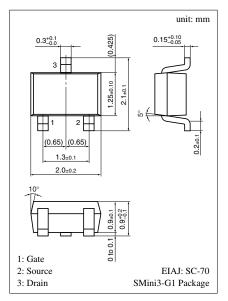
For switching

■ Features

- High-speed switching
- Wide frequency band
- Incorporating a built-in gate protection-diode
- Allowing 2.5V drive

■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Ratings	Unit	
Drain to Source voltage	V _{DS}	50	V	
Gate to Source voltage	V _{GSO}	10	V	
Drain current	I _D	50	mA	
Max drain current	I _{DP}	100	mA	
Allowable power dissipation	P _D	150	mW	
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



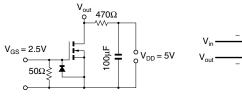
Marking Symbol: 4V

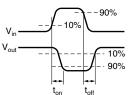
■ Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I_{DSS}	$V_{DS} = 20V, V_{GS} = 0$			1	μΑ
Gate to Source leakage current	I_{GSS}	$V_{GS} = 10V$, $V_{DS} = 0$			1	μΑ
Drain to Source breakdown voltage	V _{DSS}	$I_D = 10 \mu A, V_{GS} = 0$	50	100		V
Gate threshold voltage	V _{th}	$I_D = 100 \mu A, V_{DS} = 5 V$	0.5	0.8	1.1	V
Drain to Source ON-resistance	R _{DS(on)} *1	$I_D = 10 \text{mA}, V_{GS} = 2.5 \text{V}$		27	50	Ω
Forward transfer admittance	Y _{fs}	$I_D = 10 \text{mA}, V_{DS} = 5 \text{V}, f = 1 \text{kHz}$	20	39		mS
Input capacitance (Common Source)	C _{iss}			4.5		pF
Output capacitance (Common Source)	C _{oss}	$V_{DS} = 5V, V_{GS} = 0, f = 1MHz$		4.1		pF
Reverse transfer capacitance (Common Source)	C _{rss}			1.2		pF
Turn-on time	t _{on} *2	$V_{DD} = 5V$, $V_{GS} = 0$ to 2.5V, $R_L = 470\Omega$		0.2		μs
Turn-off time	t _{off} *2	$V_{DD} = 5V$, $V_{GS} = 2.5$ to $0V$, $R_L = 470\Omega$		0.2		μs

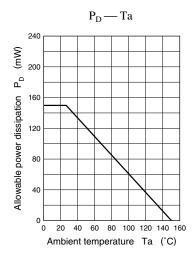
^{*1} Pulse measurement

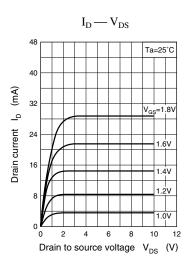
 $^{^{*2}}$ t_{on} , t_{off} measurement circuit

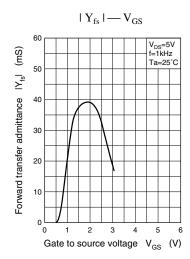


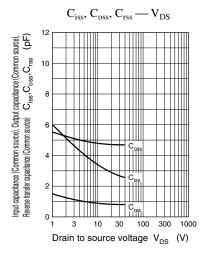


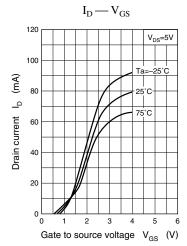
Panasonic 295

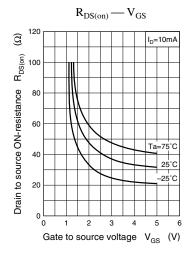


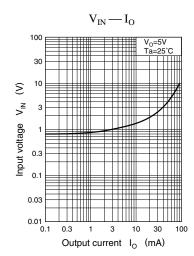












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