TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π−MOSV)

# 2SK2493

Chopper Regulator and DC−DC Converter Applications

- 2.5 V gate drive
- Low drain−source ON resistance : $R_{DS\ (ON)}$ = 0.08 mΩ (typ.)
- High forward transfer admittance : $|Y_{fs}|$ = 8.0 S (typ.)
- Low leakage current : $I_{DSS}$ = 100 μA (max) ($V_{DS}$ = 16 V)
- Enhancement−mode : $V_{th}$ = 0.5~1.1 V ($V_{DS}$ = 10 V, $I_D$ = 1 mA)

## Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit |
|---|---|---|---|---|
| Drain−source voltage | | $V_{DSS}$ | 16 | V |
| Drain−gate voltage ($R_{GS}$ = 20 kΩ) | | $V_{DGR}$ | 16 | V |
| Gate−source voltage | | $V_{GSS}$ | ±8 | V |
| Drain current | DC (Note 1) | $I_D$ | 5 | A |
| | Pulse (Note 1) | $I_{DP}$ | 20 | A |
| Drain power dissipation (Tc = 25°C) | | $P_D$ | 20 | W |
| Channel temperature | | $T_{ch}$ | 150 | °C |
| Storage temperature range | | $T_{stg}$ | −55~150 | °C |

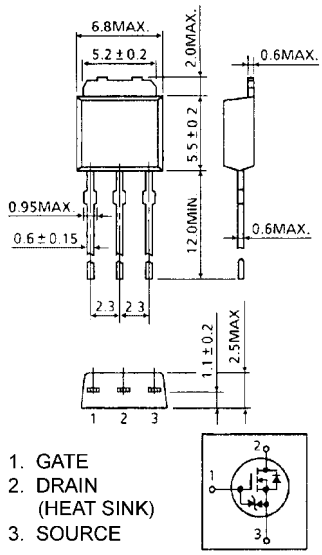
## Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|---|---|---|---|
| Thermal resistance, channel to case | $R_{th\ (ch-c)}$ | 6.25 | °C / W |
| Thermal resistance, channel to ambient | $R_{th\ (ch-a)}$ | 125 | °C / W |

Note 1: Please use devices on condition that the channel temperature is below 150°C.

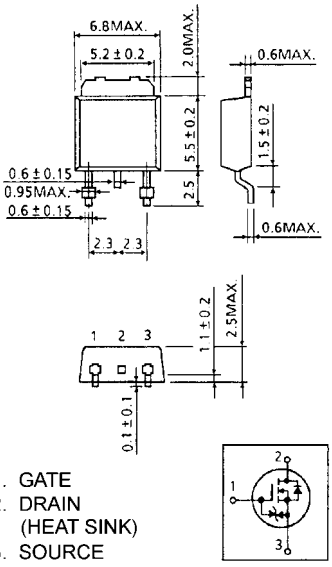
This transistor is an electrostatic sensitive device.
Please handle with caution.

Unit: mm




| | |
|---|---|
| JEDEC | — |
| JEITA | SC-64 |
| TOSHIBA | 2-7B1B |

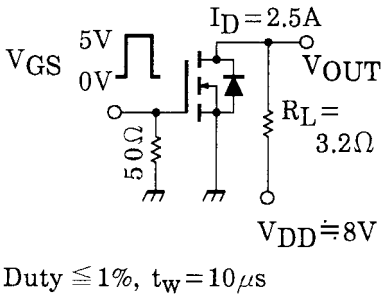
Weight: 0.36 g (typ.)




| | |
|---|---|
| JEDEC | — |
| JEITA | — |
| TOSHIBA | 2-7B3B |

Weight: 0.36 g (typ.)

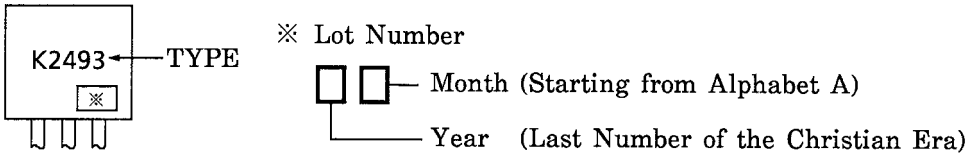
## Electrical Characteristics (Ta = 25°C)

| Characteristics | | Symbol | Test Condition | Min | Typ. | Max | Unit |
|---|---|---|---|---|---|---|---|
| Gate leakage current | | $I_{GSS}$ | $V_{GS}$ = ±6.5 V, $V_{DS}$ = 0 V | — | — | ±10 | μA |
| Drain cut−off current | | $I_{DSS}$ | $V_{DS}$ = 16 V, $V_{GS}$ = 0 V | — | — | 100 | μA |
| Drain−source breakdown voltage | | $V_{(BR)\ DSS}$ | $I_D$ = 10 mA, $V_{GS}$ = 0 V | 16 | — | — | V |
| Gate threshold voltage | | $V_{th}$ | $V_{DS}$ = 10 V, $I_D$ = 1 mA | 0.5 | — | 1.1 | V |
| Drain−source ON resistance | | $R_{DS\ (ON)}$ | $V_{GS}$ = 2.5 V, $I_D$ = 2.5 A | — | 0.08 | 0.12 | Ω |
| | | | $V_{GS}$ = 4 V, $I_D$ = 2.5 A | — | 0.07 | 0.1 | |
| Forward transfer admittance | | $\|Y_{fs}\|$ | $V_{DS}$ = 10 V, $I_D$ = 2.5 A | 4.0 | 8.0 | — | S |
| Input capacitance | | $C_{iss}$ | $V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 1 MHz | — | 1200 | — | pF |
| Reverse transfer capacitance | | $C_{rss}$ | | — | 110 | — | |
| Output capacitance | | $C_{oss}$ | | — | 380 | — | |
| Switching time | Rise time | $t_r$ |   | — | 30 | — | ns |
| | Turn−on time | $t_{on}$ | | — | 50 | — | |
| | Fall time | $t_f$ | | — | 200 | — | |
| | Turn−off time | $t_{off}$ | | — | 650 | — | |
| Total gate charge (Gate−source plus gate−drain) | | $Q_g$ | $V_{DD}$ ≈ 16 V, $V_{GS}$ = 5 V, $I_D$ = 5 A | — | 23 | — | nC |
| Gate−source charge | | $Q_{gs}$ | | — | 17 | — | |
| Gate−drain (“miller”) charge | | $Q_{gd}$ | | — | 6 | — | |

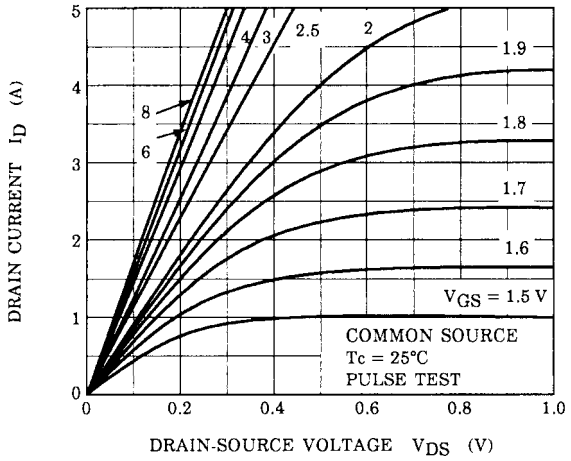
## Source−Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Typ. | Max | Unit |
|---|---|---|---|---|---|---|
| Continuous drain reverse current (Note 1) | $I_{DR}$ | — | — | — | 5 | A |
| Pulse drain reverse current (Note 1) | $I_{DRP}$ | — | — | — | 20 | A |
| Forward voltage (diode) | $V_{DSF}$ | $I_{DR}$ = 5 A, $V_{GS}$ = 0 V | — | — | −1.7 | V |
| Reverse recovery time | $t_{rr}$ | $I_{DR}$ = 5 A, $V_{GS}$ = 0 V<br>$dI_{DR}$ / dt = 50 A / μs | — | 120 | — | ns |
| Reverse recovery charge | $Q_{rr}$ | | — | 0.12 | — | μC |

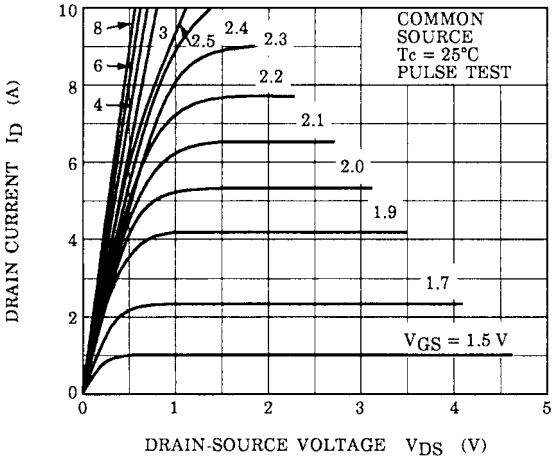
## Marking



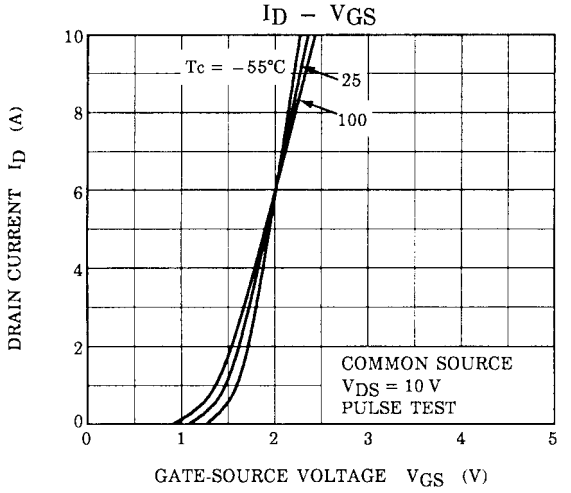
$I_D - V_{DS}$



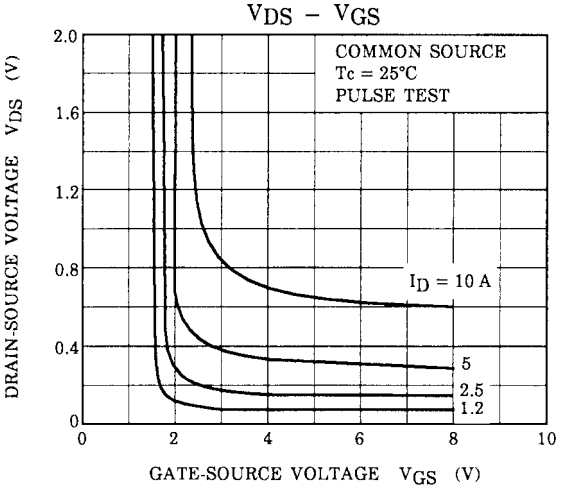

$I_D - V_{DS}$



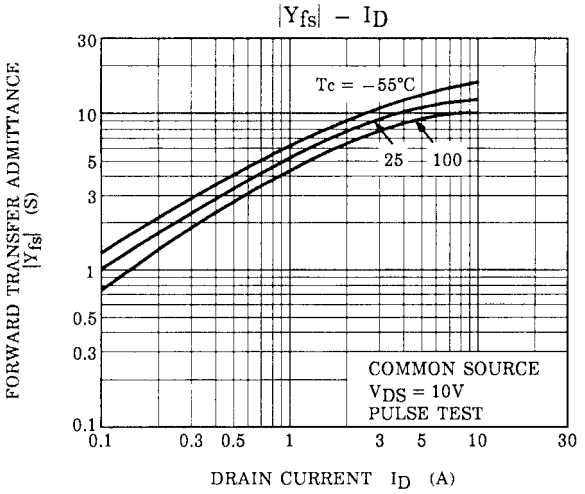

$I_D - V_{GS}$



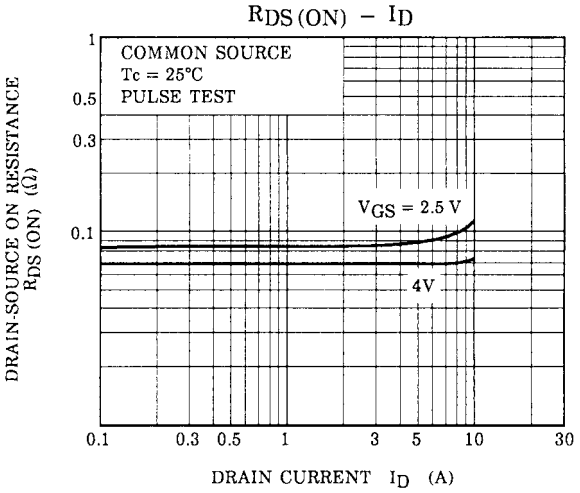

$V_{DS} - V_{GS}$



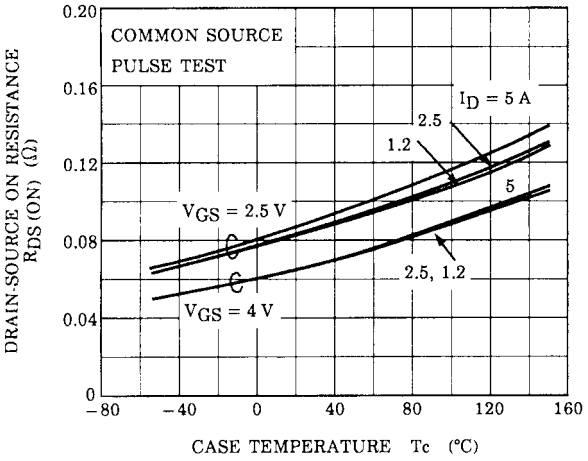

$|Y_{fs}| - I_D$



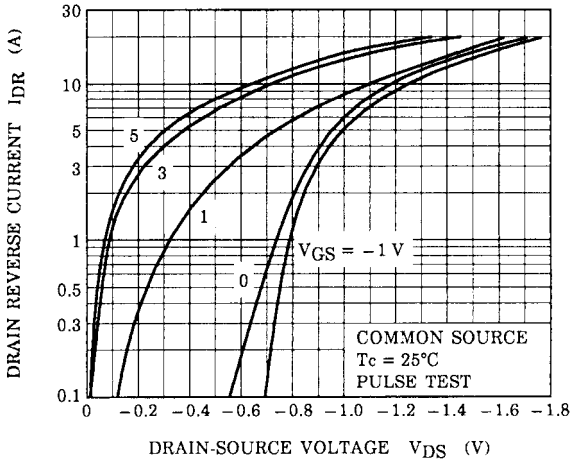

$R_{DS\,(ON)} - I_D$



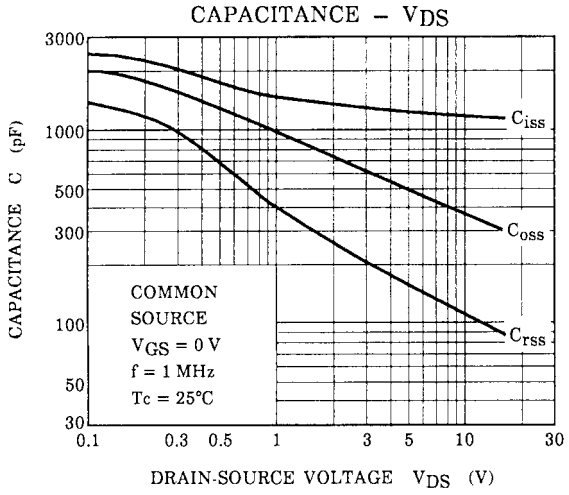
$R_{DS\,(ON)}$ – Tc



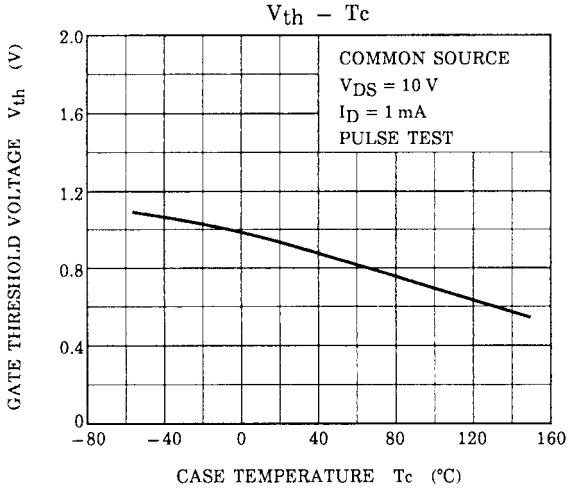

$I_{DR}$ – $V_{DS}$



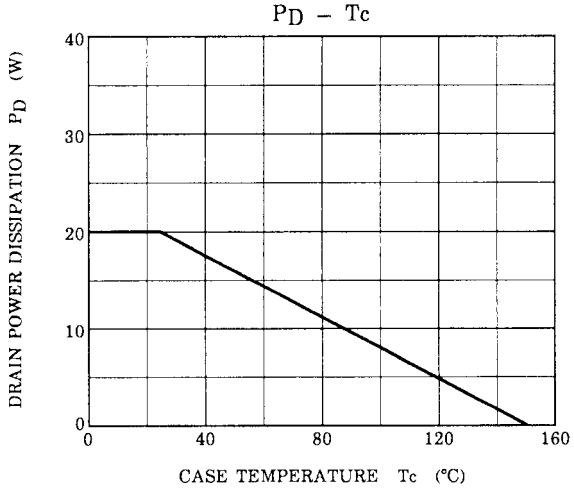

CAPACITANCE – $V_{DS}$

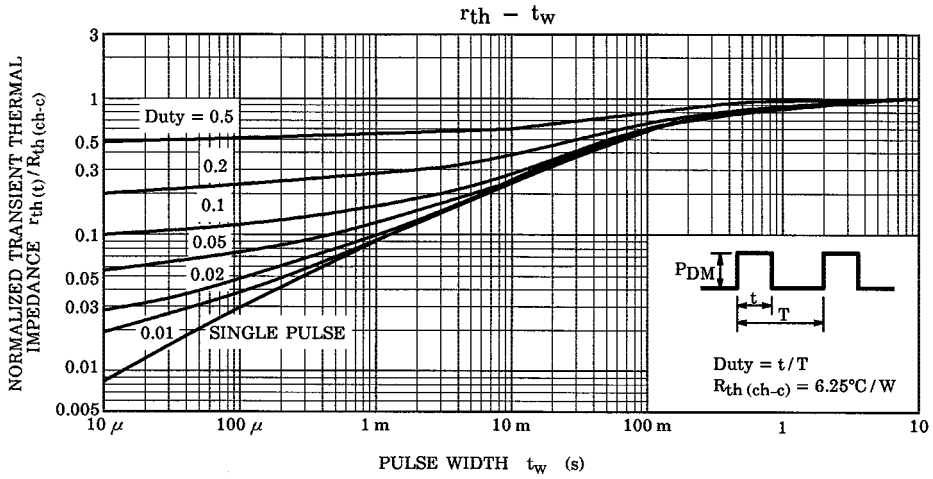


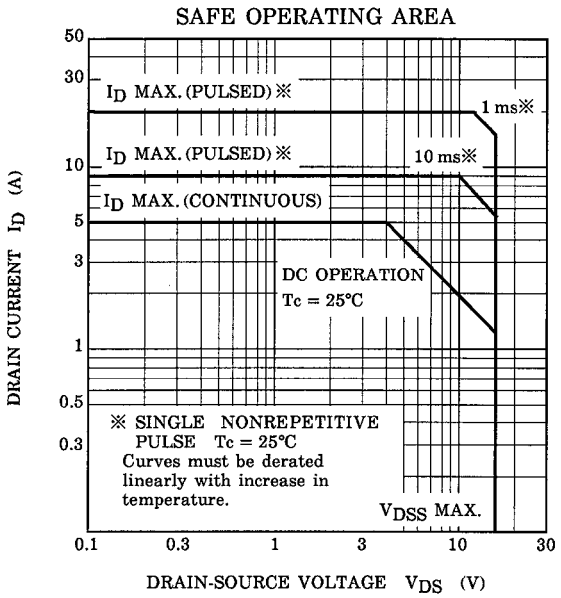

$V_{th}$ – Tc




$P_D$ – Tc




$r_{th}$ – $t_w$
NORMALIZED TRANSIENT THERMAL IMPEDANCE $r_{th}(t)/R_{th(ch-c)}$
Duty = 0.5
0.2
0.1
0.05
0.02
0.01
SINGLE PULSE
$P_{DM}$
T
t
Duty = t/T
$R_{th(ch-c)}$ = 6.25°C/W
PULSE WIDTH $t_w$ (s)


SAFE OPERATING AREA
DRAIN CURRENT $I_D$ (A)
$I_D$ MAX. (PULSED) ※
1 ms※
$I_D$ MAX. (PULSED) ※
10 ms※
$I_D$ MAX. (CONTINUOUS)
DC OPERATION
Tc = 25°C
※ SINGLE NONREPETITIVE PULSE Tc = 25°C
Curves must be derated linearly with increase in temperature.
$V_{DSS}$ MAX.
DRAIN-SOURCE VOLTAGE $V_{DS}$ (V)

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