



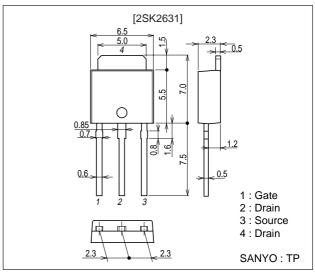
# **Ultrahigh-Speed Switching Applications**

#### **Features**

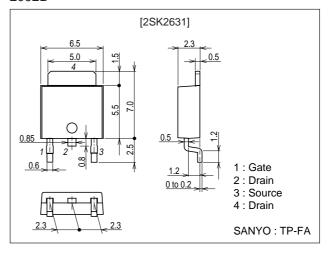
- · Low ON resistance.
- · Smaller amount of total gate charge.

### **Package Dimensions**

unit : mm 2083B



unit : mm 2092B



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## **Specifications**

### Absolute Maximum Ratings at Ta=25 °C

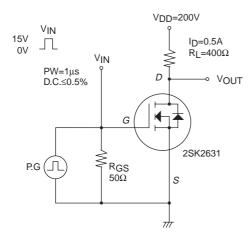
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		800	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		1	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	3	А
Allowable Power Dissipation	PD	Tc=25°C	30	W
			1.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

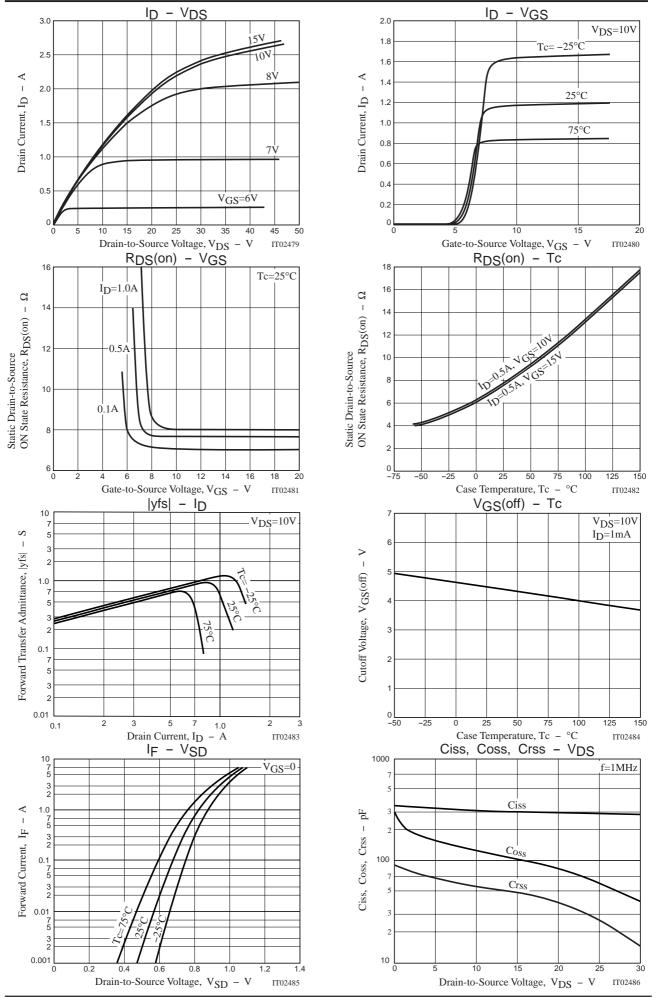
#### Electrical Characteristics at Ta=25°C

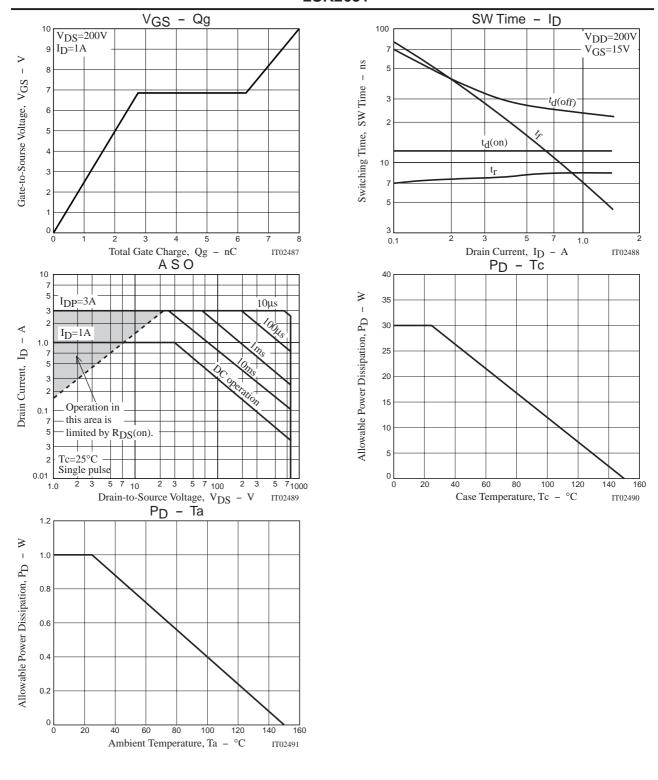
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	800			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =800V, V <sub>GS</sub> =0			1.0	mA
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0			±100	nA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	3.5		5.5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =0.5A	370	740		ms
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =0.5A, V <sub>GS</sub> =15V		7.5	10	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		300		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		85		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		40		pF
Total Gate Charge	Qg	V <sub>DS</sub> =200V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V		8		nC
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		12		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		8		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		27		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		16		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0		0.82	1.2	V

Marking: K2631

### **Switching Time Test Circuit**







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