TOSHIBA Field Effect Transistor Silicon N Channel Junction Type

2SK3320

For Low Noise Audio Amplifier Applications

- Two devices in a ultra super mini (five pins) package
- High $|Y_{fs}|$: $|Y_{fs}| = 15 \text{ mS} (typ.) (V_{DS} = 10 \text{ V}, V_{GS} = 0)$
- High breakdown voltage: $V_{GDS} = -50 V$
- Super low noise: NF = 1.0dB (typ.)

 $(V_{DS} = 10 \text{ V}, \text{ ID} = 0.5 \text{ mA}, \text{ f} = 1 \text{ kHz}, \text{ RG} = 1 \text{ k}\Omega)$

• High input impedance: $I_{GSS} = -1 nA (max) (V_{GS} = -30 V)$

Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit	
Gate-drain voltage	V _{GDS}	-50	V	
Gate current	I _G	10	mA	
Drain power dissipation	P _D (Note 1)	200	mW	
Junction temperature	Tj	125	°C	
Storage temperature range	T _{stg}	-55~125	°C	



Weight: 6.2 mg (typ.)

Note 1: Total rating

Marking



Pin Assignment (top view)



Unit: mm

Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate cut-off current	I _{GSS}	$V_{GS}=-30~V,~V_{DS}=0$	_	_	-1.0	nA
Gate-drain breakdown voltage	V (BR) GDS	$V_{DS}=0,\ I_G=-100\ \mu A$	-50	—		V
Drain current	I _{DSS} (Note)	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0$	1.2	_	14.0	mA
Gate-source cut-off voltage	V _{GS (OFF)}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.1 \mu\text{A}$	-0.2		-1.5	V
Forward transfer admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ kHz}$	4.0	15	_	mS
Input capacitance	C _{iss}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	_	13	_	pF
Reverse transfer capacitance	C _{rss}	$V_{DG} = 10 V, I_D = 0, f = 1 MHz$		3	_	pF
Noise figure	NF (1)	$\label{eq:VDS} \begin{array}{l} V_{DS} = 10 \; V, \; R_{G} = 1 \; k\Omega, \; I_{D} = 0.5 \; mA, \\ f = 10 \; Hz \end{array}$	_	5	_	dB
	NF (2)	V_{DS} = 10 V, R_G = 1 k $\Omega,~I_D$ = 0.5 mA, f = 1 kHz		1		

Note 2: I_{DSS} classification Y (Y): 1.2~3.0 mA, GR (G): 2.6~6.5 mA, BL (L): 6.0~14.0 mA

() I_{DSS} rank marking

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(Q1, Q2 common)



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