



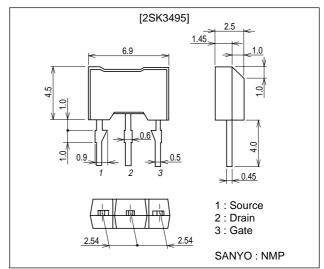
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- 4V drive.
- · Meets radial taping.

Package Dimensions

unit : mm 2087A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		1.2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	4.8	Α
Allowable Power Dissipation	PD		1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	60			٧
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0			10	μΑ
Gate-to-Sourse Leakage Current	IGSS	VGS=±16V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =0.6A	1.0	1.5		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=0.6A, VGS=10V		380	500	mΩ
	R _{DS} (on)2	I _D =0.6A, V _G S=4V		500	680	mΩ

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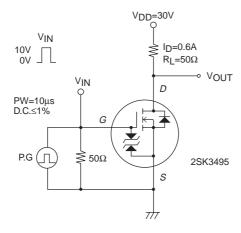
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Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		70		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		20		pF
Reverse Transfer Capacitance	Crss	VDS=20V, f=1MHz		5		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		4		ns
Rise Time	t _r	See specified Test Circuit		3		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		17		ns
Fall Time	tf	See specified Test Circuit		4		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =1.2A		3.6		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =1.2A		0.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =1.2A		0.5		nC
Diode Forward Voltage	V _{SD}	I _S =1.2A, V _{GS} =0		0.86	1.2	V

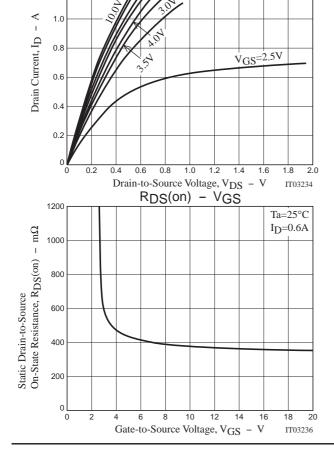
Switching Time Test Circuit

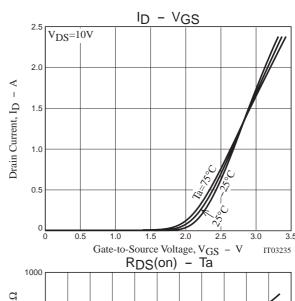
1.2

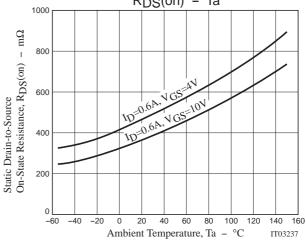


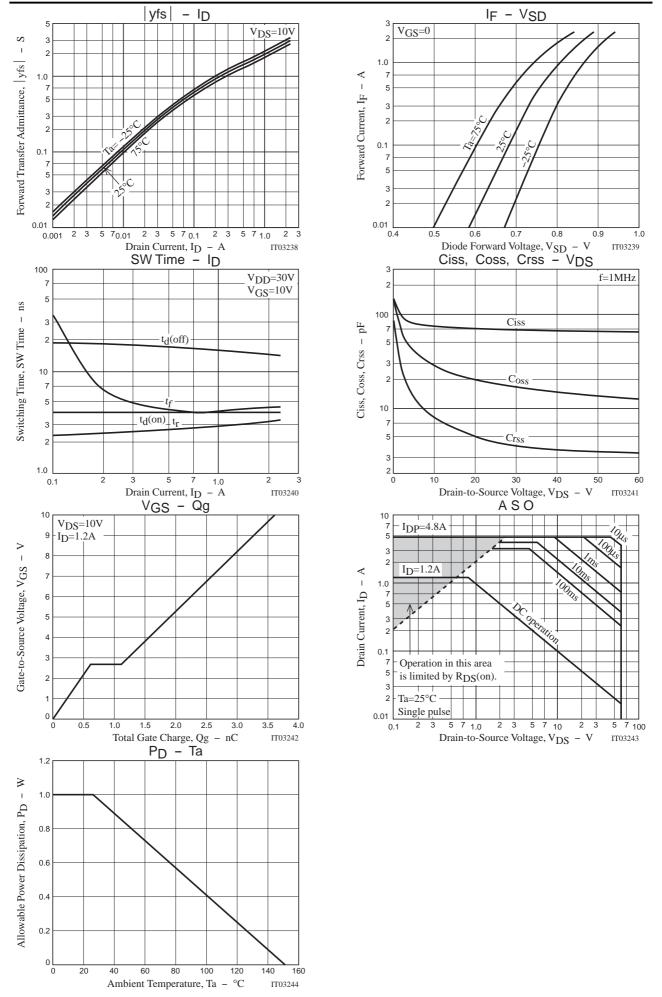
ID - VDS

8.0V 6.0V 5.0V









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