

# SANYO Semiconductors

# DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

# **2SK3707** — General-Purpose Switching Device Applications

#### **Features**

- ON-resistance RDS(on)1=45m $\Omega$  (typ.)
- Input capacitance Ciss=2150pF (typ.)

· 4V drive

#### **Specifications**

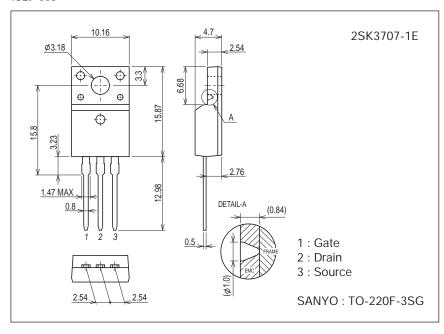
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		100	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±20	V
Drain Current (DC)	ID		20	А
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	80	А
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C	25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		125	mJ
Avalanche Current *2	IAV		20	Α

Note :\*1  $V_{DD}$ =20V, L=500 $\mu$ H, IAV=20A (Fig.1)

#### **Package Dimensions**

unit : mm (typ) 7529-001



#### **Product & Package Information**

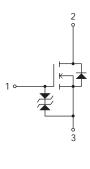
• Package : TO-220F-3SG

• JEITA, JEDEC : SC-67

• Minimum Packing Quantity : 50 pcs./magazine

#### Marking Electrical Connection





<sup>\*2</sup> L≤500µH, Single pulse

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Parameter	Syllibol	Conditions	min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	100			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =10A	11	17		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =10A, V <sub>G</sub> S=10V		45	60	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =10A, V <sub>G</sub> S=4V		56	80	mΩ
Input Capacitance	Ciss			2150		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		160		pF
Reverse Transfer Capacitance	Crss			110		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			19.5		ns
Rise Time	t <sub>r</sub>	San Fig 2		30		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See Fig.2		185		ns
Fall Time	tf			60		ns
Total Gate Charge	Qg			44		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A		7.8		nC
Gate-to-Drain "Miller" Charge	Qgd			9.8		nC
Diode Forward Voltage	VSD	IS=20A, VGS=0V		0.95	1.2	V

Fig.1 Avalanche Resistance Test Circuit

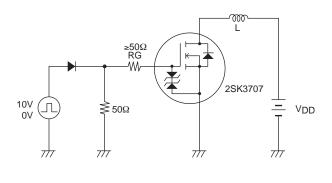
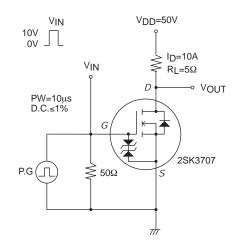
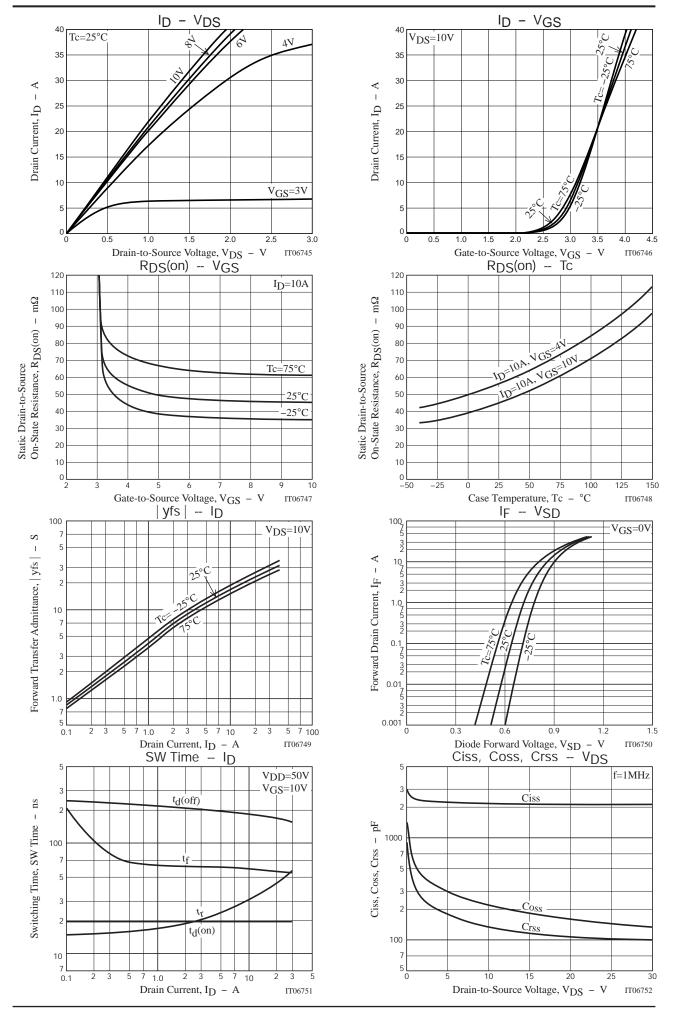


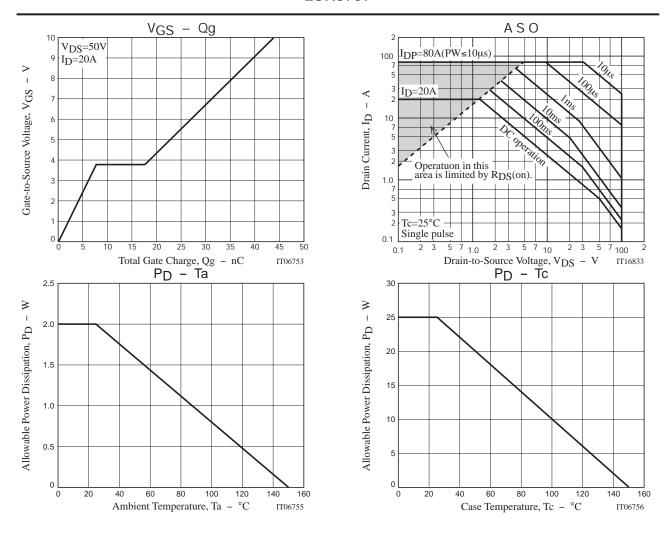
Fig.2 Switching Time Test Circuit



### **Ordering Information**

Device	Package	Shipping	memo
2SK3707-1E	TO-220F-3SG	50pcs./magazine	Pb Free





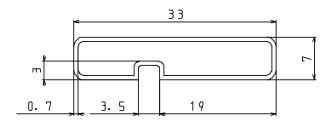
#### Magazine Specification

2SK3707-1E

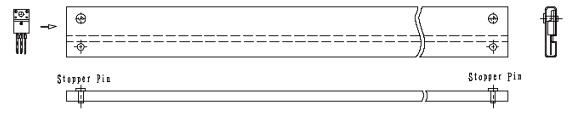
#### 1. Packing Format

Package Name	Magazine Name	Maximum Number of devices contained (pc			Packing format		
1 4 4 4 4 4 1 4 4 4 4	Ida 9 a b i a c i i a a a	l	Inner box	Outer box	Inner BOX	Outer BOX	
TO-220F-3SG	TO-220F	50	1, 000	4,000	SPD-0V0001 20 magazines contained Dimensions:mm (external) 568×150×55	SPT-081029 4 inner boxes contained Dinensions:mm (external) 590×225×178	

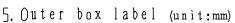
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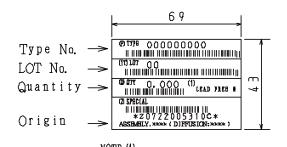
#### 3. Storage method to magazine

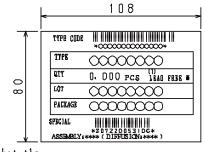


4. Inner box label (unit:mm)



It is a label at the time of factory shigments. The form of a label may change in physical distribution process.



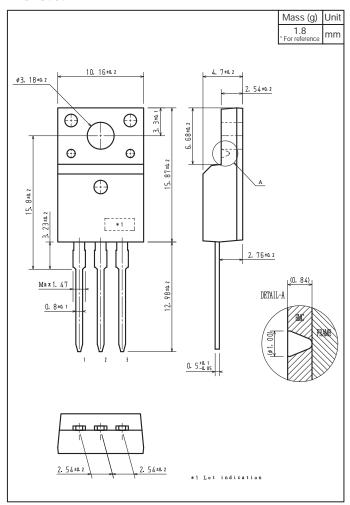


The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase			
LEAD FREE	3	JEITA Phase 3A			

## **Outline Drawing**

2SK3707-1E



Note on usage: Since the 2SK3707 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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