

# AM83135-030

PRELIMINARY DATA

# RF & MICROWAVE TRANSISTORS S-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P<sub>OUT</sub> = 30 W MIN. WITH 5.5 dB GAIN

### DESCRIPTION

The AM83135-030 device is a high power silicon bipolar NPN transistor specifically designed for S-Band radar pulsed output and driver applications.

This device is characterized at  $100\mu$ sec pulse width and 10% duty cycle, but is capable of operation over a range of pulse widths, duty cycles, and temperatures, and withstand a 3:1 output VSWR with a + 1 dB input overdrive. Low RF thermal resistance, refractory/gold metallization, and computerized automatic wire bonding techniques ensure high reliability and product consistency (including phase characteristics).

The AM83135-030 is supplied in the IMPAC<sup>™</sup> Hermetic Metal/Ceramic package with internal Input/Output impedance matching circuitry, and is intended for military and other high reliability applications.





Symbol	Parameter	Value	Unit
P <sub>DISS</sub>	Power Dissipation* $(T_C \le 50^{\circ}C)$	133	W
Ic	Device Current*	6.0	А
Vcc	Collector-Supply Voltage*	46	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +200	°C

### **ABSOLUTE MAXIMUM RATINGS** (Tcase = 25°C)

# THERMAL DATA

	R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance*	1.5	°C/W
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\*Applies only to rated RF amplifier operation

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# **ELECTRICAL SPECIFICATIONS** ( $T_{case} = 25^{\circ}C$ )

## STATIC

Cumb al	Test Conditions	Value			11		
Symbol		Test Conditions	Min.	Тур.	Max.	Unit	
ВУсво	$I_C = 20 m A$	$I_E = 0mA$		55	—	—	V
BVEBO	$I_E = 4mA$	$I_C = 0mA$		3.5			V
BVCER	IC = 20mA	$R_{BE} = 10\Omega$		55	_		V
ICES	$V_{\text{BE}} = 0V$	$V_{CE} = 40V$				15	mA
h <sub>FE</sub>	$V_{CE} = 5V$	$I_{C} = 2A$		30		300	

#### DYNAMIC

Cumhal	Test Conditions			Value			11
Symbol				Min.	Тур.	Max.	Unit
Pout	f = 3.1 — 3.5GHz	$P_{IN}=8.5W$	$V_{CC} = 40V$	30	_		W
ηc	f = 3.1 — 3.5GHz	$P_{\text{IN}}=8.5W$	$V_{CC} = 40V$	30	—		%
GP	f = 3.1 — 3.5GHz	$P_{\text{IN}}=8.5W$	$V_{CC} = 40V$	5.5	—		dB

Note: Pulse Width =  $100\mu$ Sec Duty Cycle = 10%

# PACKAGE MECHANICAL DATA





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