

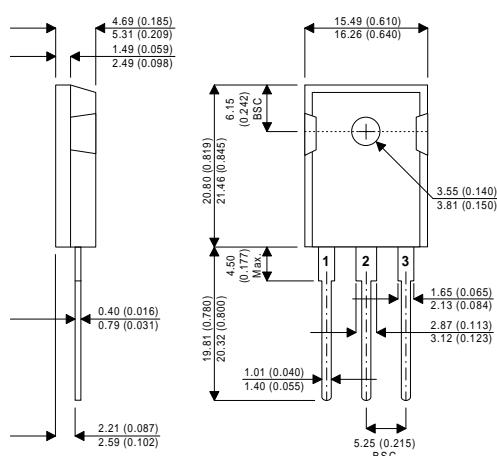


**SEME
LAB**

BFC52

TO247-AD Package Outline.

Dimensions in mm (inches)



4TH GENERATION MOSFET

**N-CHANNEL
ENHANCEMENT MODE
HIGH VOLTAGE
POWER MOSFETS**

V_{DSS} **500V**
 $I_{D(\text{cont})}$ **9.5A**
 $R_{DS(\text{on})}$ **0.85Ω**

Terminal 1 Gate **Terminal 2** Drain
Terminal 3 Source

ABSOLUTE MAXIMUM RATINGS ($T_{\text{case}} = 25^\circ\text{C}$ unless otherwise stated)

V_{DSS}	Drain – Source Voltage	500	V
I_D	Continuous Drain Current	9.5	A
I_{DM}	Pulsed Drain Current ¹	38	A
V_{GS}	Gate – Source Voltage	± 30	V
P_D	Total Power Dissipation @ $T_{\text{case}} = 25^\circ\text{C}$	180	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Lead Temperature : 0.063" from Case for 10 Sec.	300	

STATIC ELECTRICAL RATINGS ($T_{\text{case}} = 25^\circ\text{C}$ unless otherwise stated)

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain – Source Breakdown Voltage	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	500			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0\text{V}$)	$V_{DS} = V_{DSS}$			250	μA
		$V_{DS} = 0.8V_{DSS}$, $T_C = 125^\circ\text{C}$			1000	
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$			± 100	nA
$V_{GS(\text{TH})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 1.0\text{mA}$	2		4	V
$I_{D(\text{ON})}$	On State Drain Current ²	$V_{DS} > I_{D(\text{ON})} \times R_{DS(\text{ON})}$ Max				
		$V_{GS} = 10\text{V}$	9.5			A
$R_{DS(\text{ON})}$	Drain – Source On State Resistance ²	$V_{GS} = 10\text{V}$, $I_D = 0.5 I_D$ [Cont.]			0.85	Ω

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 μs , Duty Cycle < 2%



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DYNAMIC CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1MHz$		740	950	pF
C_{oss}	Output Capacitance			167	234	
C_{rss}	Reverse Transfer Capacitance			63	94	
Q_g	Total Gate Charge ³	$V_{GS} = 10V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [Cont.] @ 25^\circ C$		33	55	nC
Q_{gs}	Gate – Source Charge			5.6	8	
Q_{gd}	Gate – Drain (“Miller”) Charge			16	24	
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [Cont.] @ 25^\circ C$		10	20	ns
t_r	Rise Time			14	28	
$t_{d(off)}$	Turn-off Delay Time			35	48	
t_f	Fall Time	$R_G = 1.8\Omega$		11	22	

SOURCE – DRAIN DIODE RATINGS AND CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	(Body Diode)			9.5	A
I_{SM}	Pulsed Source Current ¹	(Body Diode)			38	
V_{SD}	Diode Forward Voltage ²	$V_{GS} = 0V , I_S = -I_D [Cont.]$			1.3	V
t_{rr}	Reverse Recovery Time	$I_S = -I_D [Cont.] , dI_S / dt = 100A/\mu s$	108	216	432	ns
Q_{rr}	Reverse Recovery Charge		1.2	2.5	5.0	μC

SAFE OPERATING AREA CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
SOA1	Safe Operating Area	$V_{DS} = 0.4V_{DSS} , t = 1 Sec.$ $I_{DS} = P_D / 0.4V_{DSS}$	180			W
SOA2	Safe Operating Area	$V_{DS} = P_D / I_D [Cont.]$ $I_{DS} = I_D [Cont.] , t = 1 Sec.$	180			W
I_{LM}	Inductive Current Clamped		38			A

THERMAL CHARACTERISTICS

	Characteristic	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction to Case			0.68	$^{\circ}C/W$
$R_{\theta JA}$	Junction to Ambient			40	

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 μ s , Duty Cycle < 2%

3) See MIL-STD-750 Method 3471



CAUTION — Electrostatic Sensitive Devices. Anti-Static Procedures Must Be Followed.