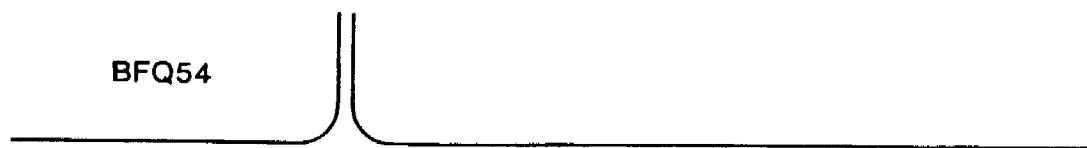


# New Jersey Semi-Conductor Products, Inc.

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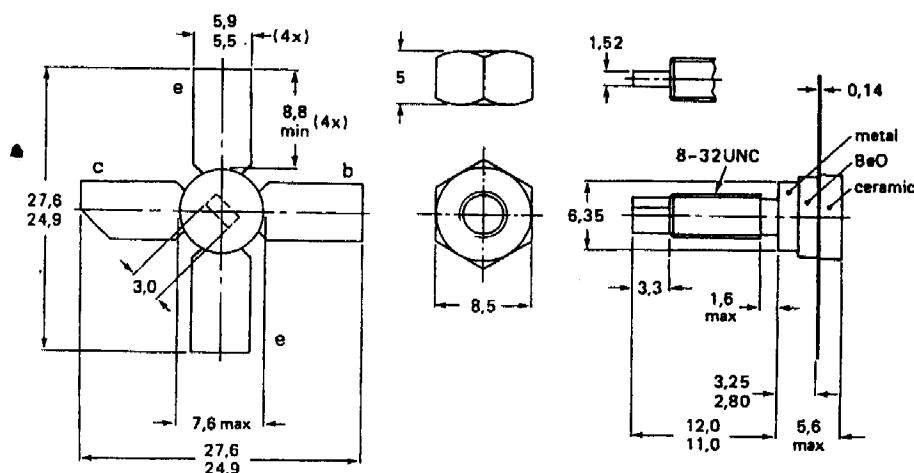
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## MECHANICAL DATA

Fig. 1 SOT122.

Dimensions in mm



Torque on nut: min. 0.75 Nm (7.5 kg cm) diameter of clearance hole in heatsink: max. 4.2 mm (mounting hole to have no burrs at either end).  
max. 0.85 Nm (8.5 kg cm) de-burrings must leave surface flat; do not chamfer or countersink either end of the hole.

When locking is required an adhesive is preferred instead of a lock washer.

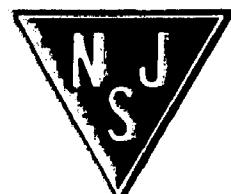
## RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134).

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	25 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	18 V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	2 V
Collector current (DC)	$I_C$	max.	150 mA
Total power dissipation up to $T_{mb} = 125^\circ\text{C}$	$P_{tot}$	max.	2.25 W
Storage temperature range	$T_{stg}$	range	$-65 \text{ to } +150^\circ\text{C}$
Operating junction temperature	$T_j$	max.	200 $^\circ\text{C}$

## THERMAL RESISTANCE

From junction to mounting base	$R_{th j-mb}$	=	28 K/W
From mounting base to heatsink	$R_{th mb-h}$	=	0.6 K/W



PNP 4 GHz wideband transistor

BFQ54

### CHARACTERISTICS

$T_{amb} = 25^\circ\text{C}$  unless otherwise specified

Collector cut-off current

$I_E = 0; -V_{CB} = 15 \text{ V}$

$I_{CBO}$  max.  $50 \mu\text{A}$

DC current gain

$-I_C = 150 \text{ mA}; -V_{CE} = 15 \text{ V}$

$\text{h}_{FE}$  min.  $25$   
 $\text{h}_{FE}$  typ.  $75$

Transition frequency at  $f = 500 \text{ MHz}$  (note 1)

$-I_C = 150 \text{ mA}; -V_{CE} = 15 \text{ V}$

$f_T$  typ.  $4.5 \text{ GHz}$

Collector capacitance at  $f = 1 \text{ MHz}$

$I_E = i_e = 0; -V_{CB} = 15 \text{ V}$

$C_C$  typ.  $2.0 \text{ pF}$

Emitter capacitance at  $f = 1 \text{ MHz}$

$I_C = i_c = 0; -V_{EB} = 0.5 \text{ V}$

$C_E$  typ.  $6.5 \text{ pF}$

Feedback capacitance at  $f = 1 \text{ MHz}$

$I_C = 0 \text{ mA}; V_{CE} = 15 \text{ V}$

$C_{re}$  typ.  $1.3 \text{ pF}$

Collector-stud capacitance

$C_{cs}$  typ.  $1.2 \text{ pF}$

Maximum unilateral power gain (S12 assumed to be zero)

$-I_C = 120 \text{ mA}; -V_{CE} = 15 \text{ V}; f = 500 \text{ MHz}$

$$G_{UM} = 10 \log \frac{|S21|^2}{(1 - |S11|^2)(1 - |S22|^2)} \quad G_{UM} \text{ typ. } 16 \text{ dB}$$

Output voltage at  $d_{im} = -60 \text{ dB}$

(DIN 45005B, para 6.3: 3-tone)

$-I_C = 120 \text{ mA}; -V_{CE} = 15 \text{ V}; R_L = 75 \Omega$

$V_p = V_o$  at  $d_{im} = -60 \text{ dB}; f_p = 795.25 \text{ MHz}$

$V_q = V_o - 6 \text{ dB}; f_q = 803.25 \text{ MHz}$

$V_r = V_o - 6 \text{ dB}; f_r = 805.25 \text{ MHz}$

measured at  $f(p + q - r) = 793.25 \text{ MHz}$

$V_o$  typ.  $900 \text{ mV}$

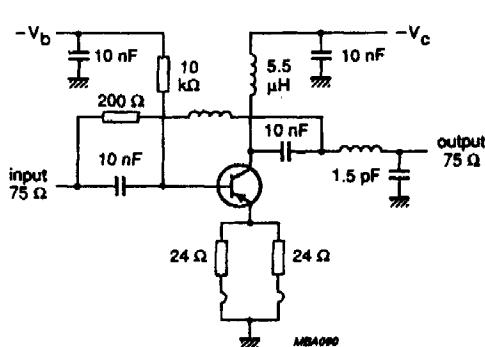


Fig. 2 MATV-test circuit  $f = 40$  to  $860 \text{ MHz}$

### Note

1. Measured under pulse conditions.