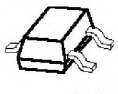
SIEMENS

SIPMOS® Small-Signal Transistor — BSS 84

- V_{DS} – 50 V
- I_D – 0.13 A
- $R_{DS(on)}$ 10 Ω
- $V_{GS(th)}$ – 0.8 ... – 1.6 V
- P channel
- Enhancement mode
- Logic level



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Type	Ordering Code	Tape and Reel Information	Pin Configuration 1	2	3	Marking	Package
BSS 84	Q62702-S568	E6327: 3000 pcs/reel	G	S	D	BSS 84 marked SPs	SOT-23
BSS 84	Q67000-S243	E6433: 10000 pcs/reel					

Maximum Ratings

Parameter	Symbol	Values	Unit
Drain-source voltage	V_{DS}	– 50	V
Drain-gate voltage, R_{GS} = 20 kΩ	V_{DGR}	– 50	
Gate-source voltage	V_{GS}	± 20	
Continuous drain current, T_A = 30 ℃	I_D	– 0.13	A
Pulsed drain current, T_A = 25 ℃	$I_{D\,puls}$	– 0.52	
Max. power dissipation, T_A = 25 ℃	P_{tot}	0.36	W
Operating and storage temperature range	T_j, T_{stg}	– 55 ... + 150	℃

Thermal resistance, chip-ambient (without heat sink)	R_{thJA}	≤ 350	K/W
chip-substrate – reverse side [1]	R_{thJSR}	≤ 285	
DIN humidity category, DIN 40 040	–	E	–
IEC climatic category, DIN IEC 68-1	–	55/150/56	

[1] For package mounted on aluminum 15 mm × 16.7 mm × 0.7 mm.

Electrical Characteristics

at T_j = 25 °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain-source breakdown voltage V_{GS} = 0, I_D = – 0.25 mA	$V_{(BR)DSS}$	– 50	–	–	V
Gate threshold voltage V_{GS} = V_{DS}, I_D = – 1 mA	$V_{GS(th)}$	– 0.8	– 1.5	– 2.0	
Zero gate voltage drain current V_{DS} = – 50 V, V_{GS} = 0 T_j = 25 °C T_j = 125 °C V_{DS} = – 25 V, V_{GS} = 0 T_j = 25 °C	I_{DSS}	 – – –	 – 0.1 – 2 –	 – 1.0 – 60 – 0.1	µA
Gate-source leakage current V_{GS} = – 20 V, V_{DS} = 0	I_{GSS}	–	– 1	– 10	nA
Drain-source on-resistance V_{GS} = – 10 V, I_D = – 0.13 A	$R_{DS(on)}$	–	5.0	10	Ω
Dynamic Characteristics					
Forward transconductance $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$, I_D = – 0.13 A	g_{fs}	0.05	0.085	–	S
Input capacitance V_{GS} = 0, V_{DS} = – 25 V, f = 1 MHz	C_{iss}	–	30	40	pF
Output capacitance V_{GS} = 0, V_{DS} = – 25 V, f = 1 MHz	C_{oss}	–	17	25	
Reverse transfer capacitance V_{GS} = 0, V_{DS} = – 25 V, f = 1 MHz	C_{rss}	–	8	12	
Turn-on time t_{on}, (t_{on} = $t_{d(on)}$ + t_r) V_{DD} = – 30 V, V_{GS} = – 10 V, R_{GS} = 50 Ω, I_D = – 0.27 A	$t_{d(on)}$	–	7	10	ns
	t_r	–	12	18	
Turn-off time t_{off}, (t_{off} = $t_{d(off)}$ + t_f) V_{DD} = – 30 V, V_{GS} = – 10 V, R_{GS} = 50 Ω, I_D = – 0.27 A	$t_{d(off)}$	–	10	13	
	t_f	–	20	27	

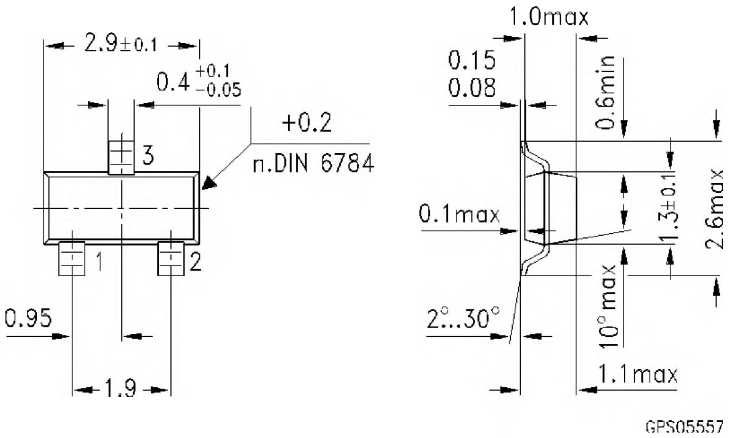
Electrical Characteristics (cont'd)

at T_j = 25 °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Reverse Diode					
Continuous reverse drain current T_A = 25 °C	I_S	–	–	– 0.13	A
Pulsed reverse drain current T_A = 25 °C	I_{SM}	–	–	– 0.52	
Diode forward on-voltage I_F = – 0.26 A, V_{GS} = 0	V_{SD}	–	– 0.9	– 1.2	V

Package Outline

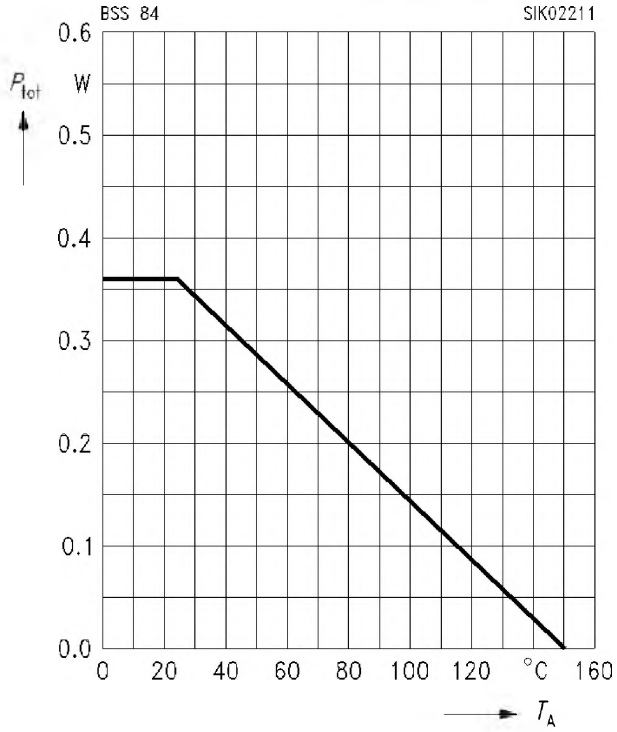
SOT-23



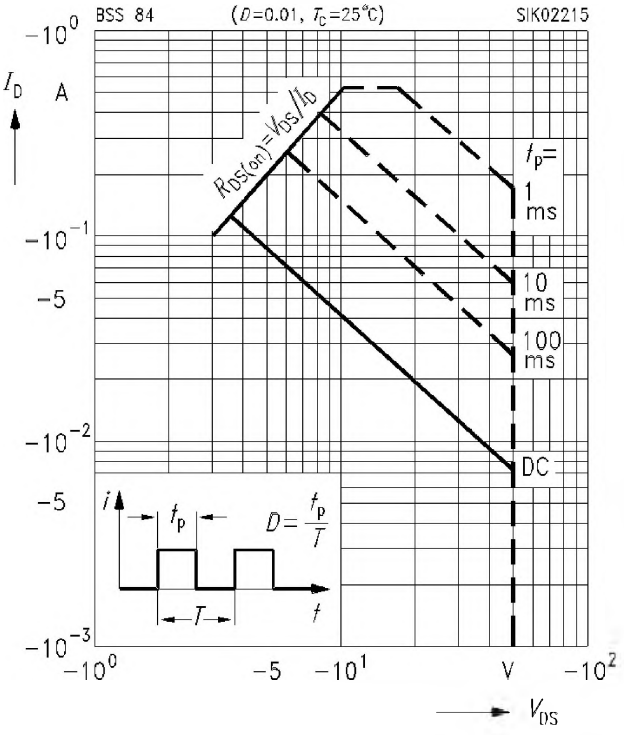
Dimensions in mm

Characteristics
at T_j = 25 °C, unless otherwise specified.

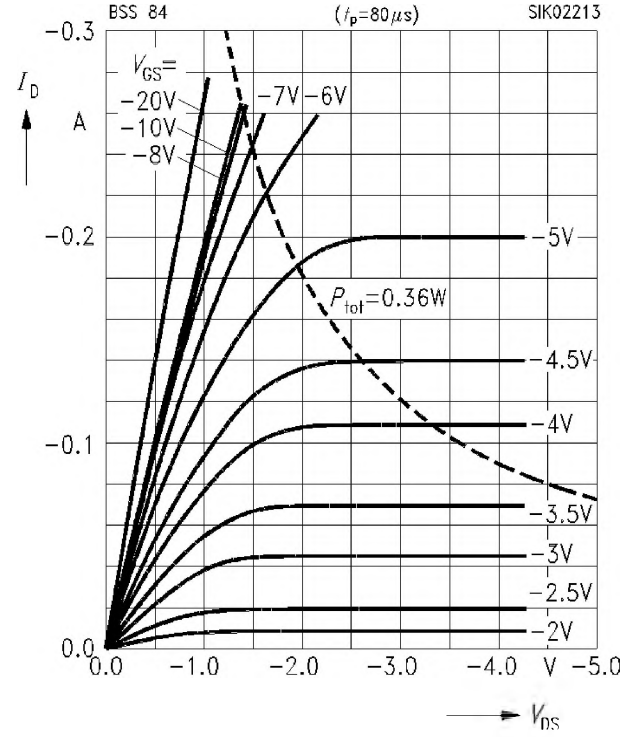
Total power dissipation $P_{tot} = f(T_A)$



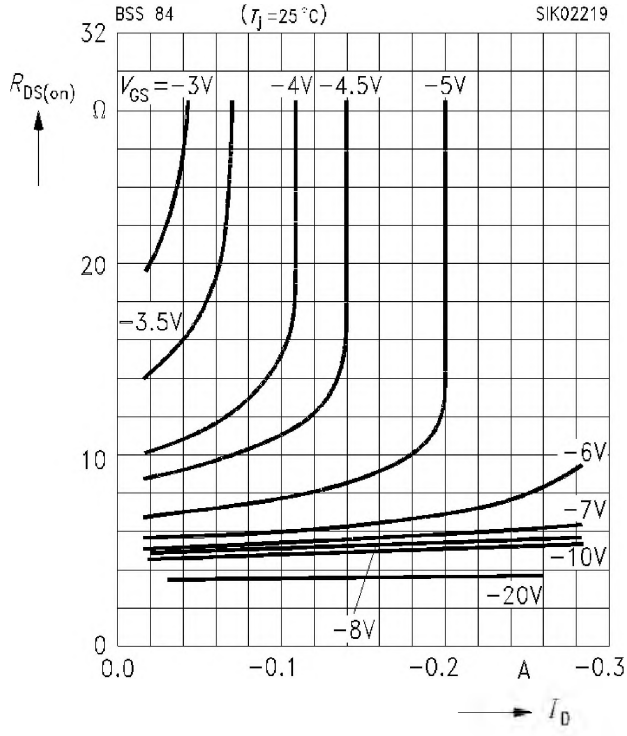
Safe operating area $I_D = f(V_{DS})$
parameter: D = 0.01, T_C = 25 °C



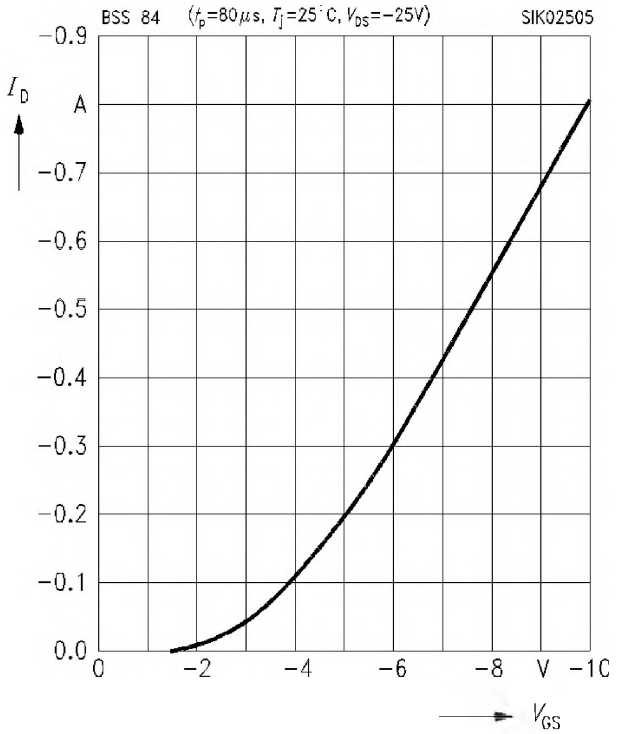
Typ. output characteristics $I_D = f(V_{DS})$
parameter: t_p = 80 µs



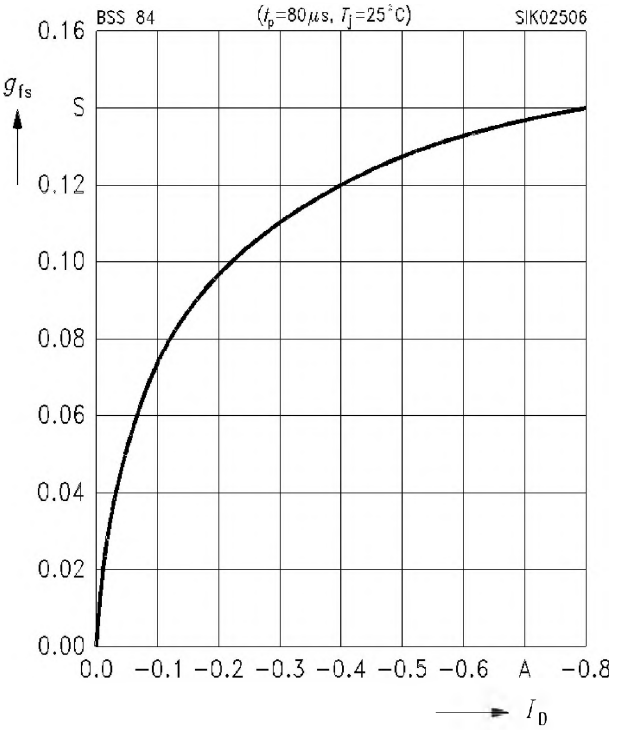
Typ. drain-source on-resistance
$R_{DS(on)} = f(I_D)$
parameter: V_{GS}



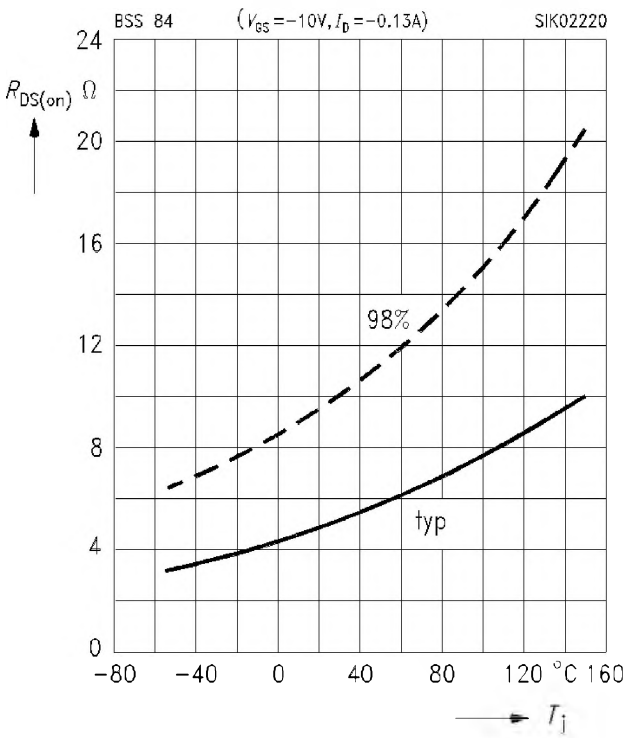
Typ. transfer characteristics $I_D = f(V_{GS})$
parameter: t_p = 80 µs, $V_{DS} \geq 2 \times I_D \times R_{DS(on)max.}$



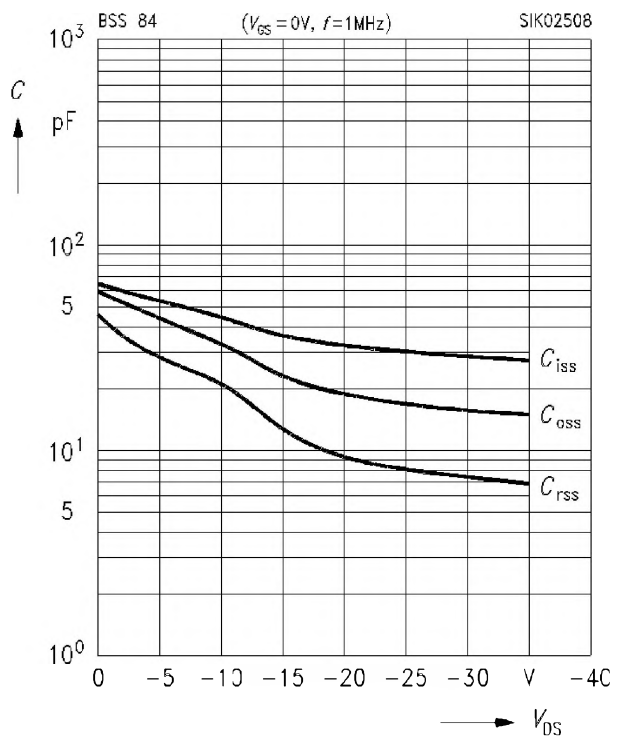
Typ. forward transconductance $g_{fs} = f(I_D)$
parameter: $V_{DS} \geq 2 \times I_D \times R_{DS(on)max.}$, t_p = 80 µs



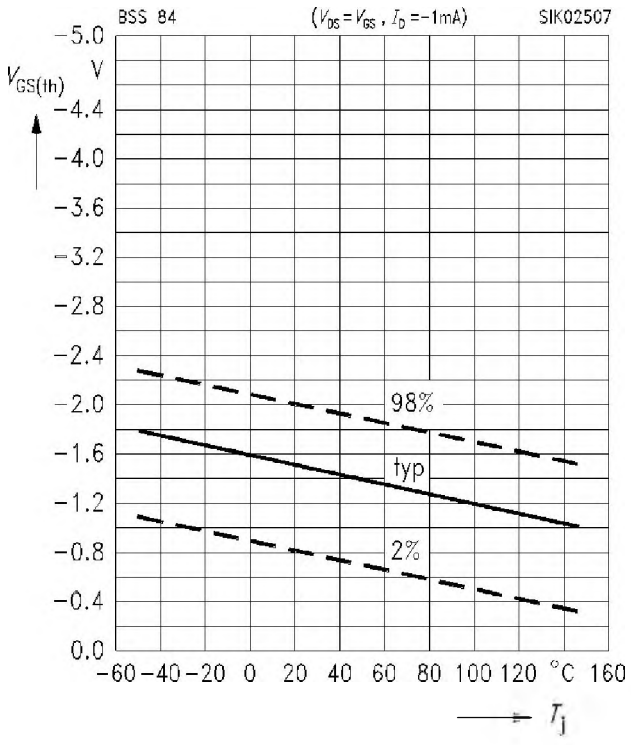
Drain-source on-resistance
$R_{DS(on)} = f(T_j)$
parameter: I_D = – 0.13 A, V_{GS} = – 10 V, (spread)



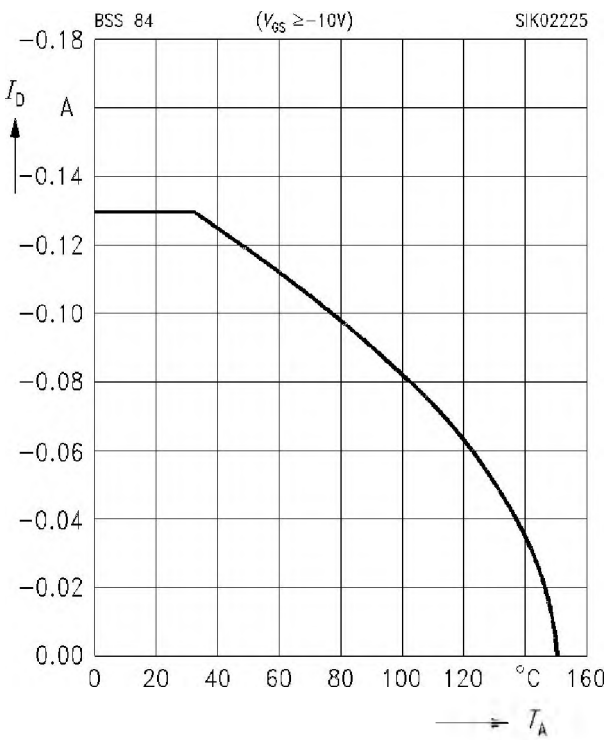
Typ. capacitances $C = f(V_{DS})$
parameter: V_{GS} = 0, f = 1 MHz



Gate threshold voltage $V_{GS(th)} = f(T_j)$
parameter: $V_{DS} = V_{GS}$, $I_D = -1$ mA, (spread)



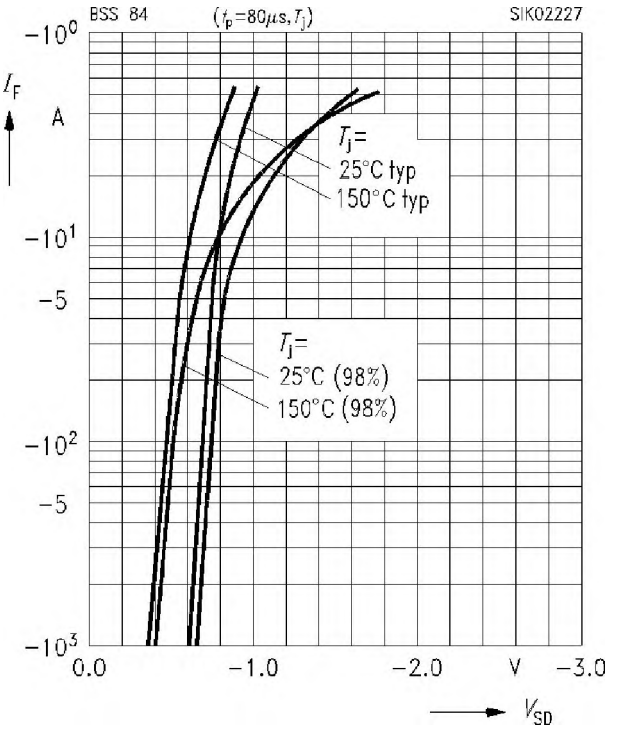
Drain current $I_D = f(T_A)$
parameter: $V_{GS} \geq -10$ V



Forward characteristics of reverse diode

$I_F = f(V_{SD})$

parameter: t_p = 80 µs, T_j, (spread)



Drain-source breakdown voltage

$V_{(BR)\,DSS} = b \times V_{(BR)DSS}$ (25 ℃)