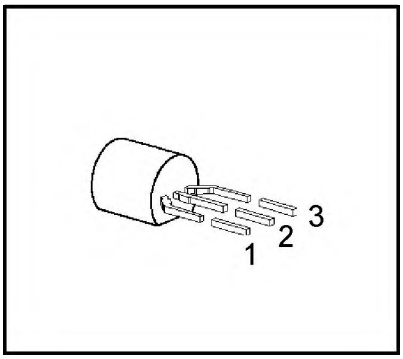
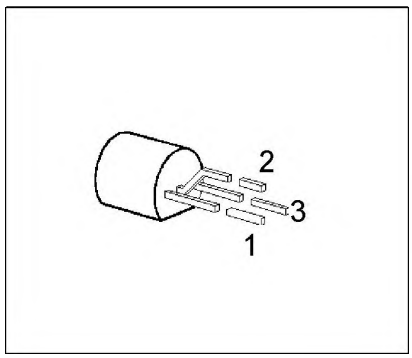
SIEMENS

SIPMOS® Small-Signal Transistor — BSS 92

- V_{DS} – 240 V
- I_D – 0.15 A
- $R_{DS(on)}$ 20 Ω
- $V_{GS(th)}$ – 0.8 … – 2.0 V
- P channel
- Enhancement mode
- Logic level





Type	Ordering Code	Tape and Reel Information	Pin Configuration			Marking	Package
			1	2	3		
BSS 92	Q62702-S458	bulk	G	D	S	BSS 92 marked SS92	TO-92
BSS 92	Q62702-S497	E6288: 1500 pcs/reel; 2 reels/carton; gate first					
BSS 92	Q62702-S633	E6296: 1500 pcs/reel; 2 reels/carton; source first					
BSS 92	Q62702-S502	E6325: 2000 pcs/carton; Ammopack					

Maximum Ratings

Parameter	Symbol	Values	Unit
Drain-source voltage	V_{DS}	– 240	V
Drain-gate voltage, R_{GS} = 20 kΩ	V_{DGR}	– 240	
Gate-source voltage	V_{GS}	± 20	
Continuous drain current, T_A = 33 ℃	I_D	– 0.15	A
Pulsed drain current, T_A = 25 ℃	$I_{D\,puls}$	– 0.6	
Max. power dissipation, T_A = 25 ℃	P_{tot}	1.0	W
Operating and storage temperature range	T_j, T_{stg}	– 55 … + 150	℃

Thermal resistance, chip-ambient (without heat sink)	R_{thJA}	≤ 125	K/W
DIN humidity category, DIN 40 040	–	E	–
IEC climatic category, DIN IEC 68-1	–	55/150/56	

Electrical Characteristics

at T_j = 25 °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain-source breakdown voltage V_{GS} = 0, I_D = – 0.25 mA	$V_{(BR)DSS}$	– 240	–	–	V
Gate threshold voltage V_{GS} = V_{DS}, I_D = – 1 mA	$V_{GS(th)}$	– 0.8	– 1.5	– 2.0	
Zero gate voltage drain current V_{DS} = – 240 V, V_{GS} = 0 T_j = 25 °C T_j = 125 °C V_{DS} = – 60 V, V_{GS} = 0 T_j = 25 °C	I_{DSS}	 – – –	 – 0.1 – 10 –	 – 1.0 – 100 – 0.2	µA
Gate-source leakage current V_{GS} = – 20 V, V_{DS} = 0	I_{GSS}	–	– 10	– 100	nA
Drain-source on-resistance V_{GS} = – 10 V, I_D = – 0.15 A	$R_{DS(on)}$	–	10	20	Ω
Dynamic Characteristics					
Forward transconductance $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$, I_D = – 0.15 A	g_{fs}	0.06	0.12	–	S
Input capacitance V_{GS} = 0, V_{DS} = – 25 V, f = 1 MHz	C_{iss}	–	95	130	pF
Output capacitance V_{GS} = 0, V_{DS} = – 25 V, f = 1 MHz	C_{oss}	–	20	30	
Reverse transfer capacitance V_{GS} = 0, V_{DS} = – 25 V, f = 1 MHz	C_{rss}	–	10	15	
Turn-on time t_{on}, (t_{on} = $t_{d(on)}$ + t_r)	$t_{d(on)}$	–	8	12	ns
V_{DD} = – 30 V, V_{GS} = – 10 V, R_{GS} = 50 Ω, I_D = – 0.25 A	t_r	–	25	40	
Turn-off time t_{off}, (t_{off} = $t_{d(off)}$ + t_f)	$t_{d(off)}$	–	25	33	
V_{DD} = – 30 V, V_{GS} = – 10 V, R_{GS} = 50 Ω, I_D = – 0.25 A	t_f	–	42	55	

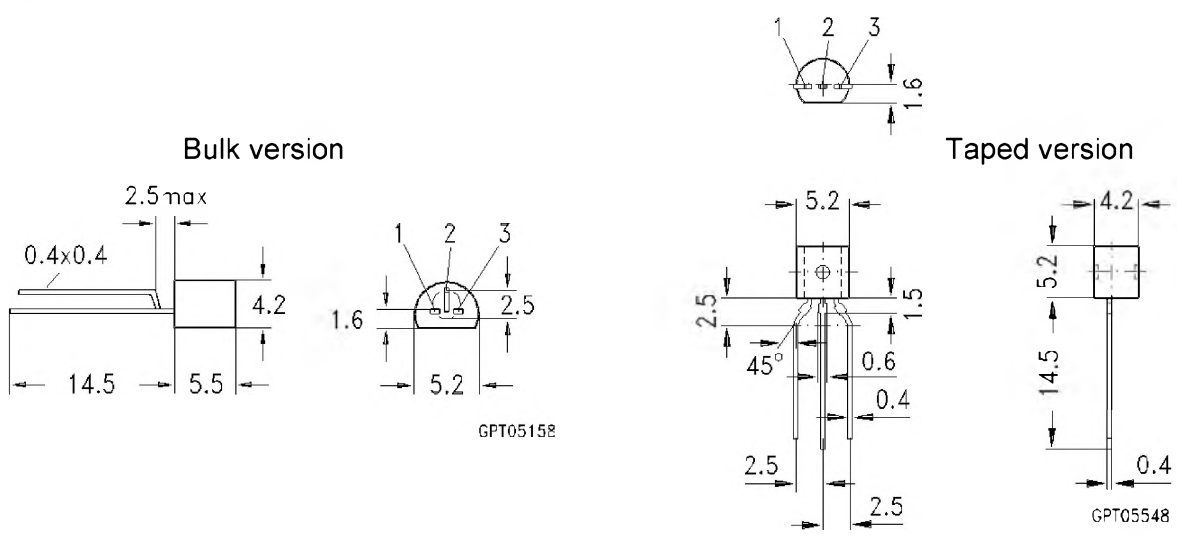
Electrical Characteristics (cont'd)

at T_j = 25 °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Reverse Diode					
Continuous reverse drain current T_A = 25 °C	I_S	–	–	– 0.15	A
Pulsed reverse drain current T_A = 25 °C	I_{SM}	–	–	– 0.60	
Diode forward on-voltage I_F = – 0.3 A, V_{GS} = 0	V_{SD}	–	– 0.85	– 1.2	V

Package Outline

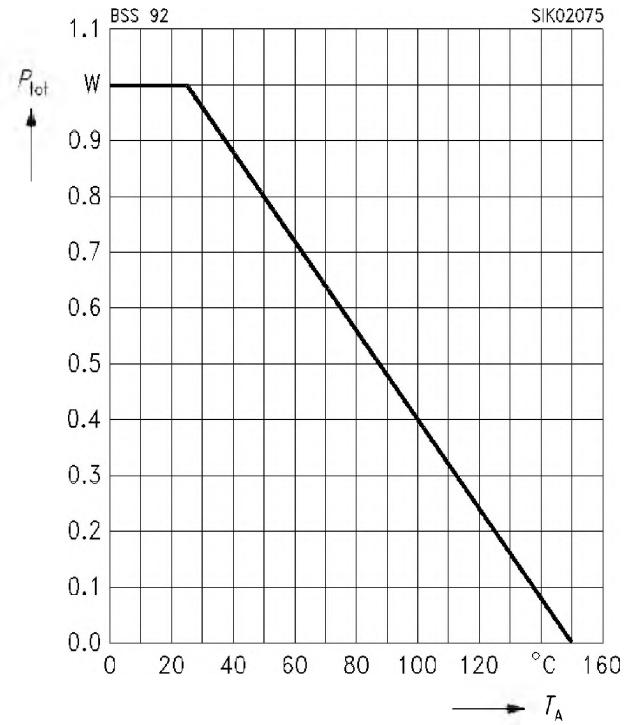
TO-92



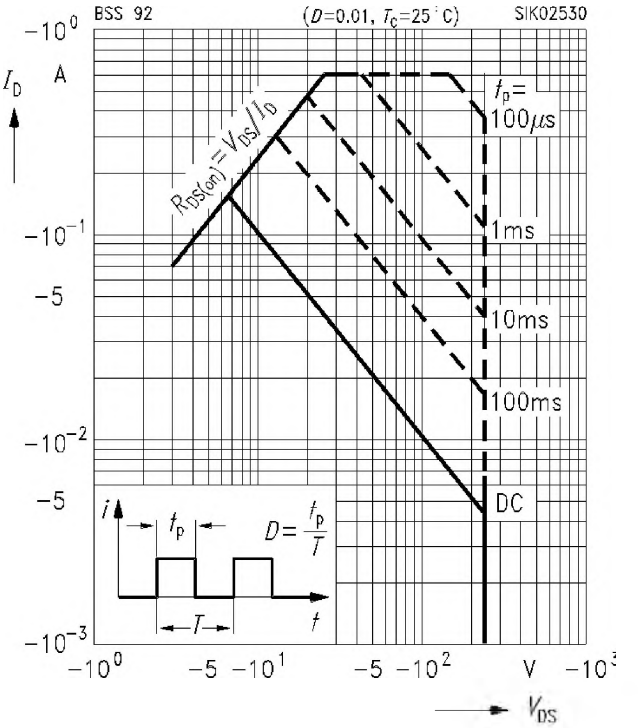
Dimensions in mm

Characteristics
at T_j = 25 °C, unless otherwise specified.

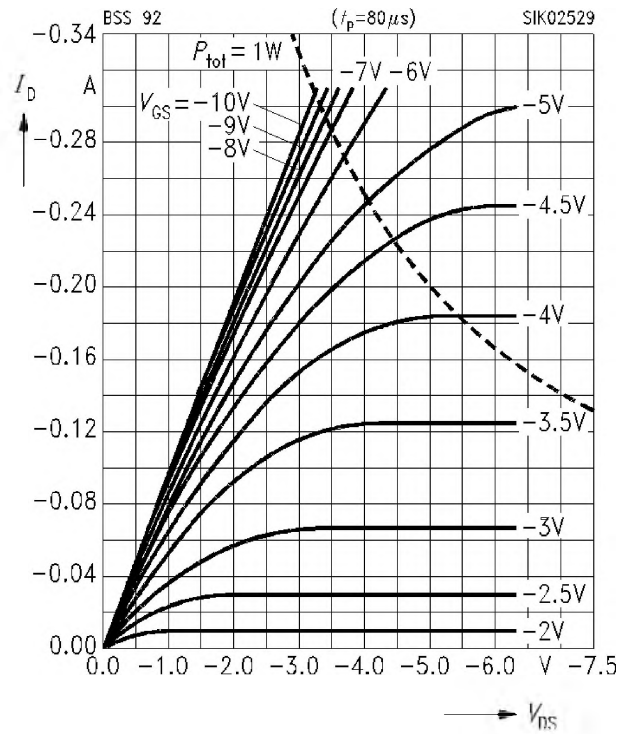
Total power dissipation $P_{tot} = f(T_A)$



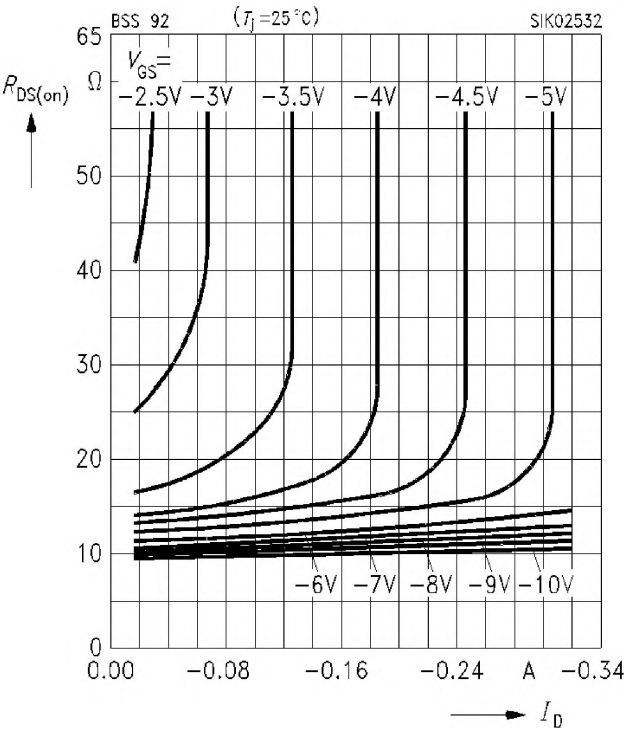
Safe operating area $I_D = f(V_{DS})$
parameter: D = 0.01, T_C = 25 °C



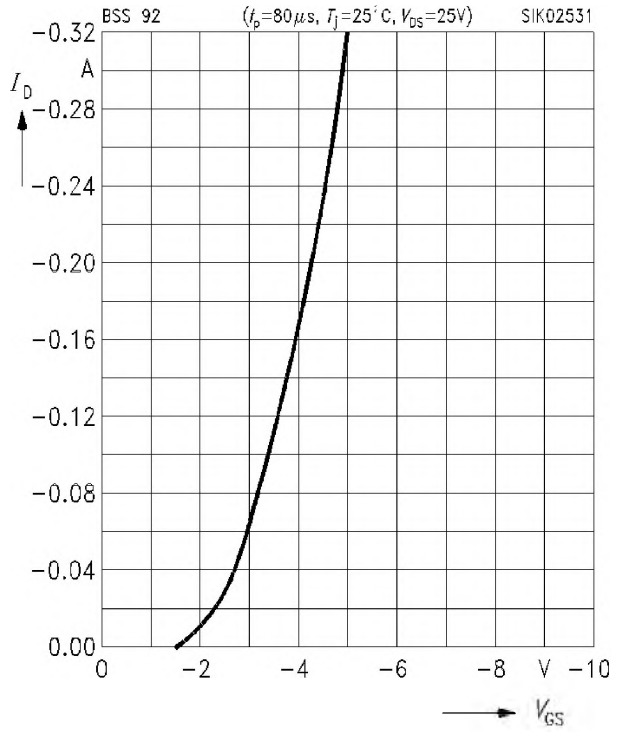
Typ. output characteristics $I_D = f(V_{DS})$
parameter: t_p = 80 µs



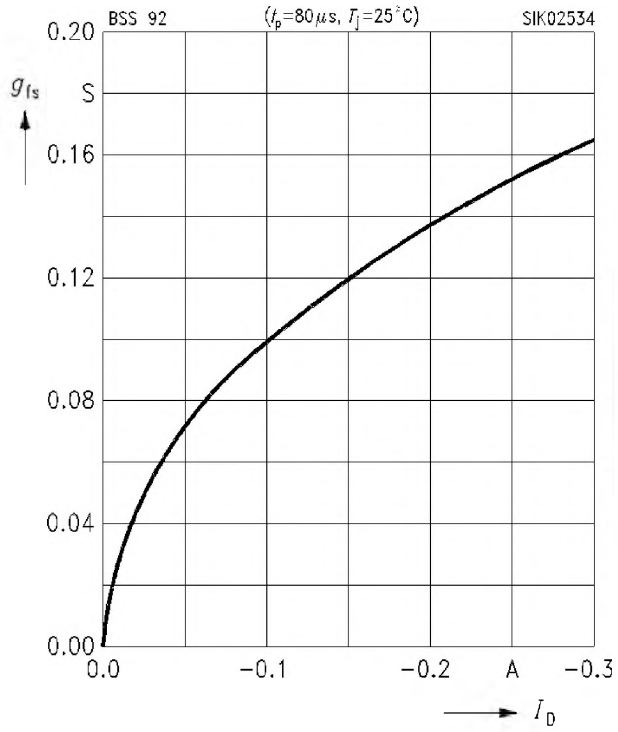
Typ. drain-source on-resistance
$R_{DS(on)} = f(I_D)$
parameter: V_{GS}



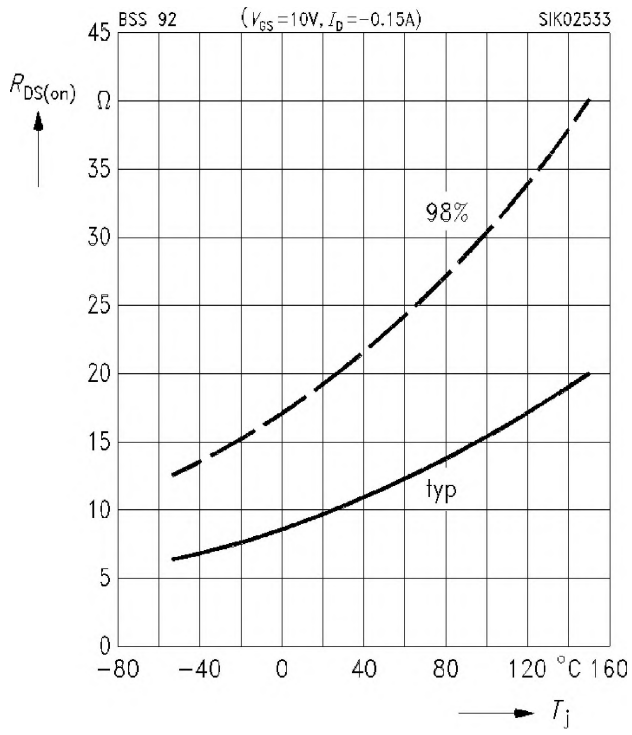
Typ. transfer characteristics $I_D = f(V_{GS})$
parameter: t_p = 80 µs, $V_{DS} \geq 2 \times I_D \times R_{DS(on)max.}$



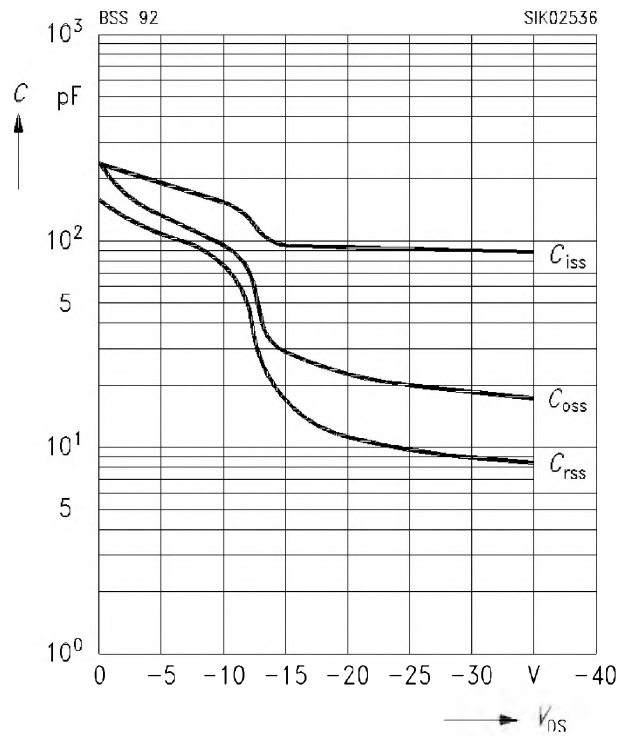
Typ. forward transconductance $g_{fs} = f(I_D)$
parameter: $V_{DS} \geq 2 \times I_D \times R_{DS(on)max.}$, t_p = 80 µs



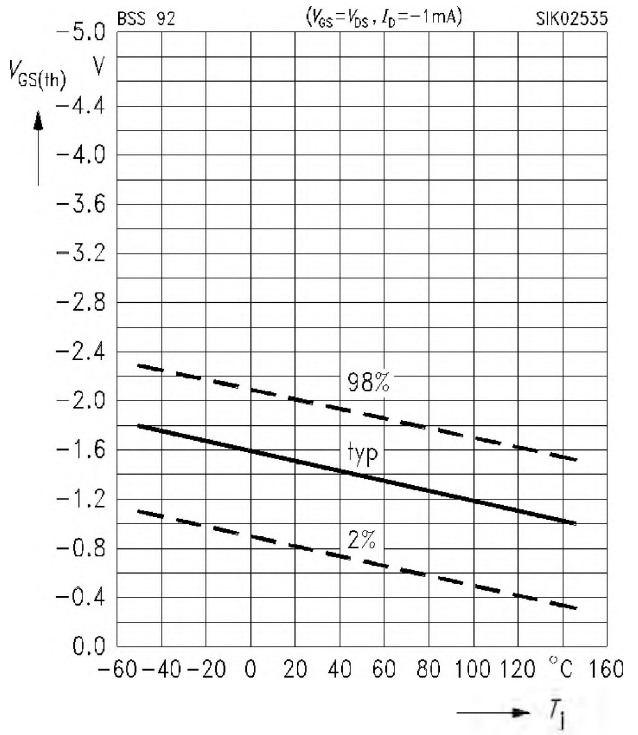
Drain-source on-resistance
$R_{DS(on)} = f(T_j)$
parameter: I_D = – 0.15 A, V_{GS} = 10 V, (spread)



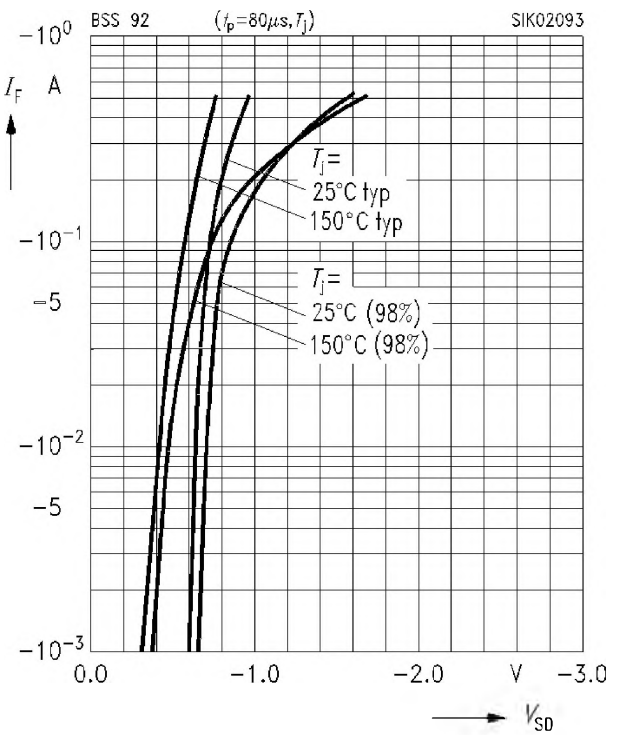
Typ. capacitances $C = f(V_{DS})$
parameter: V_{GS} = 0, f = 1 MHz



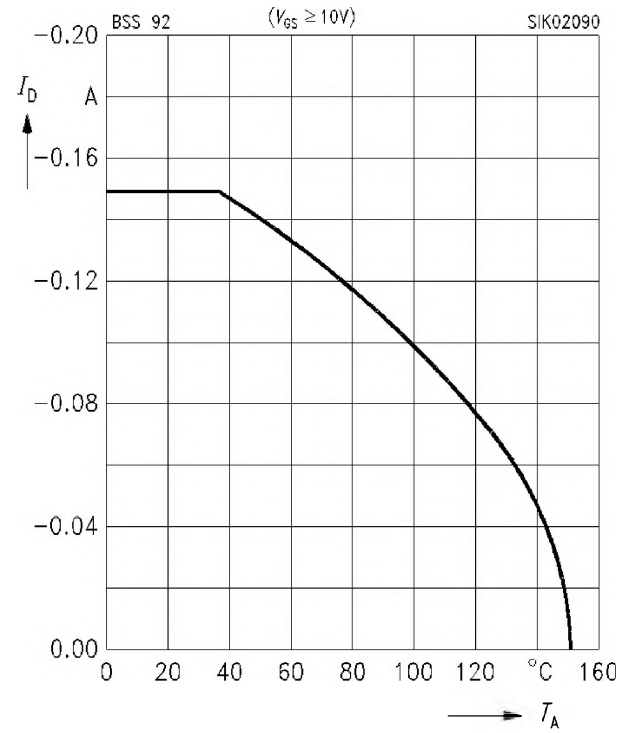
Gate threshold voltage $V_{GS(th)} = f(T_j)$
parameter: $V_{DS} = V_{GS}$, I_D = 1 mA, (spread)



Forward characteristics of reverse diode
$I_F = f(V_{SD})$
parameter: t_p = 80 µs, T_j, (spread)



Drain current $I_D = f(T_A)$
parameter: $V_{GS} \geq 10$ V



Drain-source breakdown voltage
$V_{(BR)\,DSS} = b \times V_{(BR)DSS}$ (25 °C)