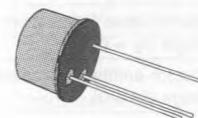


HIGH VOLTAGE SWITCH

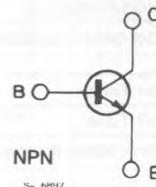
DESCRIPTION

The BSW 67 and BSW 68 are silicon epitaxial planar NPN transistors in Jedec TO-39 metal case. They are intended for high voltage inductive load switching applications.



TO-39

INTERNAL SCHEMATIC DIAGRAM



S-6857

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		BSW67	BSW68	
V_{CBO}	Collector-base Voltage ($I_E = 0$)	120	150	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	120	150	V
I_C	Collector Current		1.5	A
I_{CM}	Collector Peak Current		2	A
P_{tot}	Total Power Dissipation at $T_{amb} \leq 45^\circ C$		0.7	W
	$T_{case} \leq 25^\circ C$		5	W
	$T_{case} \leq 100^\circ C$		2.85	W
T_{stg}	Storage Temperature	- 65 to 200		°C
T_J	Junction Temperature	200		°C

THERMAL DATA

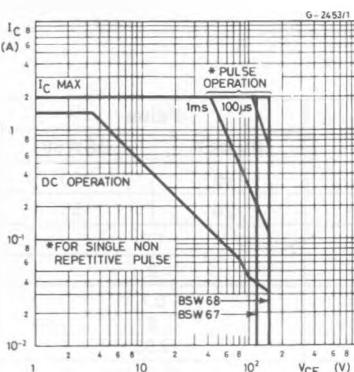
$R_{th\ j-case}$	Thermal Resistance Junction-case	Max	35	$^{\circ}\text{C}/\text{W}$
$R_{th\ j-amb}$	Thermal Resistance Junction-ambient	Max	220	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}\text{C}$ unless otherwise specified)

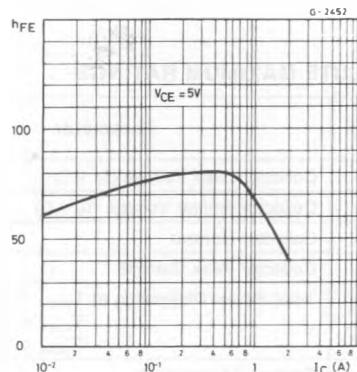
Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
I_{CBO}	Collector Cutoff Current ($I_E = 0$)	for BSW67				100	nA
		$V_{CB} = 60\text{ V}$				50	μA
		$V_{CB} = 60\text{ V}$	$T_{case} = 150^{\circ}\text{C}$			100	nA
		for BSW68				50	μA
$V_{(BR)CBO}$	Collector-base Breakdown Voltage ($I_E = 0$)	$V_{CB} = 75\text{ V}$				100	nA
		$V_{CB} = 75\text{ V}$	$T_{case} = 150^{\circ}\text{C}$			50	μA
$V_{CEO(sus)}^*$	Collector-emitter Sustaining Voltage ($I_B = 0$)	$I_C = 100\text{ }\mu\text{A}$	for BSW67	120			V
			for BSW68	150			V
V_{EBO}^*	Emitter-base Voltage ($I_C = 0$)	$I_C = 100\text{ }\mu\text{A}$	for BSW67	120			V
			for BSW68	150			V
$V_{CE(sat)}^*$	Collector-emitter Saturation Voltage	$I_C = 0.1\text{ A}$ $I_C = 0.5\text{ A}$ $I_C = 1\text{ A}$	$I_B = 0.01\text{ A}$ $I_B = 0.05\text{ A}$ $I_B = 0.15\text{ A}$			0.15 0.5 1	V
$V_{BE(sat)}^*$	Base-emitter Voltage	$I_C = 0.1\text{ A}$ $I_C = 0.5\text{ A}$ $I_C = 1\text{ A}$	$I_S = 0.01\text{ A}$ $I_B = 0.05\text{ A}$ $I_B = 0.15\text{ A}$			0.9 1.1 1.2	V
h_{FE}^*	DC Current Gain	$I_C = 0.1\text{ A}$ $I_C = 0.5\text{ A}$ $I_C = 1\text{ A}$	$V_{CE} = 5\text{ V}$ $V_{CE} = 5\text{ V}$ $V_{CE} = 5\text{ V}$	40 30 15			
f_T	Transition Frequency	$I_C = 100\text{ mA}$	$V_{CE} = 20\text{ V}$		80		MHz
C_{CBO}	Collector-base Capacitance	$I_E = 0$ $f = 1\text{ MHz}$	$V_{CB} = 10\text{ V}$			35	pF
t_{on}	Turn-on Time	$I_C = 0.5\text{ A}$	$V_{CC} = 20\text{ V}$		0.3		μs
t_{off}	Turn-off Time	$I_{B1} = -I_{B2} = 0.05\text{ A}$			1		μs

* Pulsed : pulse duration = 300 μs , duty cycle = 1.5%.

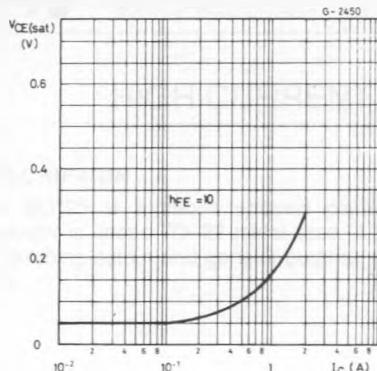
Safe Operating Areas.



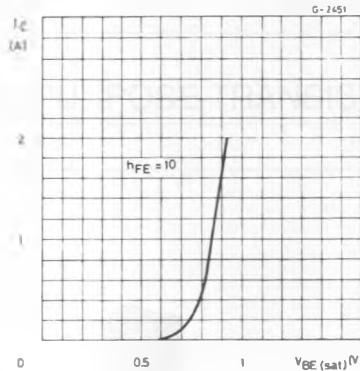
DC Current Gain.



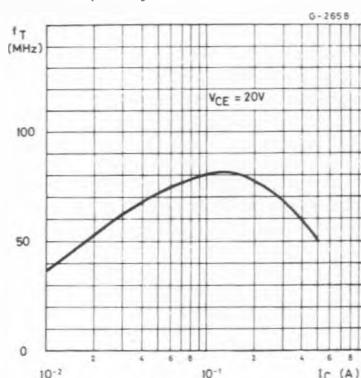
Collector-emitter Saturation Voltage.



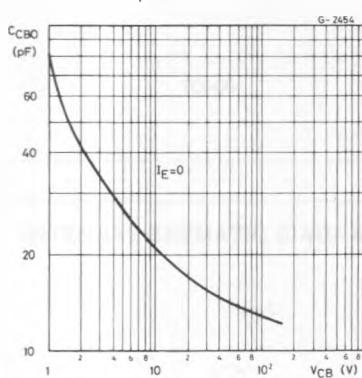
Base-emitter Saturation Voltage.



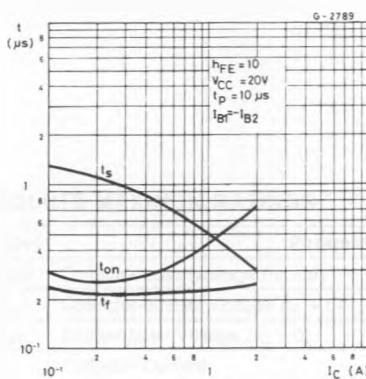
Transition Frequency.



Collector-base Capacitance.



Saturated Switching Characteristics.



Power Rating Chart.

