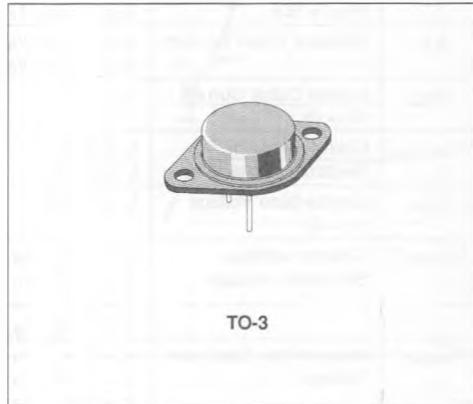
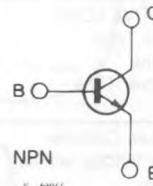


FAST SWITCHING POWER TRANSISTOR

- HIGH EFFICIENCY SWITCHING
- VERY LOW SATURATION VOLTAGE
- RECTANGULAR SAFE OPERATING AREA
- WIDE ACCIDENTAL OVERLOAD AREA



INTERNAL SCHEMATIC DIAGRAM



DESCRIPTION

Suitable for motor drives, SMPS converters, uninterruptable power supply operating low voltage supply.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CEV}	Collector-emitter Voltage ($V_{BE} = -1.5V$)	200	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	125	V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	7	V
I_E	Emitter Current	50	A
I_{EM}	Emitter Peak Current	150	A
I_B	Base Current	10	A
I_{BM}	Base Peak Current	30	A
P_{tot}	Total Dissipation at $T_c < 25^\circ C$	300	W
T_{sig}	Storage Temperature	-65 to 200	°C
T_J	Max. Operating Junction Temperature	200	°C

THERMAL DATA

$R_{\text{thj,case}}$	Thermal Resistance Junction-case	Max	0.58	°C/W
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ELECTRICAL CHARACTERISTICS ($T_{\text{case}} = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit	
I_{CER}	Collector Cutoff Current ($R_{\text{BE}} = 5\Omega$)	$V_{\text{CE}} = V_{\text{CEV}}$ $V_{\text{CE}} = V_{\text{CEV}} \quad T_c = 100^\circ\text{C}$			1 5	mA mA	
I_{CEV}	Collector Cutoff Current	$V_{\text{CE}} = V_{\text{CEV}} \quad V_{\text{BE}} = -1.5\text{V}$ $V_{\text{CE}} = V_{\text{CEV}} \quad V_{\text{BE}} = -1.5\text{V} \quad T_c = 100^\circ\text{C}$			1 4	mA mA	
I_{EBO}	Emitter Cutoff Current ($I_c = 0$)	$V_{\text{EB}} = 5\text{V}$			1	mA	
$V_{\text{CEO(sus)}}^*$	Collector Emitter Sustaining Voltage	$I_c = 0.2\text{A}$ $L = 25\text{mH}$	125			V	
V_{EBO}	Emitter-base Voltage ($I_c = 0$)	$I_E = 50\text{mA}$	7			V	
$V_{\text{CE(sat)}}^*$	Collector-emitter Saturation Voltage	$I_c = 50\text{A} \quad I_B = 2.5\text{A}$ $I_c = 100\text{A} \quad I_B = 10\text{A}$ $I_c = 50\text{A} \quad I_B = 2.5\text{A} \quad T_j = 100^\circ\text{C}$ $I_c = 100\text{A} \quad I_B = 10\text{A} \quad T_j = 100^\circ\text{C}$			0.9 0.9 1.2 1.5	V V V V	
$V_{\text{BE(sat)}}^*$	Base-emitter Saturation Voltage	$I_c = 50\text{A} \quad I_B = 2.5\text{A}$ $I_c = 100\text{A} \quad I_B = 10\text{A}$ $I_c = 50\text{A} \quad I_B = 2.5\text{A} \quad T_j = 100^\circ\text{C}$ $I_c = 100\text{A} \quad I_B = 10\text{A} \quad T_j = 100^\circ\text{C}$			1.4 2 1.4 2.1	V V V V	
dI_c/dt	Rate of Rise of on-state Collector Current	$V_{\text{CC}} = 100\text{V} \quad R_C = 0$ $t_p = 3\mu\text{s}$ See fig. 1	$I_{B1} = 5\text{A}$ $T_j = 100^\circ\text{C}$	180			A/ μs
t_s t_f t_c	INDUCTIVE LOAD Storage Time Fall Time Crossover Time	$V_{\text{CC}} = 90\text{V} \quad V_{\text{clamp}} = 125\text{V}$ $I_c = 50\text{A} \quad I_{B1} = 2.5\text{A}$ $V_{\text{BB}} = -5\text{V} \quad L_C = 80\mu\text{H}$ $R_{B2} = 1\Omega \quad T_j = 100^\circ\text{C}$ See fig. 2			2 0.2 0.35	μs μs μs	
V_{CEW}	Maximum Collector Emitter Voltage without Snubber	$V_{\text{CC}} = 90\text{V} \quad I_{\text{CWoff}} = 150\text{A}$ $V_{\text{BB}} = -5\text{V} \quad I_{B1} = 10\text{A}$ $L_C = 30\mu\text{H} \quad R_{B2} = 1\Omega$ $T_j = 125^\circ\text{C} \quad \text{See fig. 2}$	125			V	

* Pulsed : Pulse duration = 3 μs , duty cycle = 2 %.

Figure 1 : Turn-on Switching Characteristics of the Transistor.

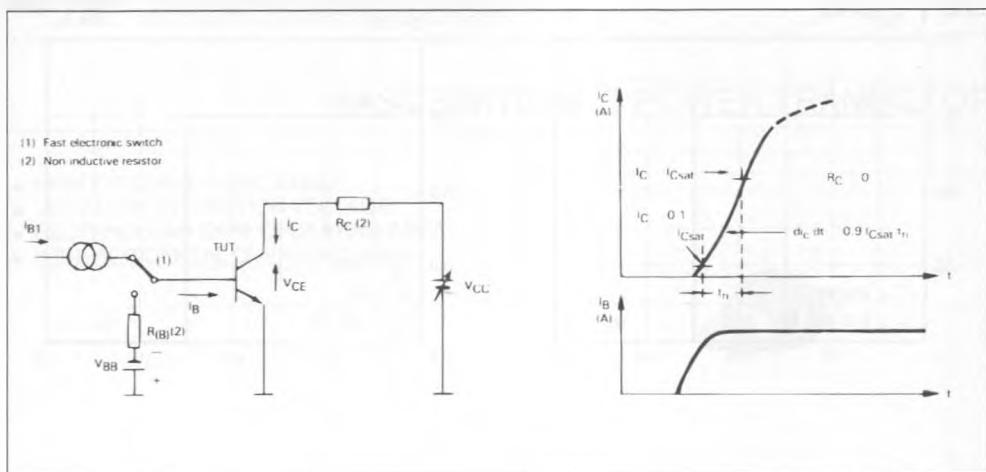
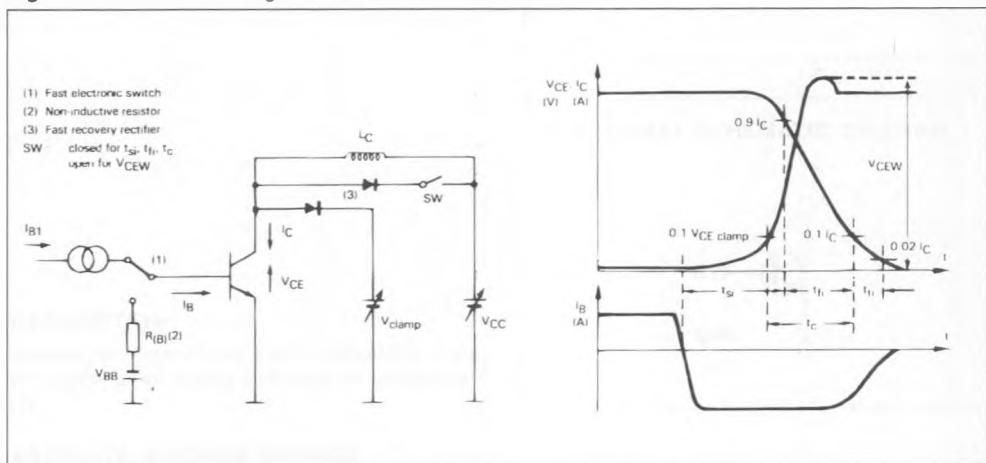
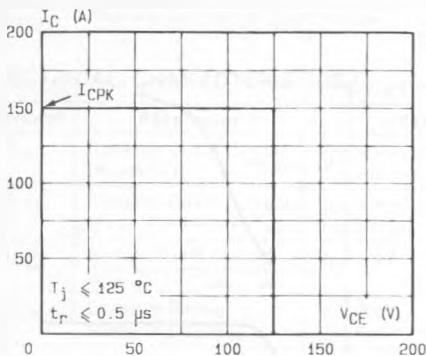


Figure 2 : Turn-off Switching Characteristics of the Transistor.



Forward biased Safe Operating Area (FBSOA).



Reverse biased Safe Operating Area (RBSOA).

