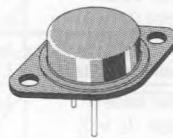


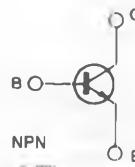
FAST SWITCHING POWER TRANSISTOR

- FAST SWITCHING TIMES
- LOW SWITCHING LOSSES
- VERY LOW SATURATION VOLTAGE AND HIGH GAIN FOR REDUCED LOAD OPERATION



TO-3

INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CEV}	Collector-emitter Voltage ($V_{BE} = -1.5V$)	400	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	300	V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	7	V
I_C	Collector Current	12	A
I_{CM}	Collector Peak Current	18	A
I_B	Base Current	2.5	A
I_{BM}	Base Peak Current	4	A
P_{tot}	Total Dissipation at $T_c < 25^\circ\text{C}$	120	W
T_{stg}	Storage Temperature	-65 to 200	°C
T_J	Max. Operating Junction Temperature	200	°C

THERMAL DATA

$R_{\text{thj-case}}$	Thermal Resistance Junction-case	Max	1.46	$^{\circ}\text{C/W}$
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ELECTRICAL CHARACTERISTICS ($T_{\text{case}} = 25^{\circ}\text{C}$ unless otherwise specified)

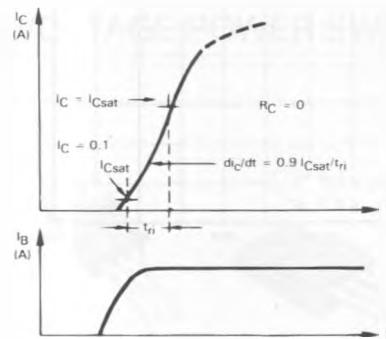
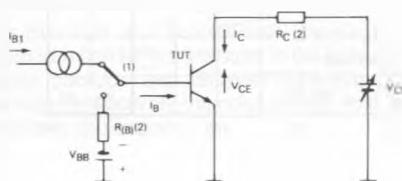
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CEA}	Collector Cutoff Current ($R_{\text{BE}} = 10\Omega$)	$V_{\text{CE}} = V_{\text{CEV}}$ $V_{\text{CE}} = V_{\text{CEV}} \quad T_c = 100^{\circ}\text{C}$			0.5 2.5	mA mA
I_{CEV}	Collector Cutoff Current	$V_{\text{CE}} = V_{\text{CEV}} \quad V_{\text{BE}} = -1.5\text{V}$ $V_{\text{CE}} = V_{\text{CEV}} \quad V_{\text{BE}} = -1.5\text{V} \quad T_c = 100^{\circ}\text{C}$			0.5 2	mA mA
I_{EBO}	Emitter Cutoff Current ($I_C = 0$)	$V_{\text{EB}} = 5\text{V}$			1	mA
$V_{\text{CEO(sus)}}^*$	Collector Emitter Sustaining Voltage	$I_C = 0.2\text{A}$ $L = 25\text{mH}$	300			V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	$I_E = 50\text{mA}$	7			V
$V_{\text{CE(sat)}}^*$	Collector-emitter Saturation Voltage	$I_C = 4\text{A} \quad I_B = 0.4\text{A}$ $I_C = 4\text{A} \quad I_B = 0.4\text{A} \quad T_j = 100^{\circ}\text{C}$			0.9 1.9	V V
$V_{\text{BE(sat)}}^*$	Base-emitter Saturation Voltage	$I_C = 4\text{A} \quad I_B = 0.4\text{A}$ $I_C = 4\text{A} \quad I_B = 0.4\text{A} \quad T_j = 100^{\circ}\text{C}$			1.3 1.5	V V
dI_c/dt	Rate of Rise of on-state Collector Current	$V_{\text{CC}} = 250\text{V} \quad R_C = 0 \quad I_{B1} = 0.6\text{A}$ $t_p = 3\mu\text{s} \quad T_j = 100^{\circ}\text{C}$ See fig. 1	25			A/ μs

INDUCTIVE LOAD

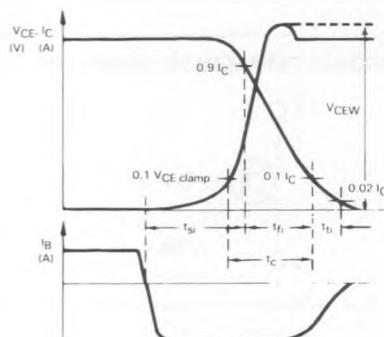
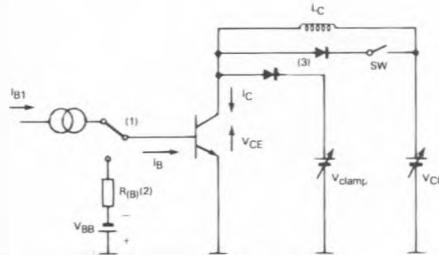
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t_s t_f t_c	Storage Time Fall Time Crossover Time	$V_{\text{CC}} = 250\text{V}$ $I_C = 4\text{A}$ $V_{\text{BB}} = -5\text{V}$ $L_C = 3.1\text{mH}$ see fig. 2			3 0.4 0.7	μs μs μs
V_{CEW}	Maximum Collector Emitter Voltage without Snubber	$V_{\text{CC}} = 50\text{V}$ $V_{\text{BB}} = -5\text{V}$ $L_C = 0.42\text{mH}$ $T_j = 125^{\circ}\text{C}$	300			V

Figure 1 : Turn-on Switching Characteristics of the Transistor.

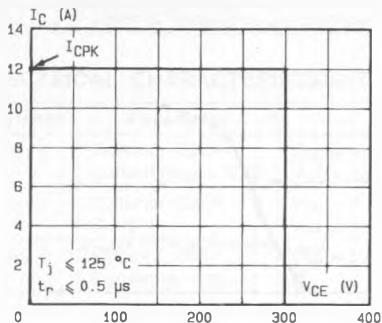
- (1) Fast electronic switch
- (2) Non-inductive resistor

**Figure 2 : Turn-off Switching Characteristics of the Transistor.**

- (1) Fast electronic switch
 - (2) Non-inductive resistor
 - (3) Fast recovery rectifier
- SW : – closed for t_{si} , t_f , t_c
– open for V_{CEW}



Forward Biased Safe Operating Area (FBSOA).



Reverse Biased Safe Operating Area (RBSOA).

