

# **DC/DC Converter Applications**

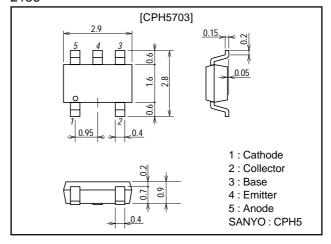
#### **Features**

- Composite type with a NPN transistor and a Schottky barrier diode contained in one package facilitating high-density mounting.
- The CPH5703 consists of two chips encapsulated in a package which are equivalent to the CPH3205 and the SB05-05CP, respectively.
- · Ultrasmall-sized package permitting applied sets to be made small and slim (0.9mm).

## **Package Dimensions**

unit:mm

2156



## **Specifications**

#### **Absolute Maximum Ratings** at $Ta = 25^{\circ}C$

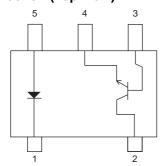
Parameter	Symbol	Conditions	Ratings	Unit
[TR]	'			
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCEO		50	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	Ic		3	А
Collector Current (Pulse)	I <sub>CP</sub>		6	Α
Base Current	IB		600	mA
Collector Dissipation	PC	Mounted on a ceramic board (600mm²×0.8mm)	0.9	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +125	°C
[SBD]			-	
Repetitive Peak Reverse Voltage	VRRM		50	V
Non-repetitive Peak Reverse Surge Voltage	VRSM		55	V
Average Output Current	Io		500	mA
Surge Current	IFSM	50Hz sine wave, 1 cycle	5	А
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

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#### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Onit
[TR]	•					
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =40V, I <sub>E</sub> =0			1	μA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =4V, I <sub>C</sub> =0			1	μA
DC Current Gain	hFE	V <sub>CE</sub> =2V, I <sub>C</sub> =100mA	200		560	
Gain-Bandwidth Product	fT	V <sub>CE</sub> =10V, I <sub>C</sub> =500mA		380		MHz
Output Capacitance	Cob	V <sub>CB</sub> =10V, f=1MHz		13		pF
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub> 1	I <sub>C</sub> =1.0A, I <sub>B</sub> =50mA		80	120	mV
	V <sub>CE(sat)</sub> <sup>2</sup>	I <sub>C</sub> =2.0A, I <sub>B</sub> =100mA		140	210	mV
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =1.0A, I <sub>B</sub> =50mA		0.9	1.2	V
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =10μA, I <sub>E</sub> =0	60			V
Collector-to-Emitter Breakdown Voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> =1mA, R <sub>BE</sub> =∞	50			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I <sub>E</sub> =10μA, I <sub>C</sub> =0	6			V
Turn-ON Time	ton	See specified Test Circuit.		35		ns
Storage Time	t <sub>stg</sub>	See specified Test Circuit.		300		ns
Turn-OFF Time	t <sub>f</sub>	See specified Test Circuit.		22		ns
[SBD]						
Reverse Voltage	VR	I <sub>R</sub> =200μA	50			V
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =500mA			0.55	V
Reverse Current	I <sub>R</sub>	V <sub>R</sub> =25V			50	μΑ
Interterminal Capacitance	С	V <sub>R</sub> =10V, f=1MHz		22		pF
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =I <sub>R</sub> =100mA, See specified Test Circuit.			10	ns
Thermal Resistance	Rthj-a	Mounted on a ceramic board (600mm²×0.8mm)		151		°C/W

## **Electrical Connection (Top view)**



## 1 : Cathode

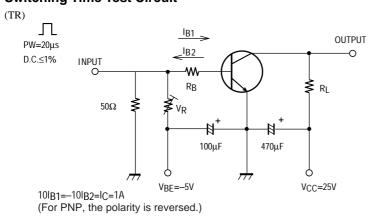
2 : Collector

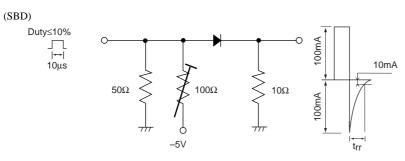
3 : Base

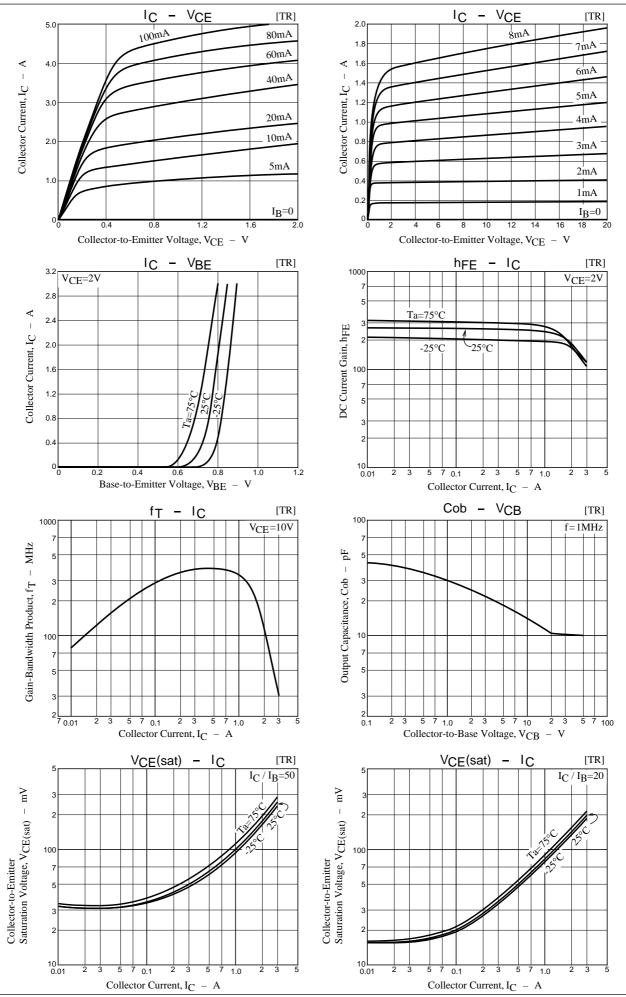
4 : Emitter

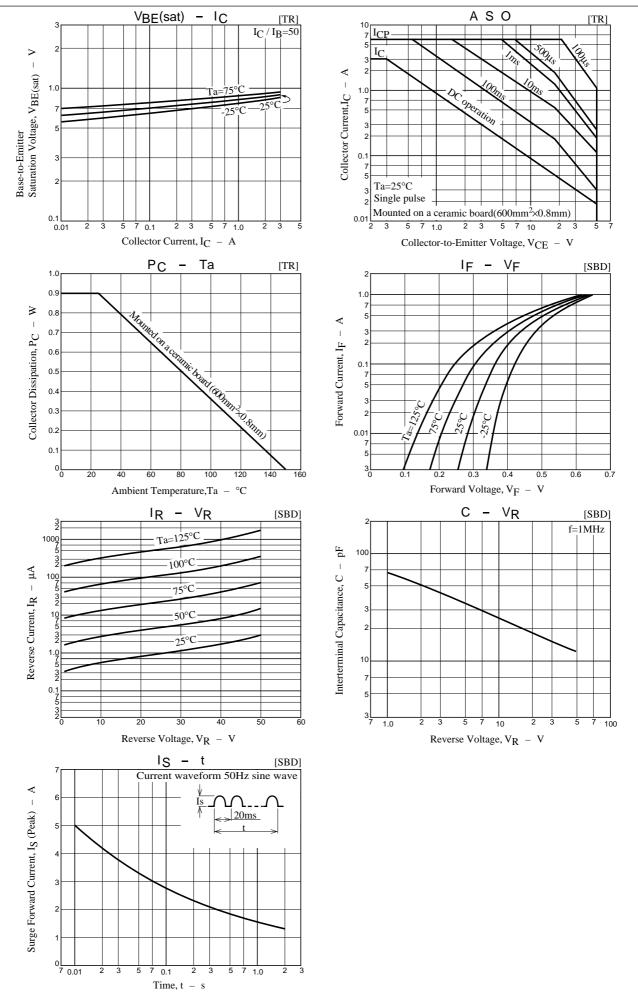
5 : Anode

## **Switching Time Test Circuit**









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