



## P-Channel NexFET™ Power MOSFET

 Check for Samples: [CSD25301W1015](#)

### FEATURES

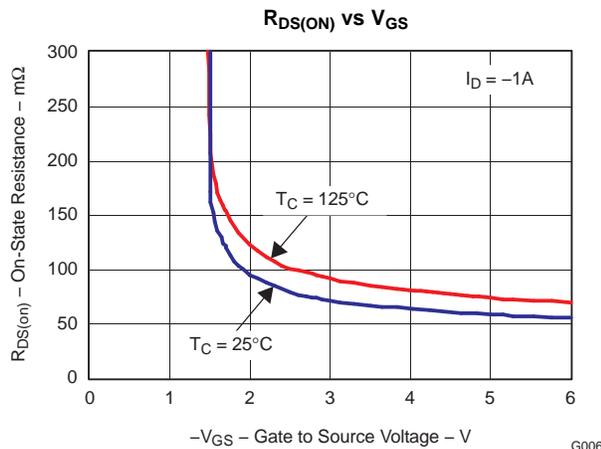
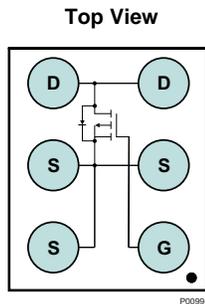
- Ultra Low Qg and Qgd
- Small Footprint
- Low Profile 0.62mm Height
- Pb Free
- RoHS Compliant
- Halogen Free
- CSP 1 × 1.5 mm Wafer Level Package

### APPLICATIONS

- Battery Management
- Load Switch
- Battery Protection

### DESCRIPTION

The device has been designed to deliver the lowest on resistance and gate charge in the smallest outline possible with excellent thermal characteristics in an ultra low profile.



### PRODUCT SUMMARY

V <sub>DS</sub>	Drain to Source Voltage	-20	V
Q <sub>g</sub>	Gate Charge Total (4.5V)	1.9	nC
Q <sub>gd</sub>	Gate Charge Gate to Drain	0.4	nC
R <sub>DS(on)</sub>	Drain to Source On Resistance	V <sub>GS</sub> = -1.5V	175 mΩ
		V <sub>GS</sub> = -2.5V	80 mΩ
		V <sub>GS</sub> = -4.5V	62 mΩ
V <sub>GS(th)</sub>	Voltage Threshold	-0.75	V

### ORDERING INFORMATION

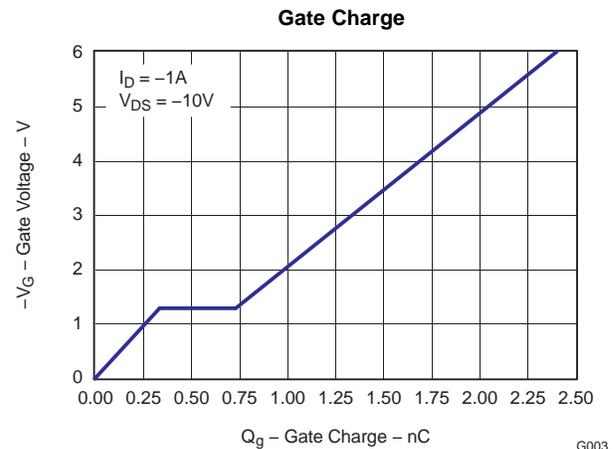
Device	Package	Media	Qty	Ship
CSD25301W1015	1 × 1.5 Wafer Level Package	7-inch reel	3000	Tape and Reel

### ABSOLUTE MAXIMUM RATINGS

T <sub>A</sub> = 25°C unless otherwise stated		VALUE	UNIT
V <sub>DS</sub>	Drain to Source Voltage	-20	V
V <sub>GS</sub>	Gate to Source Voltage	±8	V
I <sub>D</sub>	Continuous Drain Current, T <sub>C</sub> = 25°C <sup>(1)</sup>	-2.2	A
I <sub>DM</sub>	Pulsed Drain Current, T <sub>A</sub> = 25°C <sup>(2)</sup>	-8.8	A
P <sub>D</sub>	Power Dissipation <sup>(1)</sup>	1.5	W
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 150	°C

(1) R<sub>θJA</sub> = 85°C/W on 1in<sup>2</sup> Cu (2 oz.) on 0.060" thick FR4 PCB.

(2) Pulse width ≤300μs, duty cycle ≤2%



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

## ELECTRICAL CHARACTERISTICS

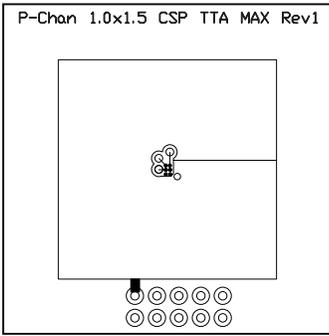
( $T_A = 25^\circ\text{C}$  unless otherwise stated)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain to Source Voltage	$V_{GS} = 0V, I_D = -250\mu A$	-20			V
$I_{DSS}$	Drain to Source Leakage Current	$V_{GS} = 0V, V_{DS} = -16V$			-1	$\mu A$
$I_{GSS}$	Gate to Source Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 8V$			-100	nA
$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.4	-0.75	-1	V
$R_{DS(on)}$	Drain to Source On Resistance	$V_{GS} = -1.5V, I_D = -1A$		175	220	m $\Omega$
		$V_{GS} = -2.5V, I_D = -1A$		80	100	m $\Omega$
		$V_{GS} = -4.5V, I_D = -1A$		62	75	m $\Omega$
$g_{fs}$	Transconductance	$V_{DS} = -10V, I_D = -1A$		5.8		S
<b>Dynamic Characteristics</b>						
$C_{ISS}$	Input Capacitance	$V_{GS} = 0V, V_{DS} = -10V, f = 1MHz$		210	270	pF
$C_{OSS}$	Output Capacitance			90	120	pF
$C_{RSS}$	Reverse Transfer Capacitance			30	40	pF
$Q_g$	Gate Charge Total (-4.5V)			1.9	2.5	nC
$Q_{gd}$	Gate Charge Gate to Drain	$V_{DS} = -10V, I_D = -1A$		0.4		nC
$Q_{gs}$	Gate Charge Gate to Source			0.35		nC
$Q_{g(th)}$	Gate Charge at $V_{th}$			0.17		nC
$Q_{OSS}$	Output Charge	$V_{DS} = -9.8V, V_{GS} = 0V$		1.7		nC
$t_{d(on)}$	Turn On Delay Time	$V_{DS} = -10V, V_{GS} = -4.5V, I_D = -1A$ $R_G = 20\Omega$		4		ns
$t_r$	Rise Time			2		ns
$t_{d(off)}$	Turn Off Delay Time			29		ns
$t_f$	Fall Time			12		ns
<b>Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage	$I_S = -1A, V_{GS} = 0V$	-0.75		-1	V
$Q_{rr}$	Reverse Recovery Charge	$V_{dd} = -9.8V, I_F = -1A, di/dt = 200A/\mu s$		0.9		nC
$t_{rr}$	Reverse Recovery Time	$V_{dd} = -9.8V, I_F = -1A, di/dt = 200A/\mu s$		8.2		ns

## THERMAL CHARACTERISTICS

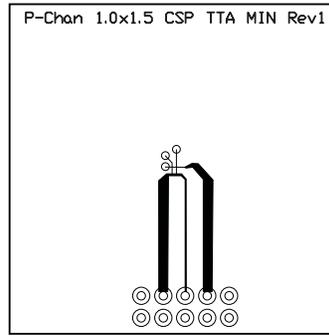
( $T_A = 25^\circ\text{C}$  unless otherwise stated)

PARAMETER		MIN	TYP	MAX	UNIT
$R_{\theta JA}$	Thermal Resistance Junction to Ambient (Minimum Cu area)			270	$^\circ\text{C/W}$
	Thermal Resistance Junction to Ambient (1 in <sup>2</sup> Cu area)			105	$^\circ\text{C/W}$



Max  $R_{\theta JA} = 105^{\circ}\text{C/W}$   
when mounted on 1  
 $\text{inch}^2$  of 2 oz. Cu.

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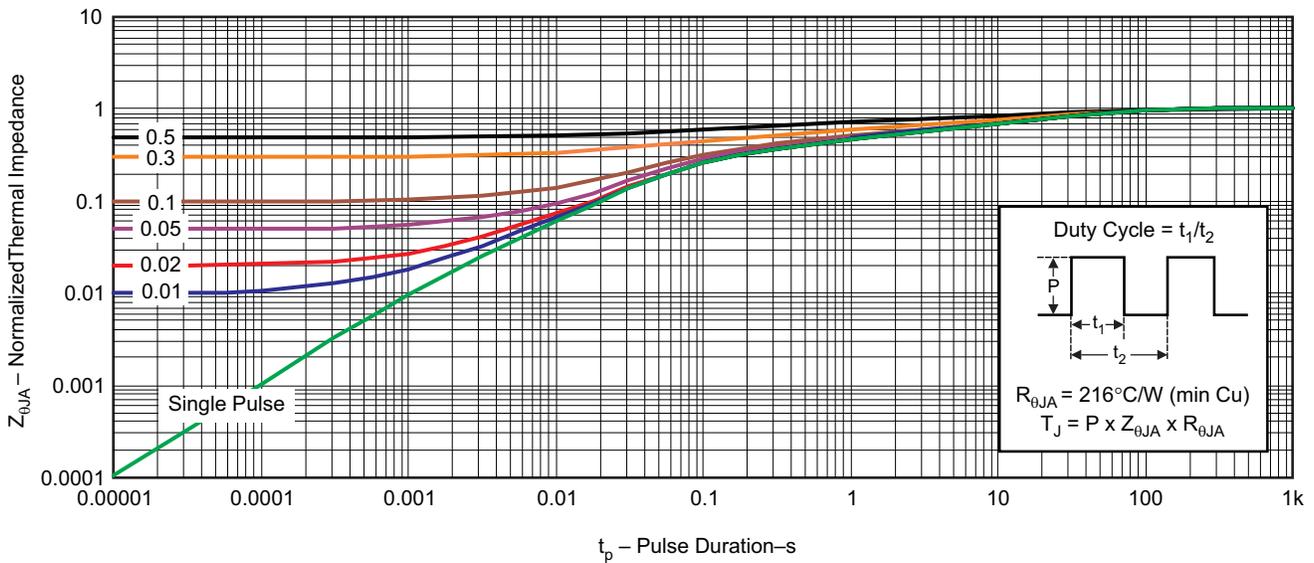


Max  $R_{\theta JA} = 270^{\circ}\text{C/W}$   
when mounted on  
minimum pad area of 2  
oz. Cu.

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### TYPICAL MOSFET CHARACTERISTICS

( $T_A = 25^{\circ}\text{C}$  unless otherwise stated)

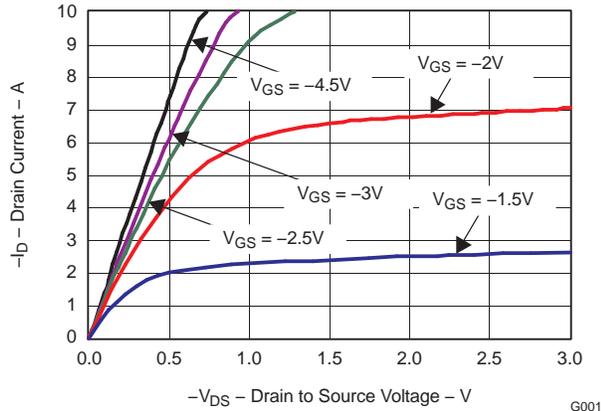


G012

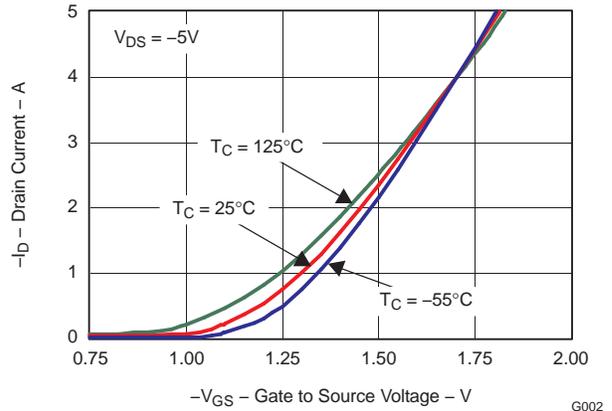
Figure 1. Transient Thermal Impedance

**TYPICAL MOSFET CHARACTERISTICS (continued)**

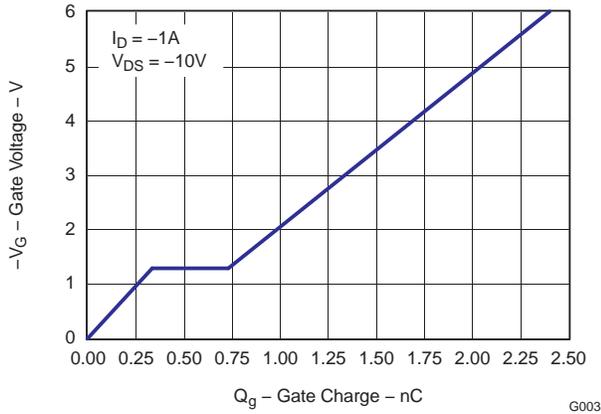
( $T_A = 25^\circ\text{C}$  unless otherwise stated)



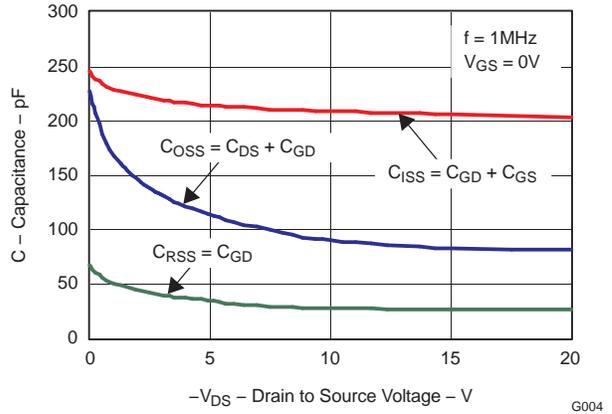
**Figure 2. Saturation Characteristics**



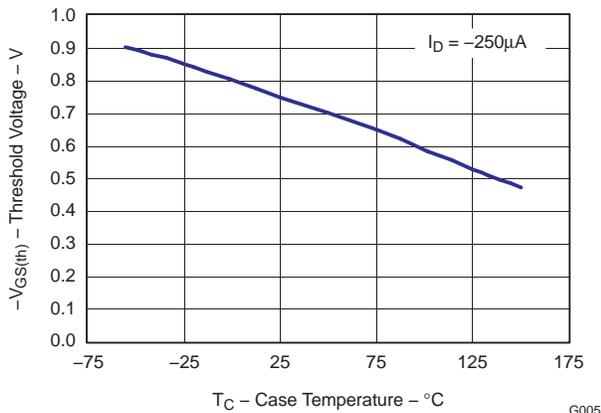
**Figure 3. Transfer Characteristics**



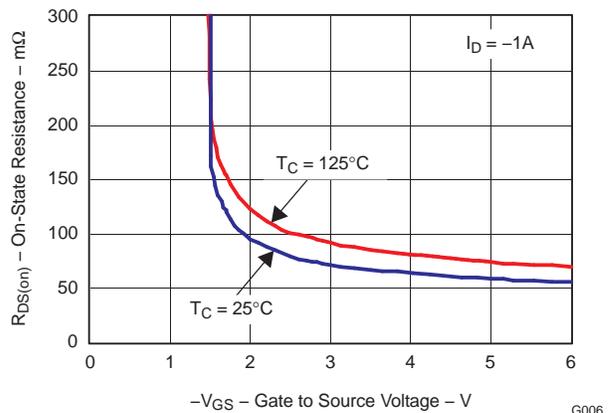
**Figure 4. Gate Charge**



**Figure 5. Capacitance**



**Figure 6. Threshold Voltage vs. Temperature**



**Figure 7. On Resistance vs. Gate Voltage**

TYPICAL MOSFET CHARACTERISTICS (continued)

( $T_A = 25^\circ\text{C}$  unless otherwise stated)

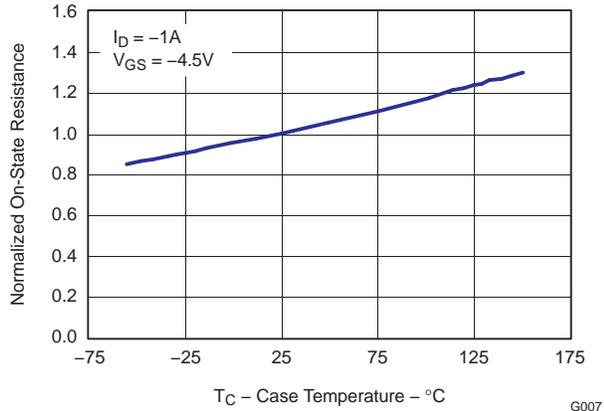


Figure 8. On Resistance vs. Temperature

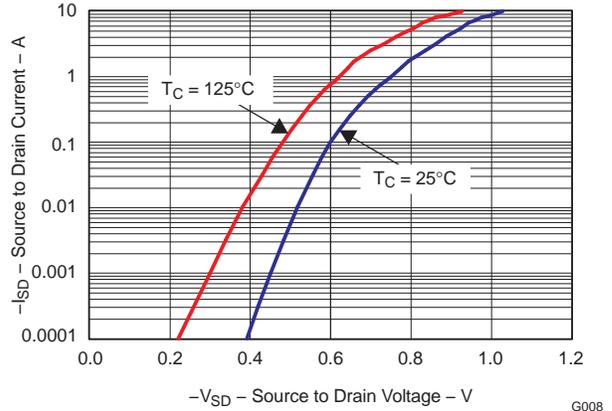


Figure 9. Typical Diode Forward Voltage

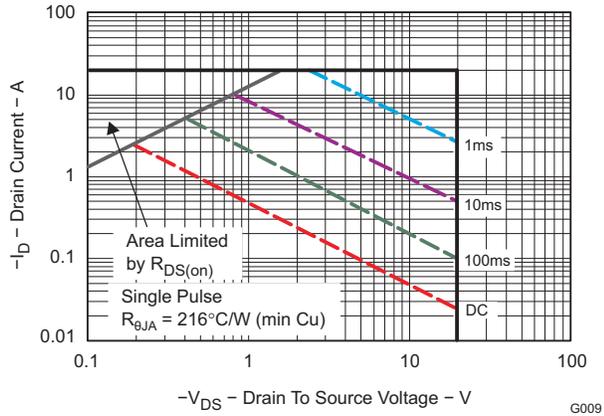


Figure 10. Maximum Safe Operating Area

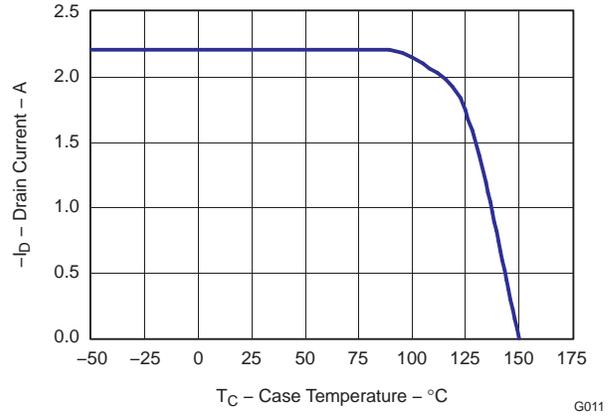
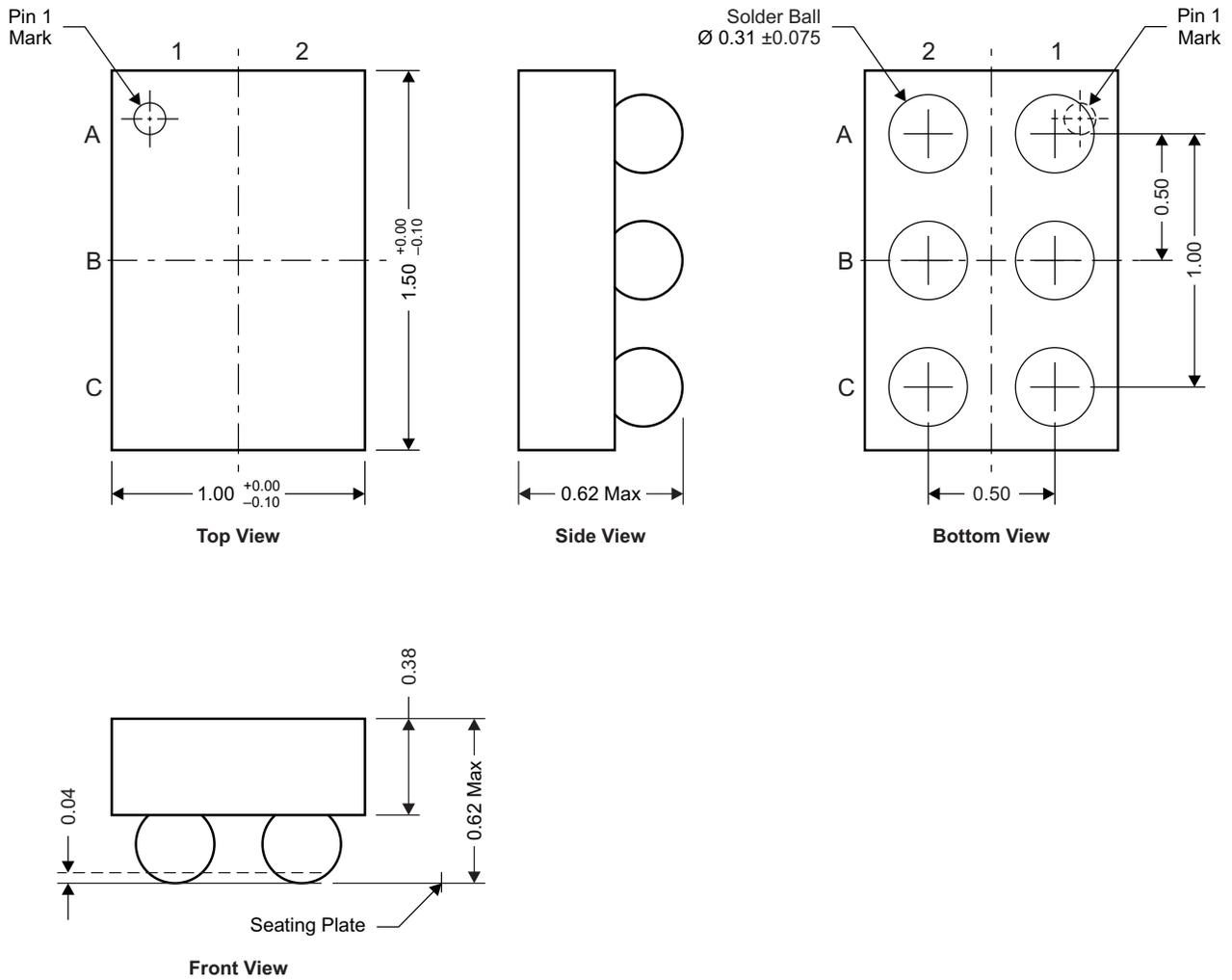


Figure 11. Maximum Drain Current vs. Temperature

**MECHANICAL DATA**

**CSD25301W1015 Package Dimensions**



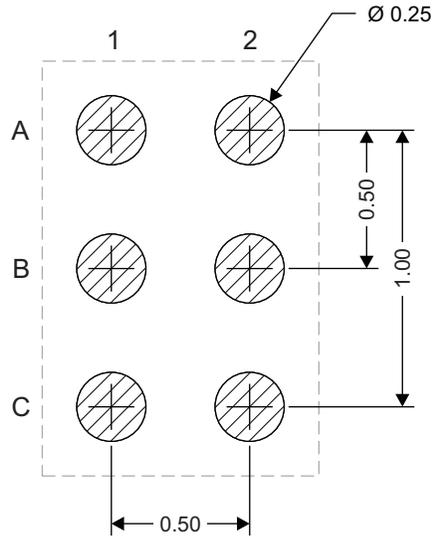
M0157-01

NOTE: All dimensions are in mm (unless otherwise specified)

**Pinout**

POSITION	DESIGNATION
C1, C2	Drain
A1	Gate
A2, B1, B2	Source

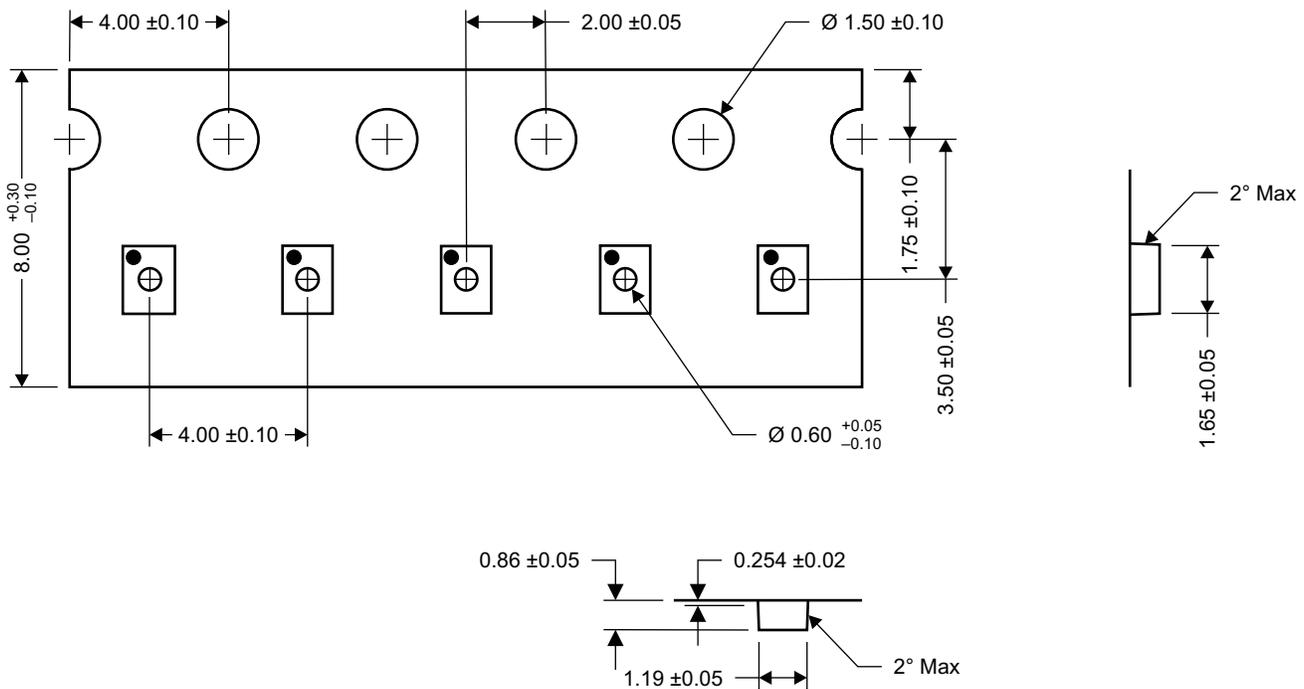
**Land Pattern Recommendation**



M0158-01

NOTE: All dimensions are in mm (unless otherwise specified)

**Tape and Reel Information**



M0159-01

NOTE: All dimensions are in mm (unless otherwise specified)

## REVISION HISTORY

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**Changes from Original (August 2009) to Revision A** **Page**

- Replaced incorrect label:  $R_{\theta JC}$  with  $R_{\theta JA}$  in the THERMAL CHARACTERISTICS table. .... [2](#)

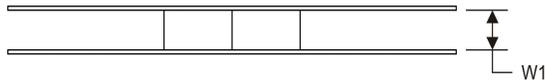
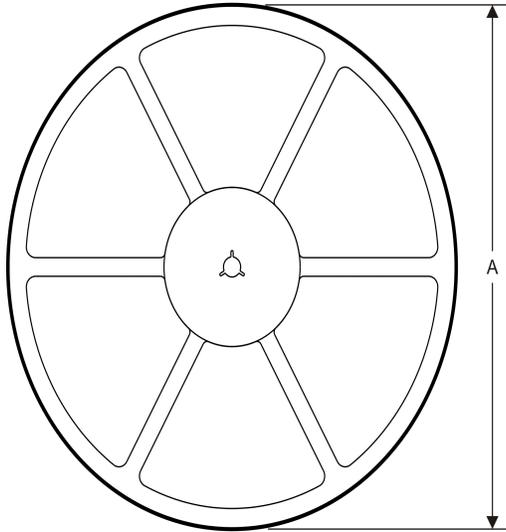
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**Changes from Revision A (August 2010) to Revision B** **Page**

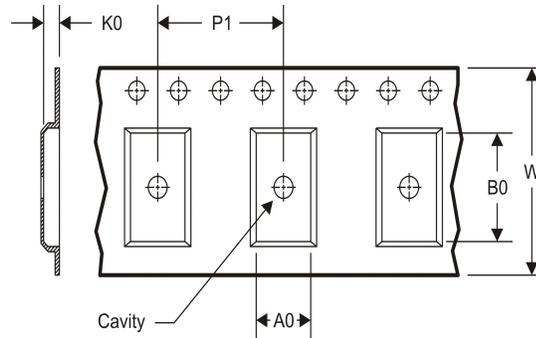
- Deleted the Package Marking Information section ..... [7](#)

**TAPE AND REEL INFORMATION**

**REEL DIMENSIONS**



**TAPE DIMENSIONS**



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

**TAPE AND REEL INFORMATION**

\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
CSD25301W1015	DSBGA	YZC	6	3000	180.0	8.4	1.09	1.56	0.65	4.0	8.0	Q1

TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
CSD25301W1015	DSBGA	YZC	6	3000	210.0	185.0	35.0

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### Applications

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