

**N - CHANNEL 500V - 7.5Ω - 1.4A - IPAK
PowerMESH™ MOSFET**

| TYPE | V _{DSS} | R _{D(on)} | I _D |
|----------|------------------|--------------------|----------------|
| STD1NB50 | 500V | < 9 Ω | 1.4 A |

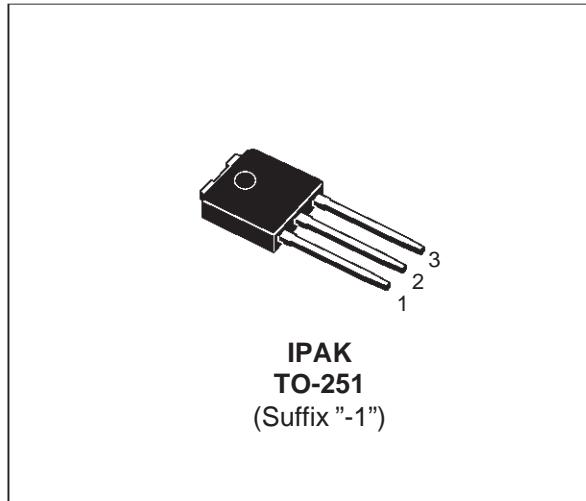
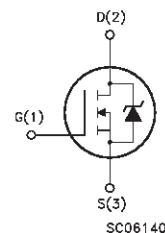
- TYPICAL R_{D(on)} = 7.5 Ω
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- VERY LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED
- FOR SMD DPAK VERSION CONTACT SALES OFFICE

DESCRIPTION

Using the latest high voltage MESH OVERLAY™ process, SGS-Thomson has designed an advanced family of power MOSFETs with outstanding performances. The new patent pending strip layout coupled with the Company's proprietary edge termination structure, gives the lowest R_{D(on)} per area, exceptional avalanche and dv/dt capabilities and unrivalled gate charge and switching characteristics.

APPLICATIONS

- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVE


INTERNAL SCHEMATIC DIAGRAM

ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | Unit |
|--------------------|---|------------|------|
| V _{DS} | Drain-source Voltage (V _{GS} = 0) | 500 | V |
| V _{DGR} | Drain-gate Voltage (R _{GS} = 20 kΩ) | 500 | V |
| V _{GS} | Gate-source Voltage | ± 36 | V |
| I _D | Drain Current (continuous) at T _c = 25 °C | 1.4 | A |
| I _D | Drain Current (continuous) at T _c = 100 °C | 0.91 | A |
| I _{DM(•)} | Drain Current (pulsed) | 5.6 | A |
| P _{tot} | Total Dissipation at T _c = 25 °C | 45 | W |
| | Derating Factor | 0.36 | W/°C |
| dv/dt(1) | Peak Diode Recovery voltage slope | 3.5 | V/ns |
| T _{stg} | Storage Temperature | -65 to 150 | °C |
| T _j | Max. Operating Junction Temperature | 150 | °C |

(•) Pulse width limited by safe operating area

(1) I_{SD} ≤ 1.4A, di/dt ≤ 150 A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

STD1NB50

THERMAL DATA

| | | | | |
|-----------------------|--|-----|------|------|
| R _{thj-case} | Thermal Resistance Junction-case | Max | 2.78 | °C/W |
| R _{thj-amb} | Thermal Resistance Junction-ambient | Max | 100 | °C/W |
| R _{thc-sink} | Thermal Resistance Case-sink | Typ | 1.5 | °C/W |
| T _I | Maximum Lead Temperature For Soldering Purpose | | 275 | °C |

AVALANCHE CHARACTERISTICS

| Symbol | Parameter | Max Value | Unit |
|-----------------|---|-----------|------|
| I _{AR} | Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max, δ < 1%) | 1.4 | A |
| E _{AS} | Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 50 V) | 40 | mJ |

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|----------------------|---|--|------|------|---------|----------|
| V _{(BR)DSS} | Drain-source Breakdown Voltage | I _D = 250 μA V _{GS} = 0 | 500 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current (V _{GS} = 0) | V _{DS} = Max Rating V _{DS} = Max Rating T _c = 125 °C | | | 1 50 | μA μA |
| I _{GSS} | Gate-body Leakage Current (V _{DS} = 0) | V _{GS} = ± 30 V | | | ± 100 | nA |

ON (*)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|----------------------|-----------------------------------|--|------|------|------|------|
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} I _D = 250 μA | 2 | 3 | 4 | V |
| R _{DSS(on)} | Static Drain-source On Resistance | V _{GS} = 10V I _D = 0.7 A | | 7.5 | 9 | Ω |
| I _{D(on)} | On State Drain Current | V _{DS} > I _{D(on)} × R _{DSS(on)max} V _{GS} = 10 V | 1.4 | | | A |

DYNAMIC

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|--|---|---|------|------------------|------------------|----------------|
| g _{fs} (*) | Forward Transconductance | V _{DS} > I _{D(on)} × R _{DSS(on)max} I _D = 0.7 A | 0.45 | 0.7 | | S |
| C _{iss} C _{oss} C _{rss} | Input Capacitance Output Capacitance Reverse Transfer Capacitance | V _{DS} = 25 V f = 1 MHz V _{GS} = 0 | | 150 24 2.5 | 200 32 3.3 | pF pF pF |

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------|--|--|------|-----------------|----------|----------------|
| $t_{d(on)}$ t_r | Turn-on Time Rise Time | $V_{DD} = 250 \text{ V}$ $I_D = 0.7 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 3) | | 8 8 | 12 12 | ns ns |
| Q_g Q_{gs} Q_{gd} | Total Gate Charge Gate-Source Charge Gate-Drain Charge | $V_{DD} = 400 \text{ V}$ $I_D = 1.4 \text{ A}$ $V_{GS} = 10 \text{ V}$ | | 9 5.5 2.4 | 13 | nC nC nC |

SWITCHING OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---------------------------------|---|--|------|----------------|----------------|----------------|
| $t_{r(Voff)}$ t_f t_c | Off-voltage Rise Time Fall Time Cross-over Time | $V_{DD} = 400 \text{ V}$ $I_D = 1.4 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 5) | | 20 22 30 | 28 31 42 | ns ns ns |

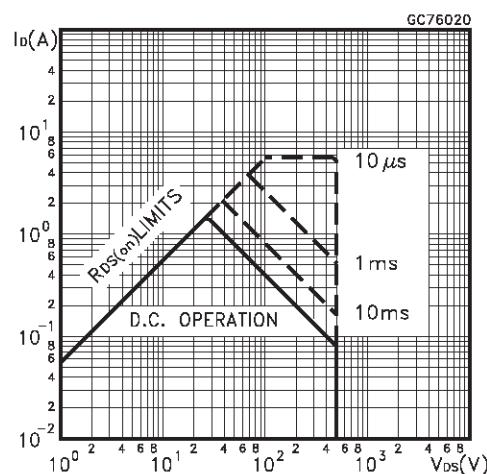
SOURCE DRAIN DIODE

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-----------------------------------|---|---|-------------------|------|------------|---------------|
| I_{SD} $I_{SDM}(\bullet)$ | Source-drain Current Source-drain Current (pulsed) | | | | 1.4 5.6 | A A |
| $V_{SD} (\ast)$ | Forward On Voltage | $I_{SD} = 1.4 \text{ A}$ $V_{GS} = 0$ | | | 1.6 | V |
| t_{rr} Q_{rr} I_{RRM} | Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current | $I_{SD} = 1.4 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 100 \text{ V}$ $T_j = 150 \text{ }^\circ\text{C}$ (see test circuit, figure 5) | 330 780 4.7 | | | ns nC A |

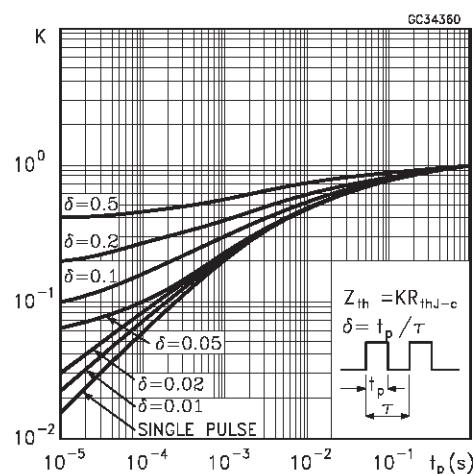
(*) Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %

(•) Pulse width limited by safe operating area

Safe Operating Area

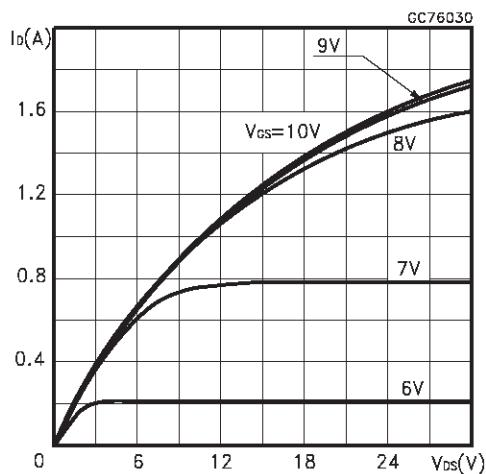


Thermal Impedance

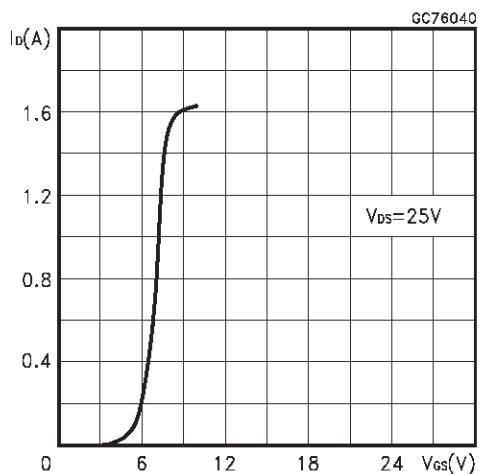


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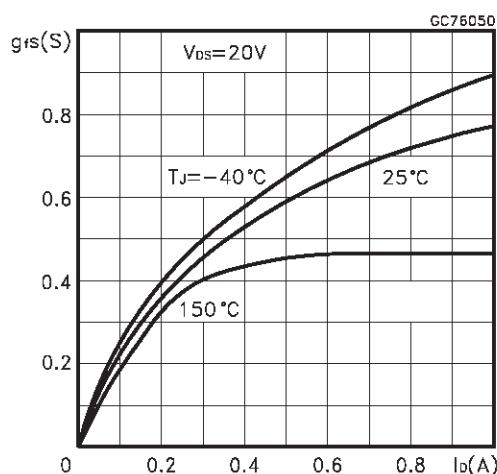
Output Characteristics



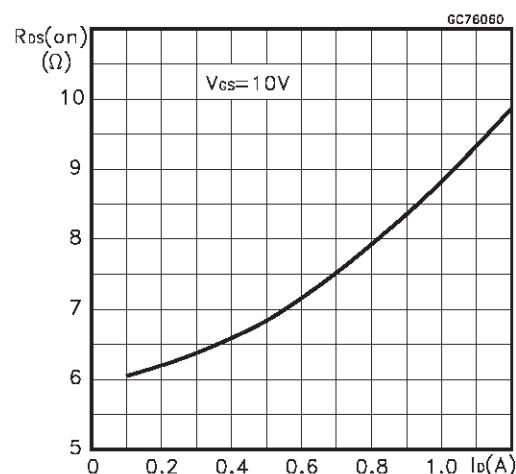
Transfer Characteristics



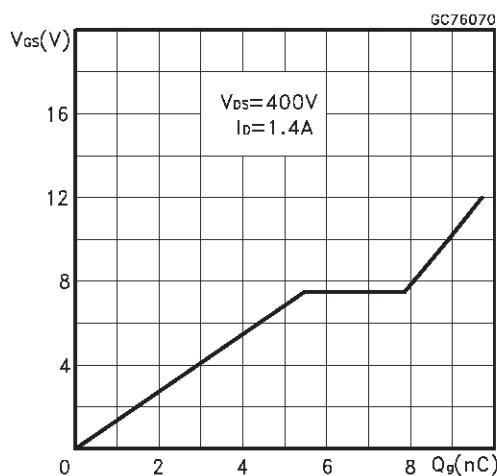
Transconductance



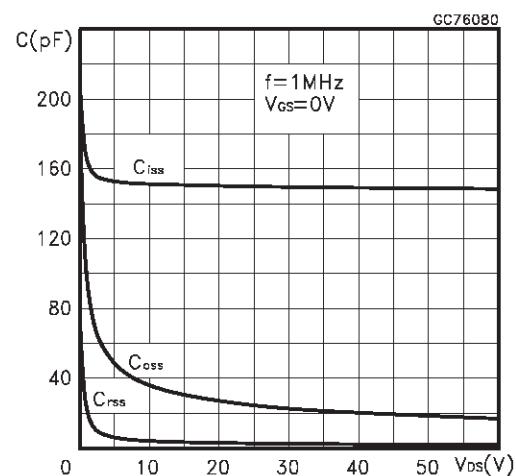
Static Drain-source On Resistance



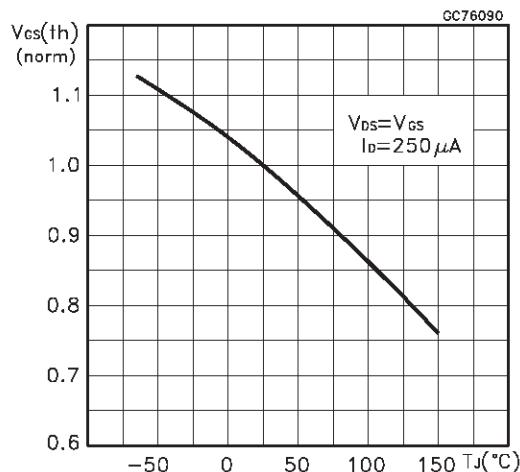
Gate Charge vs Gate-source Voltage



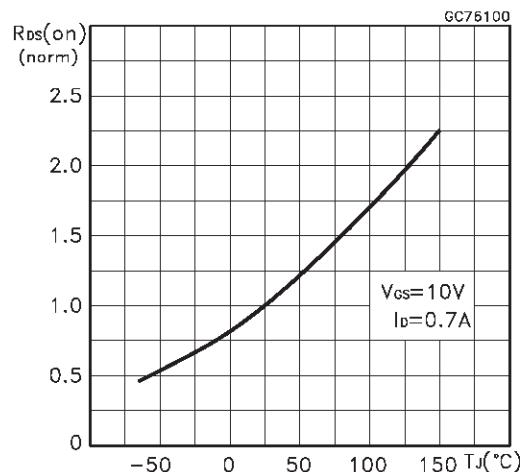
Capacitance Variations



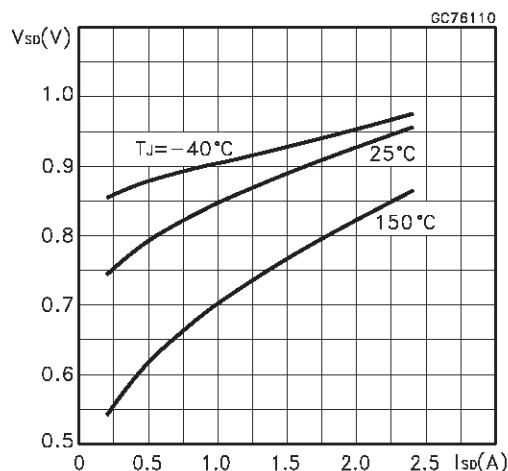
Normalized Gate Threshold Voltage vs Temperature



Normalized On Resistance vs Temperature



Source-drain Diode Forward Characteristics



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Fig. 1: Unclamped Inductive Load Test Circuit

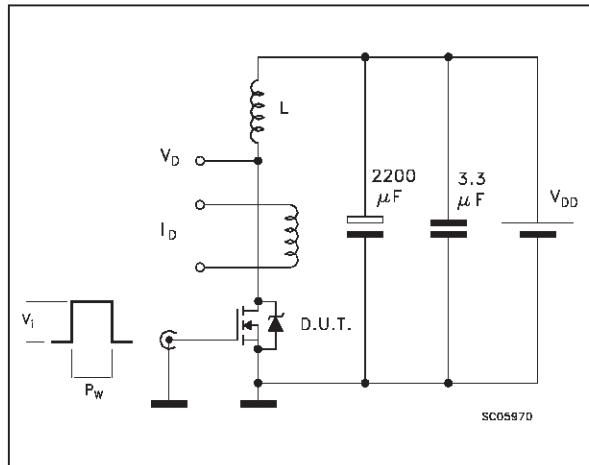


Fig. 2: Unclamped Inductive Waveform

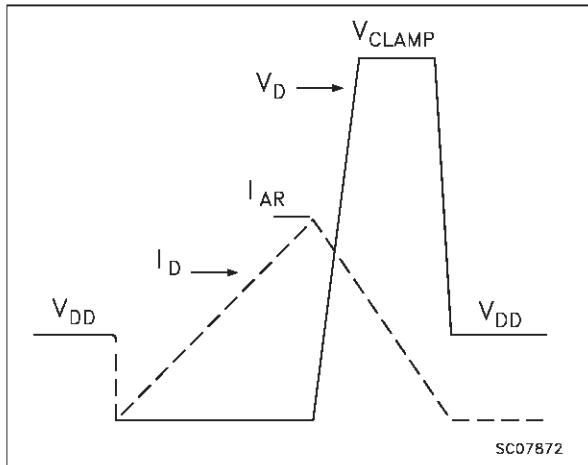


Fig. 3: Switching Times Test Circuits For Resistive Load

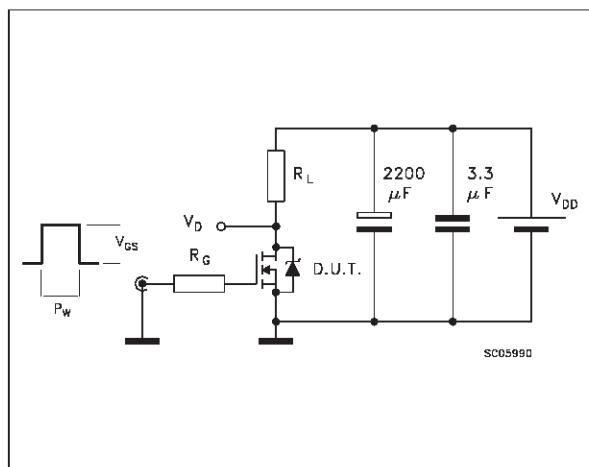


Fig. 4: Gate Charge test Circuit

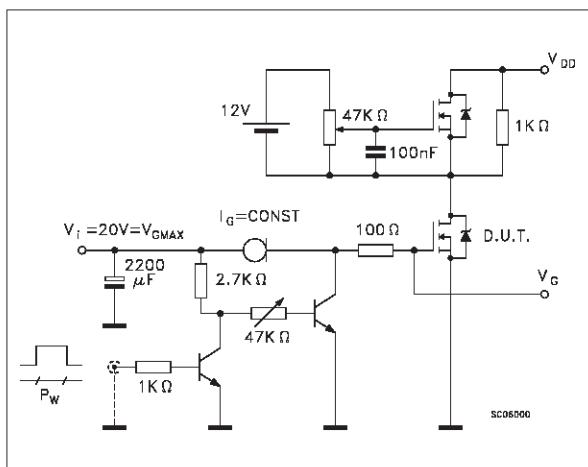
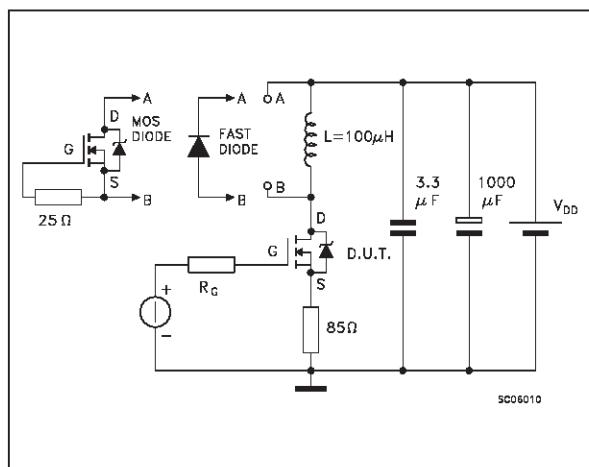
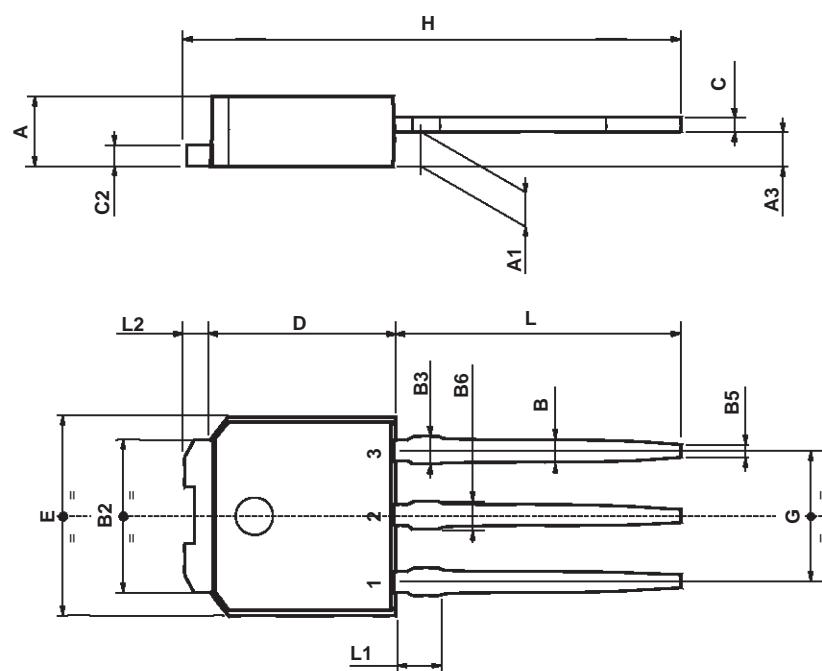


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times



TO-251 (IPAK) MECHANICAL DATA

| DIM. | mm | | | inch | | |
|------|------|------|------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 2.2 | | 2.4 | 0.086 | | 0.094 |
| A1 | 0.9 | | 1.1 | 0.035 | | 0.043 |
| A3 | 0.7 | | 1.3 | 0.027 | | 0.051 |
| B | 0.64 | | 0.9 | 0.025 | | 0.031 |
| B2 | 5.2 | | 5.4 | 0.204 | | 0.212 |
| B3 | | | 0.85 | | | 0.033 |
| B5 | | 0.3 | | | 0.012 | |
| B6 | | | 0.95 | | | 0.037 |
| C | 0.45 | | 0.6 | 0.017 | | 0.023 |
| C2 | 0.48 | | 0.6 | 0.019 | | 0.023 |
| D | 6 | | 6.2 | 0.236 | | 0.244 |
| E | 6.4 | | 6.6 | 0.252 | | 0.260 |
| G | 4.4 | | 4.6 | 0.173 | | 0.181 |
| H | 15.9 | | 16.3 | 0.626 | | 0.641 |
| L | 9 | | 9.4 | 0.354 | | 0.370 |
| L1 | 0.8 | | 1.2 | 0.031 | | 0.047 |
| L2 | | 0.8 | 1 | | 0.031 | 0.039 |



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