Complementary Silicon Power Transistors

These complementary silicon power transistors are designed for high–speed switching applications, such as switching regulators and high frequency inverters. The devices are also well–suited for drivers for high power switching circuits.

- Fast Switching
 - $t_f = 90 \text{ ns} (\text{Max})$
- Key Parameters Specified @ 100°C
- Low Collector–Emitter Saturation Voltage
 - $V_{CE(sat)} = 1.0 V (Max) @ 8.0 A$
- Complementary Pairs Simplify Circuit Designs

IAAIMUM RATINGS					
Rating	Symbol	Value	Unit		
Collector–Emitter Voltage	V _{CEO}	80	Vdc		
Collector–Emitter Voltage	V _{CEV}	100	Vdc		
Emitter Base Voltage	V _{EB}	7.0	Vdc		
Collector Current — Continuous — Peak (1)	I _С I _{СМ}	15 20	Adc		
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	83 0.67	Watts W/°C		
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to 150	°C		

MAXIMUM RATINGS



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ extsf{ heta}JC}$	1.5	°C/W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	62.5	°C/W
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	ΤL	275	°C

(2) Pulse Width \leq 6.0 ms, Duty Cycle \leq 50%.

NOTE: All polarities are shown for NPN transistors. For PNP transistors, reverse polarities.



D44VH

D45VH

15 AMPERE COMPLEMENTARY

SILICON

POWER TRANSISTORS

D44VH D45VH

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

	Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERIST	CS						•
Collector–Emitter Sus $(I_C = 25 \text{ mAdc}, I_B =$	Sustaining Voltage (2)		V _{CEO(sus)}	80	—	_	Vdc
(V _{CE} = Rated V _{CEV}			I _{CEV}			10 100	μAdc
Emitter Base Cutoff C (V _{EB} = 7.0 Vdc, I _C :			I _{EBO}		—	10	μAdc
ON CHARACTERISTIC	CS (2)						
DC Current Gain ($I_C = 2.0 \text{ Adc}, V_{CE} =$ ($I_C = 4.0 \text{ Adc}, V_{CE} =$			h _{FE}	35 20			_
Collector–Emitter Sat ($I_C = 8.0 \text{ Adc}, I_B = 0$ ($I_C = 8.0 \text{ Adc}, I_B = 0$ ($I_C = 15 \text{ Adc}, I_B = 3$).4 Adc)	D44VH10 D45VH10 D44VH10 D45VH10	V _{CE(sat)}	 	 	0.4 1.0 0.8 1.5	Vdc
).4 Adc)	D44VH10 D45VH10 D44VH10 D45VH10	V _{BE(sat)}		 	1.2 1.0 1.1 1.5	Vdc
OYNAMIC CHARACTE	RISTICS						
Current Gain Bandwidth Product (I _C = 0.1 Adc, V _{CE} = 10 Vdc, f = 20 MHz)		f _T		50	-	MHz	
Output Capacitance (V_{CB} = 10 Vdc, I_C = 0, f_{test} = 1.0 MHz) D44VH10 D45VH10		C _{ob}		120 275	_	pF	
SWITCHING CHARAC	TERISTICS						
Delay Time			t _d		_	50	ns
Rise Time	$(V_{CC} = 20 \text{ Vdc}, I_C = 8.0 \text{ Adc}, I_{B1} = I_{B2} = 0.8 \text{ Adc})$		t _r	_	—	250	
Storage Time			t _s		_	700	1
Fall Time			t _f	_	_	90	1

(2) Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2%.

D44VH D45VH

PACKAGE DIMENSIONS

TO-220AB CASE 221A-09 ISSUE AA



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
в	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
Κ	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
Ν	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
۷	0.045		1.15	
Ζ		0.080		2.04

D44VH D45VH

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