



ECH8410 — General-Purpose Switching Device Applications

N-Channel Silicon MOSFET

Features

- Low ON-resistance.
- 4V drive.
- Halogen free compliance.
- Protection diode in

Specifications

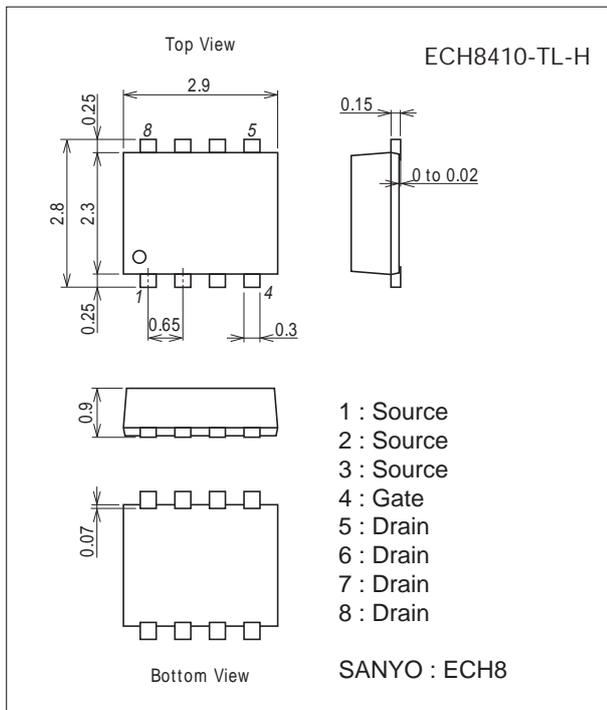
Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|------------------|---|-------------|------|
| Drain-to-Source Voltage | V _{DSS} | | 30 | V |
| Gate-to-Source Voltage | V _{GSS} | | ±20 | V |
| Drain Current (DC) | I _D | | 12 | A |
| Drain Current (Pulse) | I _{DP} | PW≤10μs, duty cycles≤1% | 60 | A |
| Allowable Power Dissipation | P _D | When mounted on ceramic substrate (900mm ² ×0.8mm) | 1.6 | W |
| Channel Temperature | T _{ch} | | 150 | °C |
| Storage Temperature | T _{stg} | | -55 to +150 | °C |

Package Dimensions

unit : mm (typ.)

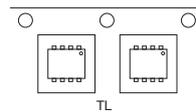
7011A-002



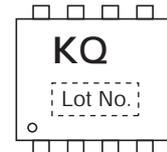
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

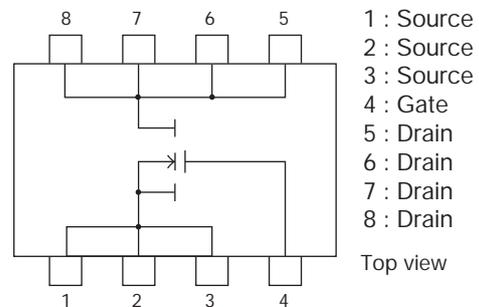
Packing Type : TL



Marking



Electrical Connection

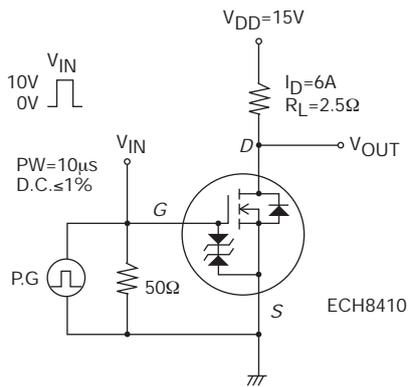


ECH8410

Electrical Characteristics at $T_a=25^\circ\text{C}$

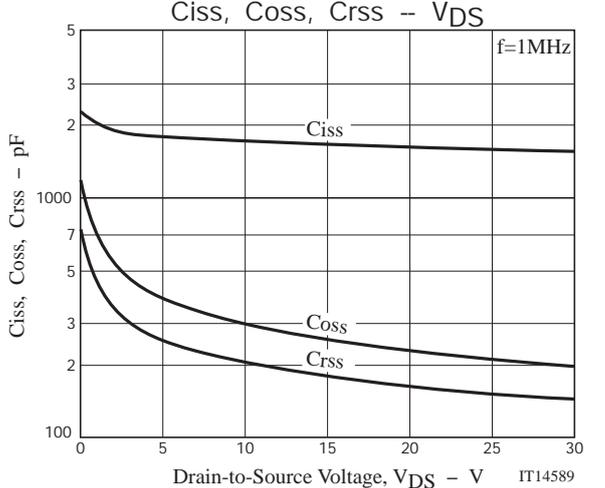
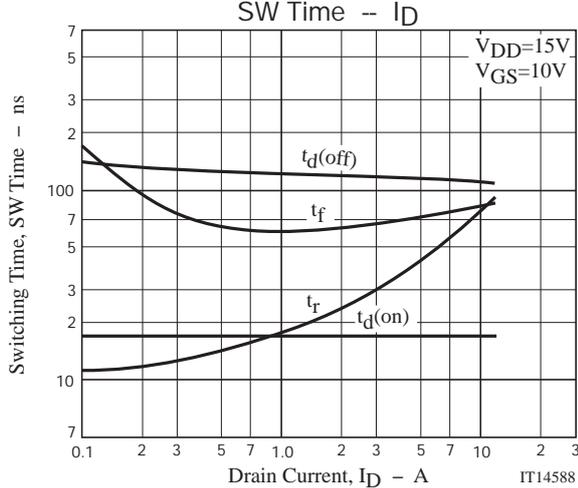
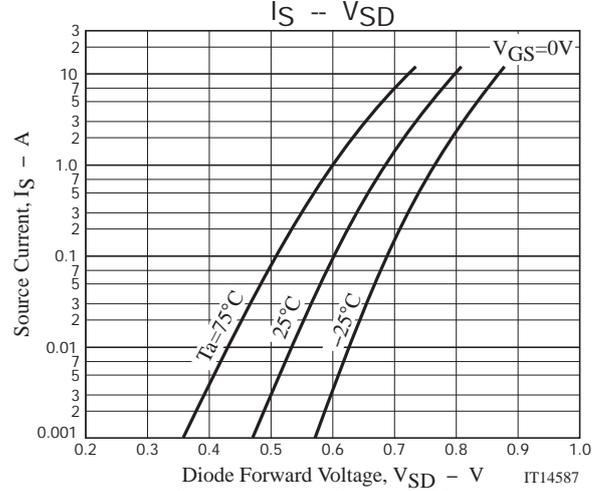
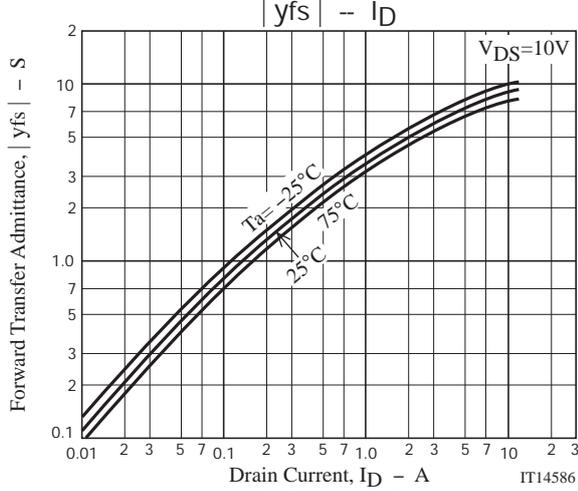
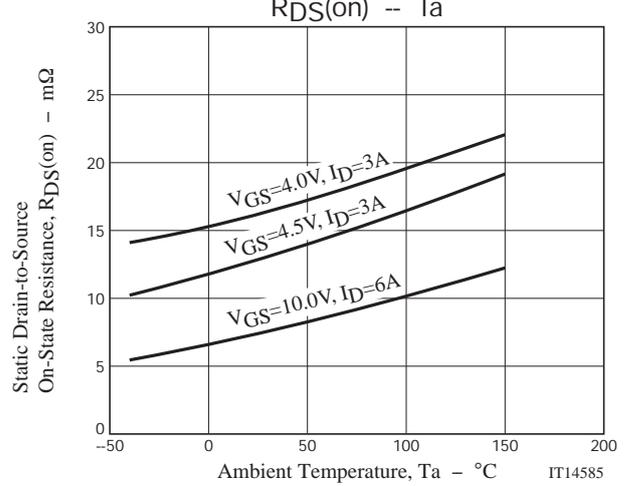
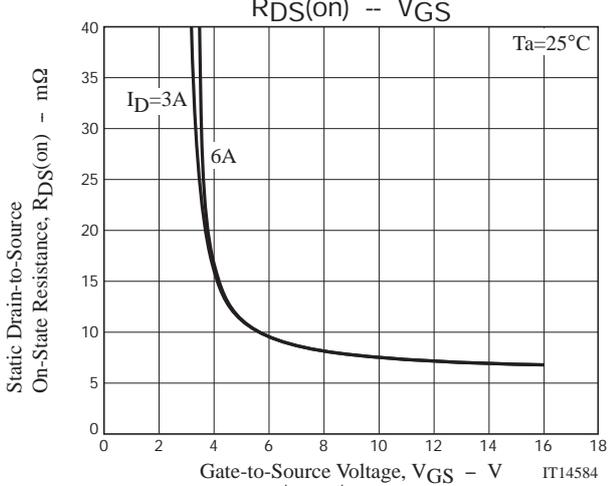
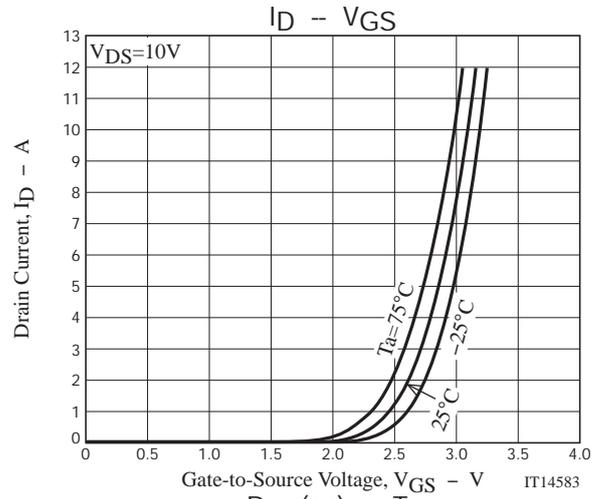
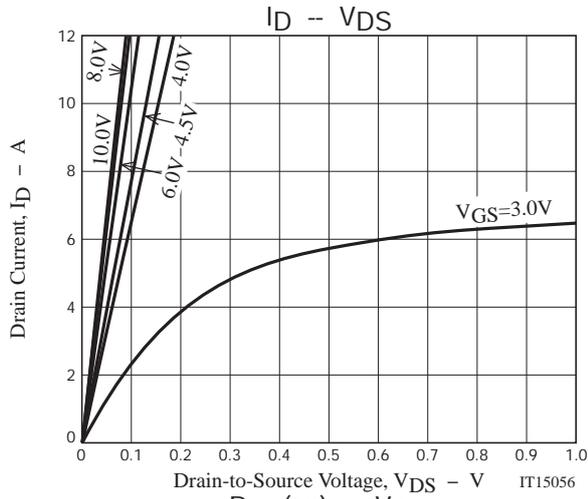
| Parameter | Symbol | Conditions | Ratings | | | Unit |
|--|---------------|--|-----------------------------|------|----------|------------------|
| | | | min. | typ. | max. | |
| Drain-to-Source Breakdown Voltage | $V_{(BR)DSS}$ | $I_D=1\text{mA}, V_{GS}=0\text{V}$ | 30 | | | V |
| Zero-Gate Voltage Drain Current | I_{DSS} | $V_{DS}=30\text{V}, V_{GS}=0\text{V}$ | | | 1 | μA |
| Gate-to-Source Leakage Current | I_{GSS} | $V_{GS}=\pm 16\text{V}, V_{DS}=0\text{V}$ | | | ± 10 | μA |
| Cutoff Voltage | $V_{GS(off)}$ | $V_{DS}=10\text{V}, I_D=1\text{mA}$ | 1.2 | | 2.6 | V |
| Forward Transfer Admittance | $ y_{fs} $ | $V_{DS}=10\text{V}, I_D=6\text{A}$ | | 7.5 | | S |
| Static Drain-to-Source On-State Resistance | $R_{DS(on)1}$ | $I_D=6\text{A}, V_{GS}=10\text{V}$ | | 7.5 | 10 | $\text{m}\Omega$ |
| | $R_{DS(on)2}$ | $I_D=3\text{A}, V_{GS}=4.5\text{V}$ | | 13 | 18.2 | $\text{m}\Omega$ |
| | $R_{DS(on)3}$ | $I_D=3\text{A}, V_{GS}=4\text{V}$ | | 15.5 | 22 | $\text{m}\Omega$ |
| Input Capacitance | C_{iss} | $V_{DS}=10\text{V}, f=1\text{MHz}$ | | 1700 | | pF |
| Output Capacitance | C_{oss} | | | 300 | | pF |
| Reverse Transfer Capacitance | C_{rss} | | | 200 | | pF |
| Turn-ON Delay Time | $t_{d(on)}$ | | See specified Test Circuit. | | 17 | |
| Rise Time | t_r | | | 50 | | ns |
| Turn-OFF Delay Time | $t_{d(off)}$ | | | 110 | | ns |
| Fall Time | t_f | | | 72 | | ns |
| Total Gate Charge | Q_g | $V_{DS}=15\text{V}, V_{GS}=10\text{V}, I_D=12\text{A}$ | | | 31 | |
| Gate-to-Source Charge | Q_{gs} | | | 5.5 | | nC |
| Gate-to-Drain "Miller" Charge | Q_{gd} | | | 5.5 | | nC |
| Diode Forward Voltage | V_{SD} | $I_S=12\text{A}, V_{GS}=0\text{V}$ | | 0.8 | 1.2 | V |

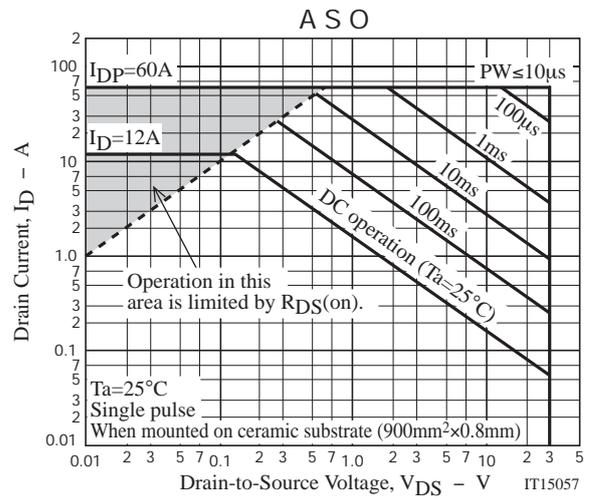
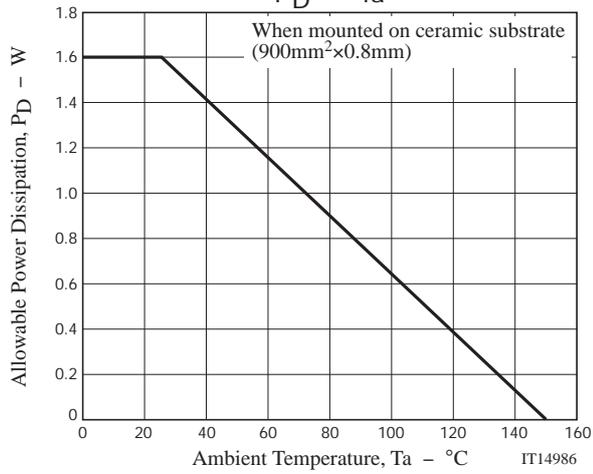
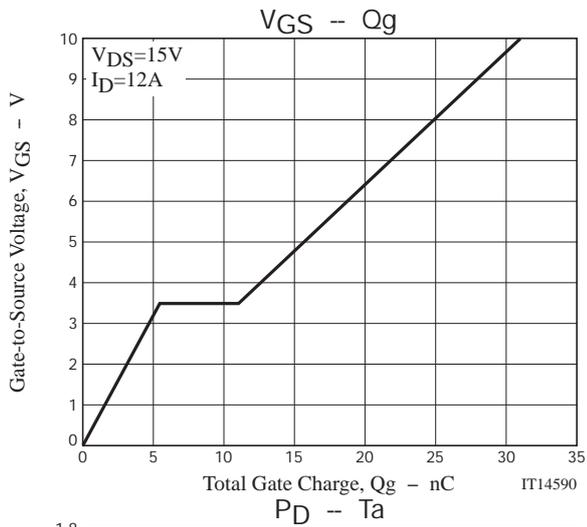
Switching Time Test Circuit



Ordering Information

| Device | Package | Shipping | memo |
|--------------|---------|----------------|--------------------------|
| ECH8410-TL-H | ECH8 | 3,000pcs./reel | Pb Free and Halogen Free |





Embossed Taping Specification

ECH8410-TL-H

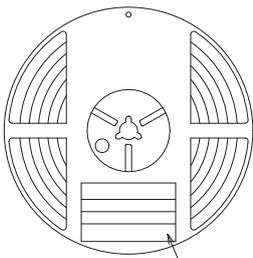
1. Packing Format

| Package Name | Carrier Tape Type | Maximum Number of devices contained (pcs) | | | Packing format | |
|--------------|-------------------|---|-----------|-----------|---|--|
| | | Reel | Inner box | Outer box | Inner BOX (C-1) | Outer BOX (A-7) |
| ECH8 | CPH6 | 3,000 | 15,000 | 90,000 | 5 reels contained Dimensions:mm (external) 183×72×185 | 6 inner boxes contained Dimensions:mm (external) 440×195×210 |

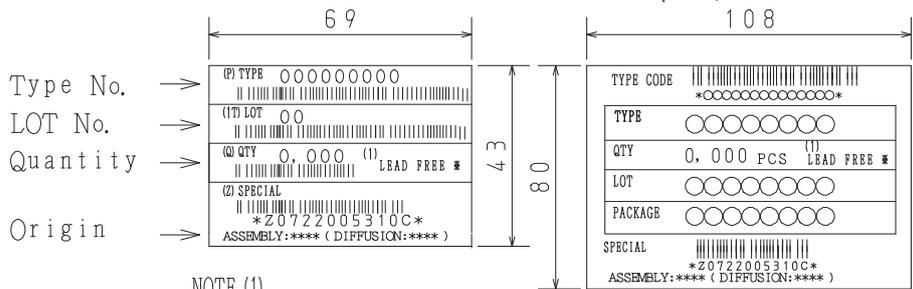
Reel label, Inner box label
(unit :mm)

Outer box label
It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

Packing method



Reel label



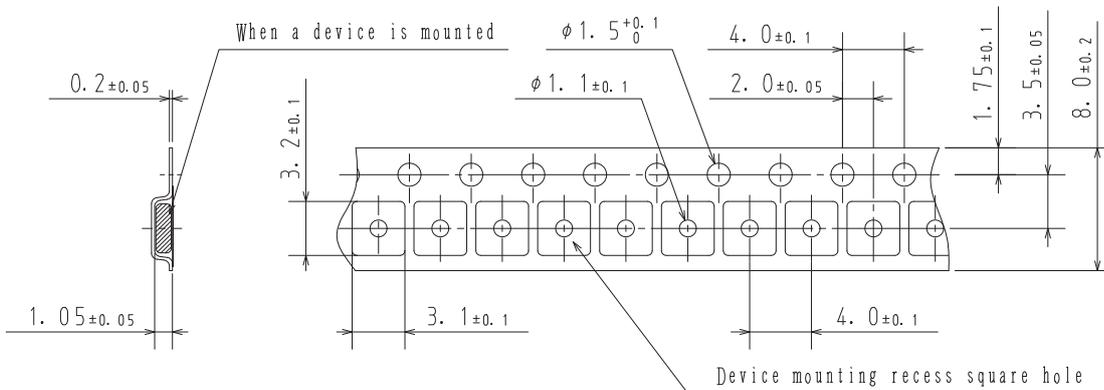
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

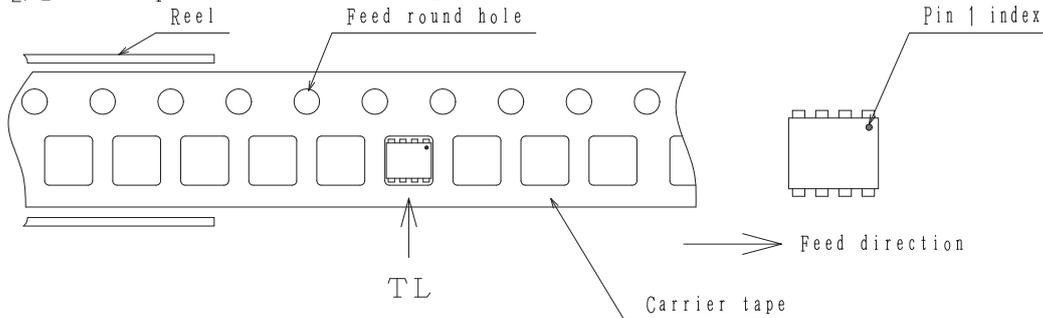
| Label | JEITA Phase |
|-------------|----------------|
| LEAD FREE 3 | JEITA Phase 3A |
| LEAD FREE 4 | JEITA Phase 3 |

2. Taping configuration

2-1. Carrier tape size (unit:mm)



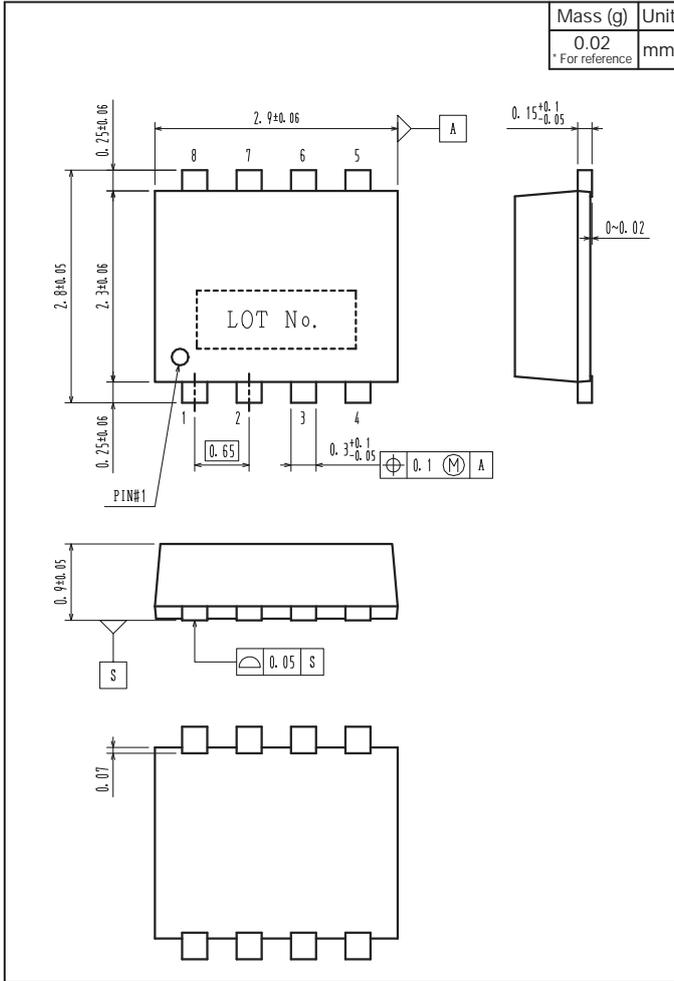
2-2. Device placement direction



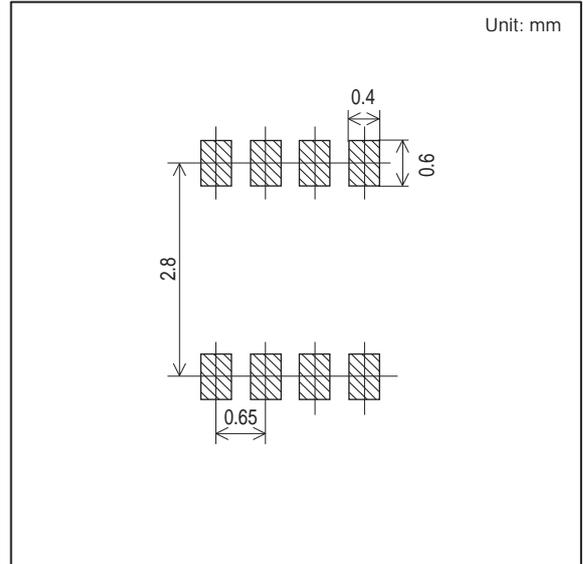
Those with pin 1 index on the feed hole side.....TL

ECH8410

Outline Drawing ECH8410-TL-H



Land Pattern Example



Note on usage : Since the ECH8410 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

- Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment. The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for new introduction or other application different from current conditions on the usage of automotive device, communication device, office equipment, industrial equipment etc. , please consult with us about usage condition (temperature, operation time etc.) prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.
- Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of May, 2012. Specifications and information herein are subject to change without notice.