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FDMS8672AS N-Channel PowerTrench[®] SyncFETTM **30V, 28A, 5.0m**Ω

Features

- Max $r_{DS(on)}$ = 5.0m Ω at V_{GS} = 10V, I_D = 18A
- Max r_{DS(on)} = 7.0mΩ at V_{GS} = 4.5V, I_D = 15A
- Advanced Package and Silicon combination for low $r_{\text{DS}(\text{on})}$ and high efficiency
- SyncFET Schottky Body Diode
- MSL1 robust package design
- RoHS Compliant



General Description

The FDMS8672AS has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest $r_{\text{DS(on)}}$ while maintaining excellent switching performance. This device has the added benefit of an efficient monolithic Schottky body diode.

Applications

- Synchronous Rectifier for DC/DC Converters
- Notebook Vcore/ GPU low side switch
- Networking Point of Load low side switch
- Telecom secondary side rectification



MOSFET Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			30	V	
V _{GS}	Gate to Source Voltage			±20	V	
ID	Drain Current -Continuous (Package limited)	T _C = 25°C		28		
	-Continuous (Silicon limited)	T _C = 25°C		99		
	-Continuous	T _A = 25°C	(Note 1a)	18	— A	
	-Pulsed			200		
E _{AS}	Single Pulse Avalanche Energy		(Note 2)	253	mJ	
P _D	Power Dissipation	T _C = 25°C		70		
	Power Dissipation T _A = 25°C (Note 1a		(Note 1a)	2.5	W	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.8	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1	a) 50	C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS8672AS	FDMS8672AS	Power 56	13"	12mm	3000units

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	icteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 1mA, V _{GS} = 0V	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 10mA, referenced to 25°C		27		mV/°C
IDSS	Zero Gate Voltage Drain Current	V _{DS} = 24V, V _{GS} = 0V			500	μΑ
I _{GSS}	Gate to Source Leakage Current	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
On Chara	cteristics					
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1mA$	1.0	1.9	3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 10mA, referenced to 25°C		-5		mV/°C
		V _{GS} = 10V, I _D = 18A		4.0	5.0	
r _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 4.5V, I _D = 15A		5.4	7.0	mΩ
		V _{GS} = 10V, I _D = 18A, T _J = 125°C		5.6	7.6	
9 _{FS}	Forward Transconductance	V _{DD} = 10V, I _D = 18A		85		S
-	Characteristics			4055	2000	5
C _{iss}	Input Capacitance	V _{DS} = 15V, V _{GS} = 0V,		1955	2600	pF
C _{oss}	Output Capacitance	f = 1MHz		1040	1385	pF
C _{rss}	Reverse Transfer Capacitance			125	190	pF
R _g	Gate Resistance	f = 1MHz		0.8		Ω
Switching	g Characteristics					_
t _{d(on)}	Turn-On Delay Time			12	22	ns
t _r	Rise Time	$-V_{DD} = 15V, I_D = 18A,$ $-V_{GS} = 10V, R_{GEN} = 6Ω$		4	10	ns
t _{d(off)}	Turn-Off Delay Time	$V_{\rm GS} = 100$, $N_{\rm GEN} = 0.22$		27	44	ns
t _f	Fall Time			3	10	ns
Qg	Total Gate Charge	$V_{GS} = 0V$ to 10V		28	40	nC
Qg	Total Gate Charge	$V_{GS} = 0V \text{ to } 4.5V$ $V_{DD} = 15V,$ $I_D = 18A$		15	21	nC
Q _{gs}	Gate to Source Charge	1 <u>D</u> = 10A		5.6		nC
Q _{gd}	Gate to Drain "Miller" Charge			3.4		nC
Drain-Sou	urce Diode Characteristics					
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0V, I _S =2A (Note 3)		0.4	0.7	V
t _{rr}	Reverse Recovery Time			32	52	ns
	Reverse Recovery Charge	— I _F = 18A, di/dt = 300A/μs		36	58	nC



a. 50°C/W when mounted on a 1 in² pad of 2 oz copper.

b. 125°C/W when mounted on a minimum pad of 2 oz copper.

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2. Starting T_J = 25, L = 3mH, I_{AS} = 13A, V_DD = 30V, V_{GS} = 10V.

3. Pulse Test: Pulse Width < $300\mu \text{s},$ Duty cycle < 2.0%.

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Typical Characteristics (continued)

SyncFET Schottky Body Diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMS8672AS.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

T_J = 125°C

T_{.1} = 100°C

TJ = 25°C

15

V_{DS}, REVERSE VOLTAGE (V)

20

25

30

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10

Figure 15. SyncFET Body Diode Reverse Leakage

vs Drain to Source Voltage

5



6

bss, REVERSE LEAKAGE CURRENT (mA)

10

1

0.1

0.01

0.001

0



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