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IFN860

Dual N-Channel Silicon Junction Field-Effect Transistor

Low-Noise Audio Amplifier

• Equivalent to Crystalonics CD860

Absolute maximum ratings at $T_A = 25 \,^{\circ}C$ Reverse Gate Source & Reverse Gate Drain Voltage

Continuous Forward Gate Current

Continuous Device Power Dissipation

Power Derating

Storage Temperature Range

-20 V

50 mA

400 mW

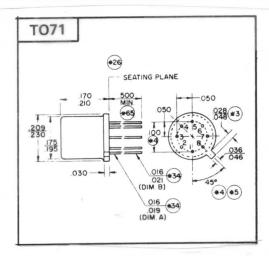
2.3 mW/°C

- 65°C to 200°C

At 25°C free air temperature: Static Electrical Characteristics		IFN860			Process NJ450L		
		Min	Тур	Max	Unit	Test Conditions	
Gate Source Breakdown Voltage	V _{(BR)GSS}	- 20			V	I _G = -1 μA, V _{DS} = ØV	
Gate Reverse Leakage Voltage	IGSS			3	nA	$V_{GS} = -10 \text{ V}, V_{DS} = \emptyset \text{ V}$	
Gate Source Cutoff Voltage	V _{GS(OFF)}	- 0.3		- 3	V	V _{DS} = 10 V, I _D = 100 μA	
Drain Saturation Current (Pulsed)	DSS	10			mA	V _{DS} = 10 V, V _{GS} = Ø V	
Differential Gate Source Voltage	V _{GS1} -V _{GS2}			25	mV	V _{DS} = 10 V, I _D = 100 μA	

Dynamic Electrical Characteristics

Transconductance	g _m	25	40		mS	$V_{DS} = 10 \text{V}, I_{D} = -10 \text{mA}$	f = 1 kHz
Common Source Input Capacitance	C _{iss}		30	35	pF	$V_{DS} = 10 \text{ V}, I_{D} = -10 \text{ mA}$	f = 1 MHz
Common Source Reverse Transfer Capacitance	C _{rss}		17	20	pF	$V_{DS} = 10 \text{ V}, I_{D} = -10 \text{ mA}$	f = 1 MHz
Equivalent Short Circuit Input Noise Voltage	ë _N			2	nV/√Hz	V _{DG} = 3 V, I _D = 10 mA	f = 1 kHz



NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors