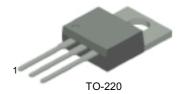


KSE13006/13007

High Voltage Switch Mode Application High Speed Switching

- Suitable for Switching Regulator and Motor Control



1.Base 2.Collector 3.Emitter

NPN Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	: KSE13006	600	V
		: KSE13007	700	V
V _{CEO}	Collector-Emitter Voltage	: KSE13006	300	V
		: KSE13007	400	V
V _{EBO}	Emitter- Base Voltage		9	V
I _C	Collector Current (DC)		8	Α
I _{CP}	Collector Current (Pulse)		16	Α
I _B	Base Current		4	Α
P _C	Collector Dissipation (T _C =25°C)		80	W
T _J	Junction Temperature		150	°C
T _{STG}	Storage Temperature		- 65 ~ 150	°C

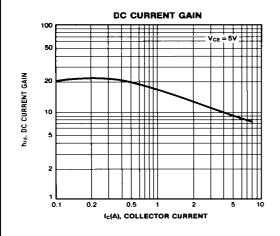
Electrical Characteristics $\rm T_C = 25^{\circ}C$ unless otherwise noted

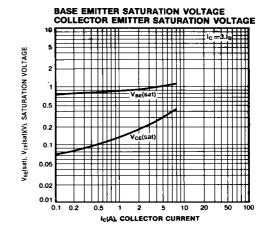
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CEO}	Collector- Emitter Breakdown Voltage : KSE13006 : KSE13007	I _C = 10mA, I _B = 0	300 400			V V
I _{EBO}	Emitter Cut-off Current	V _{EB} = 9V, I _C = 0			1	mA
h _{FE}	*DC Current Gain	$V_{CE} = 5V, I_{C} = 2A$ $V_{CE} = 5V, I_{C} = 5A$	8 5		60 30	
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_C = 2A, I_B = 0.4A$ $I_C = 5A, I_B = 1A$ $I_C = 8A, I_B = 2A$			1 2 3	V V V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	I _C = 2A, I _B = 0.4A I _C = 5A, I _B = 1A			1.2 1.6	V V
C _{ob}	Output Capacitance	V _{CB} = 10V, f = 0.1MHz		110		pF
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 0.5A	4			MHz
t _{ON}	Turn ON Time	V _{CC} = 125V, I _C = 5A			1.6	μs
t _S	Storage Time	$I_{B1} = -I_{B2} = 1A$			3	μs
t _F	Fall Time	$R_L = 50\Omega$			0.7	μs

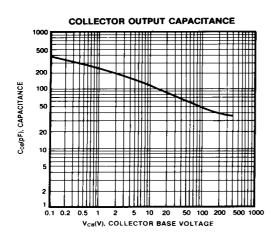
^{*} Pulse test: PW≤300µs, Duty cycle≤2%

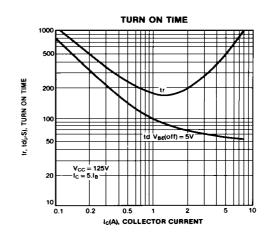
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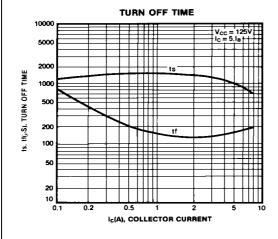
Typical Characteristics

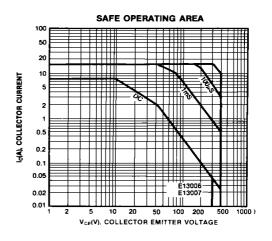






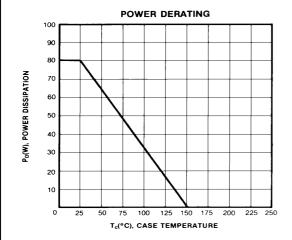






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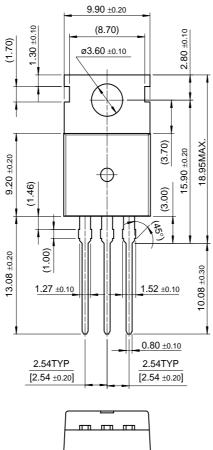
Typical Characteristics (continued)

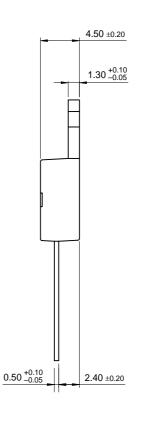


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Package Dimensions

TO-220





 10.00 ± 0.20

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