

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

MCH6604 — General-Purpose Switching Device Applications

Features

- · Low ON-resistance
- · Ultrahigh-speed switching
- · 1.5V drive
- · Composite type with 2 MOSFETs contained in a single package, facilitating high-density mounting

Specifications

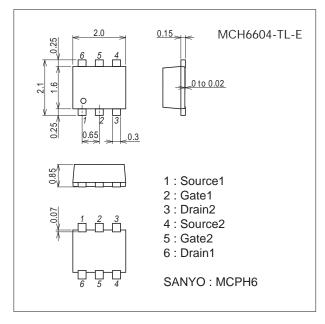
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		50	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	ID		0.25	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	1	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² ×0.8mm)1unit	0.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

This product is designed to "ESD immunity $< 200 V^*$ ", so please take care when handling.

Package Dimensions

unit : mm (typ) 7022A-006



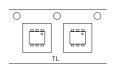
Product & Package Information

• Package : MCPH6

• JEITA, JEDEC : SC-88, SC-70-6, SOT-363

• Minimum Packing Quantity : 3,000 pcs./reel

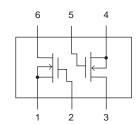
Packing Type: TL



Marking



Electrical Connection

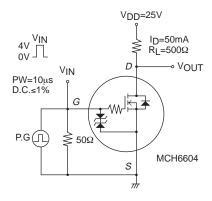


^{*} Machine Model

Electrical Characteristics at Ta=25°C

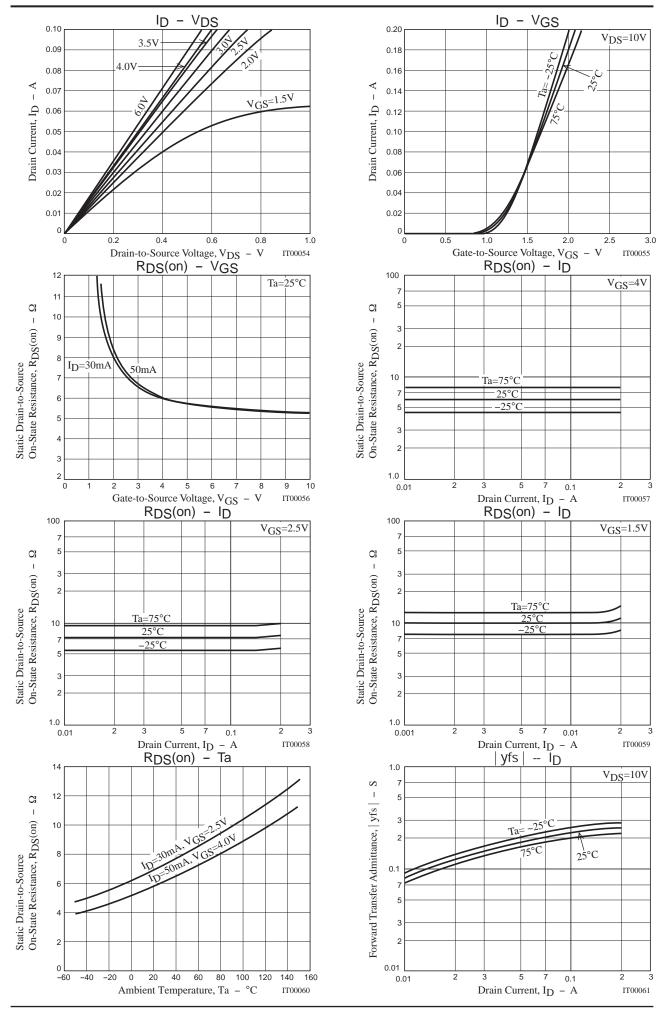
Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	50			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =50V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =100μA	0.4		1.3	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =50mA	130	180		mS	
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =50mA, V _{GS} =4V		6	7.8	Ω	
	R _{DS} (on)2	I _D =30mA, V _{GS} =2.5V		7.1	9.9	Ω	
	R _{DS} (on)3	I _D =10mA, V _{GS} =1.5V		10	20	Ω	
Input Capacitance	Ciss			6.6		pF	
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		4.7		pF	
Reverse Transfer Capacitance	Crss			1.7		pF	
Turn-ON Delay Time	t _d (on)			18		ns	
Rise Time	t _r	Considered Total Circuit		42		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		190		ns	
Fall Time	tf			105		ns	
Total Gate Charge	Qg			1.57		nC	
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =100mA		0.20		nC	
Gate-to-Drain "Miller" Charge	Qgd			0.32		nC	
Diode Forward Voltage	V _{SD}	I _S =100mA, V _{GS} =0V		0.85	1.2	V	

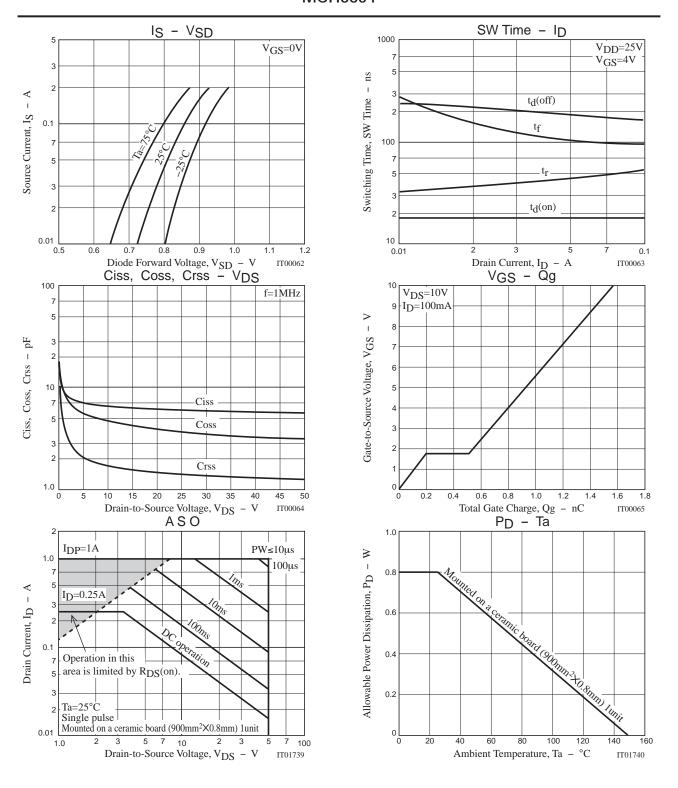
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo	
MCH6604-TL-E	H6604-TL-E MCPH6		Pb Free	



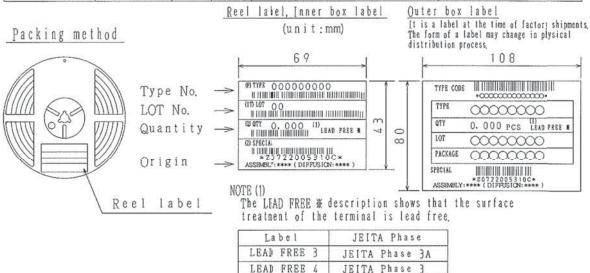


Taping Specification

MCH6604-TL-E

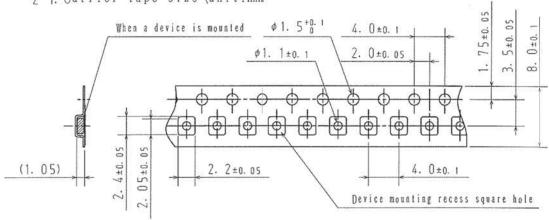
1. Packing Format

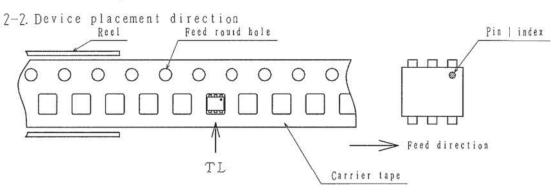
Package Name Carrier Tape Type	Carrier Tape	Maximum Number of devices contained (yes)			Packing format		
	Reel	[nner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)		
мсрн6	MCP4	3, 000	15, 000	90, 000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm(external) 440×195×210	



2. Taping configuration

2-1. Carrier tape size (unit:mm)

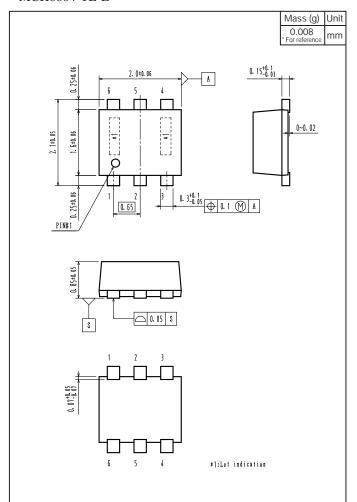




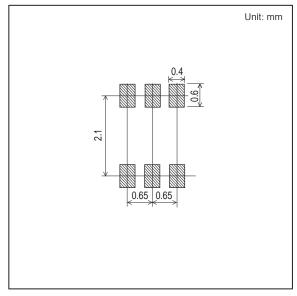
Those with pin 1 index on the feed hole side ·····TL

Outline Drawing

MCH6604-TL-E



Land Pattern Example



Note on usage: Since the MCH6604 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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