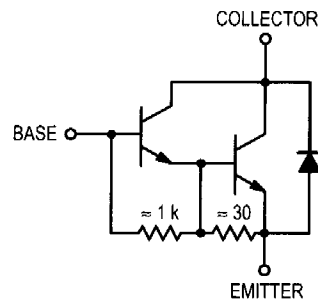


## NPN Silicon Power Darlington Transistor

The MJ10012 and MJH10012 are high-voltage, high-current Darlington transistors designed for automotive ignition, switching regulator and motor control applications.

- Collector-Emitter Sustaining Voltage —  
 $V_{CEO(sus)} = 400 \text{ Vdc (Min)}$
- 175 Watts Capability at 50 Volts
- Automotive Functional Tests



### MAXIMUM RATINGS

Rating	Symbol	MJ10012	MJH10012	Unit
Collector-Emitter Voltage	$V_{CEO}$	400		Vdc
Collector-Emitter Voltage ( $R_{BE} = 27 \Omega$ )	$V_{CER}$	550		Vdc
Collector-Base Voltage	$V_{CBO}$	600		Vdc
Emitter-Base Voltage	$V_{EBO}$	8.0		Vdc
Collector Current — Continuous	$I_C$	10		Adc
— Peak (1)		15		
Base Current	$I_B$	2.0		Adc
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	$P_D$	175	118	Watts
@ $T_C = 100^\circ\text{C}$		100	47.5	Watts
Derate above $25^\circ\text{C}$		1.0	1.05	W/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-65 to +200	-55 to +150	$^\circ\text{C}$

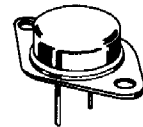
### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max		Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.0	0.95	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	$T_L$	275	275	$^\circ\text{C}$

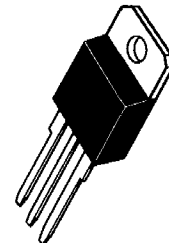
(1) Pulse Test: Pulse Width = 5.0 ms, Duty Cycle  $\leq$  10%.

**MJ10012**  
**MJH10012**

10 AMPERE  
POWER TRANSISTORS  
DARLINGTON NPN  
SILICON  
400 VOLTS  
175 AND 118 WATTS



(TO-3)  
MJ10012



TO-218 TYPE  
MJH10012



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**Quality Semi-Conductors**

# MJ10012 MJH10012

## ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS (1)</b>					
Collector-Emitter Sustaining Voltage (Figure 1) ( $I_C = 200\text{ mAdc}$ , $I_B = 0$ , $V_{\text{clamp}} = \text{Rated } V_{\text{CEO}}$ )	$V_{\text{CEO(sus)}}$	400	—	—	Vdc
Collector-Emitter Sustaining Voltage (Figure 1) ( $I_C = 200\text{ mAdc}$ , $R_{\text{BE}} = 27\text{ Ohms}$ , $V_{\text{clamp}} = \text{Rated } V_{\text{CER}}$ )	$V_{\text{CER(sus)}}$	425	—	—	Vdc
Collector Cutoff Current (Rated $V_{\text{CER}}$ , $R_{\text{BE}} = 27\text{ Ohms}$ )	$I_{\text{CER}}$	—	—	1.0	mAdc
Collector Cutoff Current (Rated $V_{\text{CBO}}$ , $I_E = 0$ )	$I_{\text{CBO}}$	—	—	1.0	mAdc
Emitter Cutoff Current ( $V_{\text{EB}} = 6.0\text{ Vdc}$ , $I_C = 0$ )	$I_{\text{EBO}}$	—	—	40	mAdc

## ON CHARACTERISTICS (1)

DC Current Gain ( $I_C = 3.0\text{ Adc}$ , $V_{\text{CE}} = 6.0\text{ Vdc}$ ) ( $I_C = 6.0\text{ Adc}$ , $V_{\text{CE}} = 6.0\text{ Vdc}$ ) ( $I_C = 10\text{ Adc}$ , $V_{\text{CE}} = 6.0\text{ Vdc}$ )	$h_{\text{FE}}$	300 100 20	550 350 150	— 2000 —	—
Collector-Emitter Saturation Voltage ( $I_C = 3.0\text{ Adc}$ , $I_B = 0.6\text{ Adc}$ ) ( $I_C = 6.0\text{ Adc}$ , $I_B = 0.6\text{ Adc}$ ) ( $I_C = 10\text{ Adc}$ , $I_B = 2.0\text{ Adc}$ )	$V_{\text{CE(sat)}}$	— — —	— — —	1.5 2.0 2.5	Vdc
Base Emitter Saturation Voltage ( $I_C = 6.0\text{ Adc}$ , $I_B = 0.6\text{ Adc}$ ) ( $I_C = 10\text{ Adc}$ , $I_B = 2.0\text{ Adc}$ )	$V_{\text{BE(sat)}}$	— —	— —	2.5 3.0	Vdc
Base Emitter On Voltage ( $I_C = 10\text{ Adc}$ , $V_{\text{CE}} = 6.0\text{ Vdc}$ )	$V_{\text{BE(on)}}$	—	—	2.8	Vdc
Diode Forward Voltage ( $I_F = 10\text{ Adc}$ )	$V_f$	—	2.0	3.5	Vdc

## DYNAMIC CHARACTERISTICS

Output Capacitance ( $V_{\text{CB}} = 10\text{ Vdc}$ , $I_E = 0$ , $f_{\text{test}} = 100\text{ kHz}$ )	$C_{\text{ob}}$	—	165	350	pF
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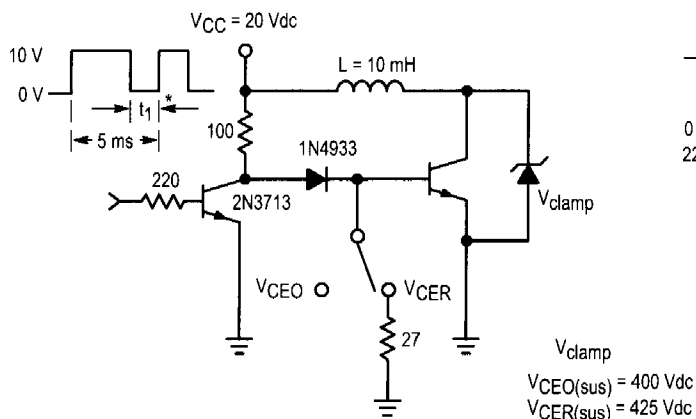
## SWITCHING CHARACTERISTICS

Storage Time	$(V_{\text{CC}} = 12\text{ Vdc}$ , $I_C = 6.0\text{ Adc}$ , $I_{\text{B1}} = I_{\text{B2}} = 0.3\text{ Adc}$ ) Figure 2	$t_s$	—	7.5	15	$\mu\text{s}$
Fall Time		$t_f$	—	5.2	15	$\mu\text{s}$

## FUNCTIONAL TESTS

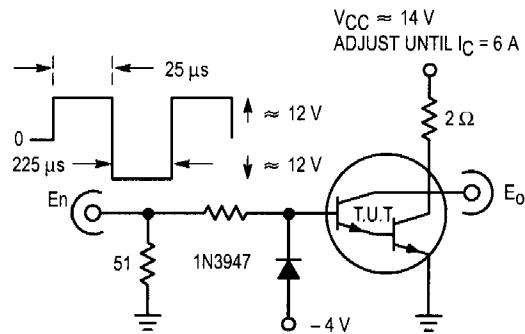
Second Breakdown Collector Current with Base-Forward Biased	$I_{\text{S/B}}$	See Figure 10			—
Pulsed Energy Test (See Figure 12)	$I_{\text{C2L/2}}$	—	—	180	mJ

(1) Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle = 2%.



\* Adjust  $t_1$  such that  $I_C$  reaches 200 mA at  $V_{\text{CE}} = V_{\text{clamp}}$

**Figure 1. Sustaining Voltage Test Circuit**



**Figure 2. Switching Times Test Circuit**