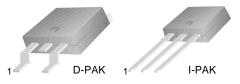


MJD41C

General Purpose Amplifier Low Speed Switching Applications D-PAK for Surface Mount Applications

- Load Formed for Surface Mount Application (No Suffix)
- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular TIP41 and TIP41C



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	100	V
V _{CEO}	Collector-Emitter Voltage	100	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current (DC)	6	Α
I _{CP}	Collector Current (Pulse)	10	Α
I _B	Base Current	2	Α
P _C	Collector Dissipation (T _C =25°C)	20	W
	Collector Dissipation (T _a =25°C)	1.75	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage	$I_C = 30 \text{mA}, I_B = 0$	100		V
I _{CEO}	Collector Cut-off Current	$V_{CE} = 60V, I_{B} = 0$		50	μΑ
I _{CES}	Collector Cut-off Current	V _{CE} = 100V, V _{BE} = 0		10	uA
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 5V, I_{C} = 0$		0.5	mA
h _{FE}	* DC Current Gain	$V_{CE} = 4V, I_{C} = 0.3A$	30		
		$V_{CE} = 4V, I_{C} = 3A$	15	75	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = 6A, I _B = 600mA		1.5	V
V _{BE} (on)	* Base-Emitter ON Voltage	$V_{CE} = 6A, I_{C} = 4A$		2	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 500mA$	3		MHz

^{*} Pulse Test: PW≤300μs, Duty Cycle≤2%

Typical Characteristics

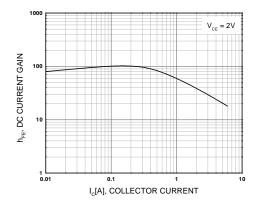


Figure 1. DC current Gain

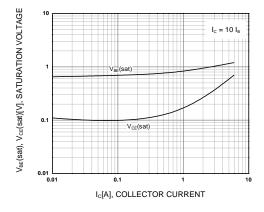


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

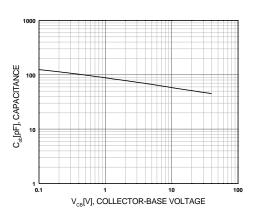


Figure 3. Collector Capacitance

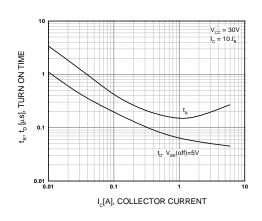


Figure 4. Turn On Time

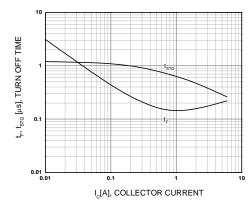


Figure 5. Turn Off Time

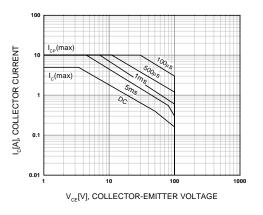


Figure 6. Safe Operating Area

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Typical Characteristics (Continued)

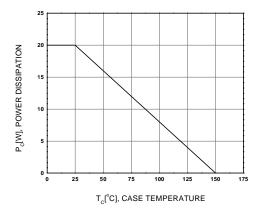
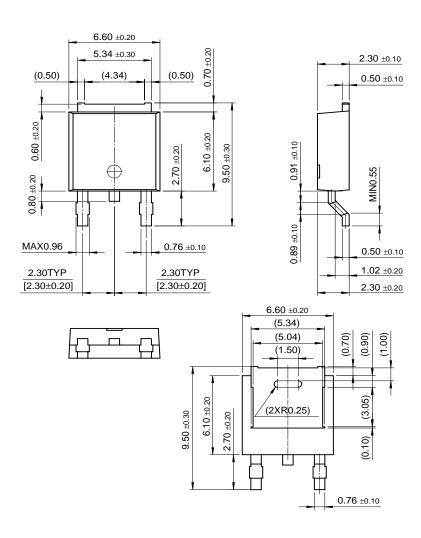


Figure 7. Power Derating

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Package Demensions

D-PAK



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