

March 2009

MJD44H11 NPN Epitaxial Silicon Transistor

- General Purpose Power and Switching Such as Output or Driver Stages in Applications
- D-PAK for Surface Mount Applications
- Load Formed for Surface Mount Application (No Suffix)
- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular MJE44H
- Fast Switching Speeds
- Low Collector Emitter Saturation Voltage



Absolute Maximum Ratings $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CEO}	Collector-Emitter Voltage	80	V	
V _{EBO}	Emitter-Base Voltage 5			
I _C	Collector Current (DC)	8		
I _{CP}	Collector-Current (Pulse)	16	А	
T _J	Junction Temperature	150 °C		
T _{STG}	Storage Temperature	- 65 ~ 150	°C	

Thermal Characteristics T_a = 25°C unless otherwise noted

Symbol	Parameter		Max.	Units	
P _D	Total Device Dissipation $ T_{c} = 25^{\circ}\text{C} $ $ T_{a} = 25^{\circ}\text{C} $	T _c = 25°C	20	W	
		$T_a = 25$ °C	1.75		
$R_{\theta JC}$	Thermal Resistance, Junction to Case		6.25	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		71.4	°C/W	

Electrical Characteristics $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage	I _C = 30mA, I _B = 0	80			V
I _{CEO}	Collector Cut-off Current	$V_{CE} = 80V, I_{B} = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 5V, I_{C} = 0$			50	μΑ
h _{FE}	*DC Current Gain	$V_{CE} = 1V$, $I_C = 2A$ $V_{CE} = 1V$, $I_C = 4A$	60 40			
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_C = 8A, I_B = 0.4A$			1	V
V _{BE} (on)	*Base-Emitter On Voltage	$I_C = 8A, I_B = 0.8A$			1.5	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 0.5A$		50		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, f = 1MHz		130		pF
t _{ON}	Turn On Time			300		ns
t _{STG}	Storage Time	$I_C = 5A$ $I_{B1} = -I_{B2} = 0.5A$		500		ns
t _F	Fall Time	D1 D2		140		ns

^{*} Pulse Test: PW≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

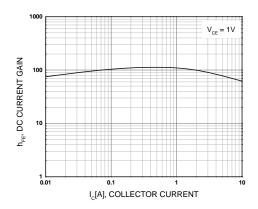


Figure 1. DC current Gain

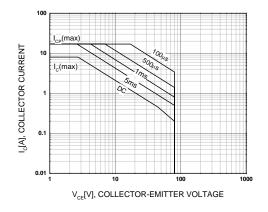


Figure 2. Safe Operating Area

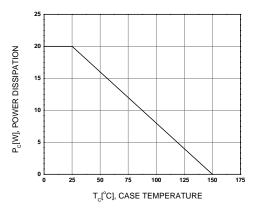
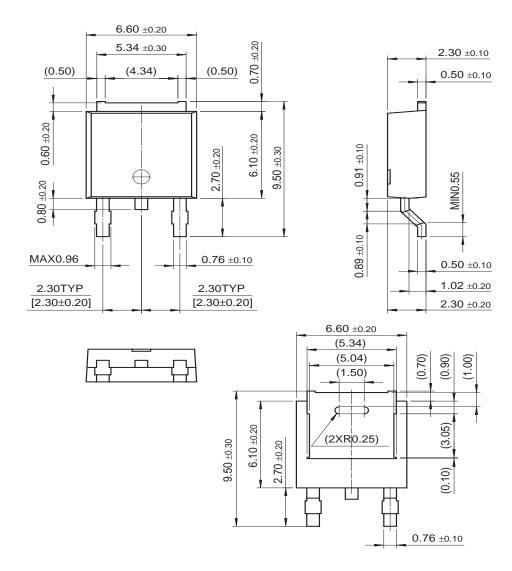


Figure 3. Power Derating

Mechanical Dimensions

D-PAK



Dimensions in Millimeters





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Definition of Terms				
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